



LARGE SYNOPTIC SURVEY TELESCOPE

Large Synoptic Survey Telescope (LSST)
Data Management

DM Verification Control Document

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LDM-692

Latest Revision: 2019-07-30

This LSST document has been approved as a Content-Controlled Document by the LSST DM Change Control Board. If this document is changed or superseded, the new document will retain the Handle designation shown above. The control is on the most recent digital document with this Handle in the LSST digital archive and not printed versions. Additional information may be found in the corresponding DM RFC.

Abstract

The DM Verification Control Document is an extract from the LSST-wide systems engineering verification database (based on Jira) which provides an overview of the verification status of the Data Management Subsystem with respect to its requirements.



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Contents

1 Introduction	1
1.1 Applicable Documents	1
2 Summary Information	2
3 Verification Control	3
A References	80
B Acronyms	82

DM Verification Control Document

1 Introduction

The requirements on the LSST Data Management System are described in LSE-61 and in the various lower-level requirements documents which are derived from it (e.g. LDM-554, LDM-555, LDM-636, etc). These requirements are verified by running tests, and recording the results of those tests in the LSST Jira system. The general approach to testing and verification in DM is described in more detail in LDM-503; note, in particular, the concepts and vocabulary described in LDM-503 §2.3.2.

This Verification Control Document (VCD) provides a summary of the status of requirements verification: which requirements are covered by which tests, and how many of those tests have been executed successfully. Section 2 presents a brief overview; the remainder of the document provides a matrix displaying all of the DM requirements, their corresponding verification elements, the applicable tests, and whether those tests have been executed.

The matrix is fully linked: any given test case mentioned in the matrix such as, LWV-T362, provides a link to the Jira element defining that test case. Similarly, relevant documentation such as test reports like DMTR-111 are linked to the bibliography and thence to the report itself¹.

1.1 Applicable Documents

LSE-61	LSST DM Subsystem Requirements
LDM-503	LSST DM Test Plan
LDM-639	DM Acceptance Test Specification

¹As such, you may find it useful to remember the appropriate keyboard shortcuts — often ⌘+[(macOS) or ctrl+← (Windows/Linux) — to return to your previous position.

2 Summary Information

The following table provides an overview of the relationships between requirements, verification elements, and test cases.

	Count of Requirements	Count of Verification Elements
Not related to any Test Case	260	536
Related to NOT executed Test Cases	352	384
Related to Failed Test Cases	2	2
Related to Passed Test Cases	79	80
Total	693	1002

The following table provides the Test Cases result summary.

Test Cases Results	
Not Executed	436
Conditional Pass	7
Fail	1
Pass	28
Total	472

Note that test cases may be related to multiple requirements or verification elements, and requirements or verification elements may be related to multiple test cases.

3 Verification Control

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0002 LSE-61	DMS-REQ-0002-V-01 LVW-3	LVV-T101 LDM-639		Not Executed
		LVV-T217 LDM-533	2018-07-04 DMTR-91 LVV-P1	Conditional Pass
DMS-REQ-0004 LSE-61	DMS-REQ-0004-V-01 LVW-4	LVV-T35 LDM-639		Not Executed
	DMS-REQ-0004-V-02 LVW-9740			
	DMS-REQ-0004-V-03 LVW-9803	LVV-T102 LDM-639		Not Executed
DMS-REQ-0008 LSE-61	DMS-REQ-0008-V-01 LVW-5	LVV-T171 LDM-639		Not Executed
		LVV-T287 LDM-538		Not Executed
DMS-REQ-0009 LSE-61	DMS-REQ-0009-V-01 LVW-6	LVV-T125 LDM-639		Not Executed
DMS-REQ-0010 LSE-61	DMS-REQ-0010-V-01 LVW-7	LVV-T18 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T20 LDM-533	2019-05-22 DMTR-53 LVV-P44	Failed
		LVV-T36 LDM-639		Not Executed
DMS-REQ-0018 LSE-61	DMS-REQ-0018-V-01 LVW-8	LVV-T29 LDM-639		Not Executed
		LVV-T283 LDM-538	2019-05-22 DMTR-61 LVV-P45	Passed
		LVV-T284 LDM-538	2019-06-24 DMTR-102 LVV-P10	Passed
DMS-REQ-0020 LSE-61	DMS-REQ-0020-V-01 LVW-9	LVV-T30 LDM-639		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T283 LDM-538	2019-05-22 DMTR-61 LVV-P45	Passed
		LVV-T284 LDM-538	2019-06-24 DMTR-102 LVV-P10	Passed
DMS-REQ-0022 LSE-61	DMS-REQ-0022-V-01 LVV-10	LVV-T31 LDM-639		Not Executed
DMS-REQ-0024 LSE-61	DMS-REQ-0024-V-01 LVV-11	LVV-T32 LDM-639		Not Executed
		LVV-T283 LDM-538	2019-05-22 DMTR-61 LVV-P45	Passed
		LVV-T284 LDM-538	2019-06-24 DMTR-102 LVV-P10	Passed
DMS-REQ-0029 LSE-61	DMS-REQ-0029-V-01 LVV-12	LVV-T15 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
		LVV-T19 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T39 LDM-639		Not Executed
DMS-REQ-0030 LSE-61	DMS-REQ-0030-V-01 LVV-13	LVV-T15 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
		LVV-T19 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T40 LDM-639		Not Executed
	DMS-REQ-0030-V-02 LVV-9741	LVV-T1240 LDM-639		Not Executed
DMS-REQ-0032 LSE-61	DMS-REQ-0032-V-01 LVV-14	LVV-T126 LDM-639		Not Executed
DMS-REQ-0033 LSE-61	DMS-REQ-0033-V-01 LVV-15	LVV-T127 LDM-639		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T362	2019-03-31 DMTR-111 LVV-P15	Passed
DMS-REQ-0034 LSE-61	DMS-REQ-0034-V-01 LVW-16	LVV-T61 LDM-639		Not Executed
DMS-REQ-0042 LSE-61	DMS-REQ-0042-V-01 LVW-17	LVV-T128 LDM-639		Not Executed
DMS-REQ-0043 LSE-61	DMS-REQ-0043-V-01 LVW-18	LVV-T21 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T22 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T129 LDM-639		Not Executed
DMS-REQ-0046 LSE-61	DMS-REQ-0046-V-01 LVW-19	LVV-T68 LDM-639		Not Executed
DMS-REQ-0047 LSE-61	DMS-REQ-0047-V-01 LVW-20	LVV-T16 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
		LVV-T62 LDM-639		Not Executed
DMS-REQ-0052 LSE-61	DMS-REQ-0052-V-01 LVW-21	LVV-T130 LDM-639		Not Executed
DMS-REQ-0059 LSE-61	DMS-REQ-0059-V-01 LVW-22	LVV-T83 LDM-639		Not Executed
DMS-REQ-0060 LSE-61	DMS-REQ-0060-V-01 LVW-23	LVV-T84 LDM-639		Not Executed
		LVV-T368	2018-12-06 DMTR-112 LVV-P16	Passed
DMS-REQ-0061 LSE-61	DMS-REQ-0061-V-01 LVW-24	LVV-T85 LDM-639		Not Executed
DMS-REQ-0062 LSE-61	DMS-REQ-0062-V-01 LVW-25	LVV-T86 LDM-639		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0063 LSE-61	DMS-REQ-0063-V-01 LVV-26	LVV-T87 LDM-639		Not Executed
DMS-REQ-0065 LSE-61	DMS-REQ-0065-V-01 LVV-27	Verified in: LVV-T134 LDM-639	DMS-API-REQ-0028-V-01(LVV-10004) DMS-API-REQ-0016-V-01(LVV-10016) DMS-API-REQ-0017-V-01(LVV-10018) DMS-API-REQ-0018-V-01(LVV-10017)	Not Executed
DMS-REQ-0068 LSE-61	DMS-REQ-0068-V-01 LVV-28	LVV-T33 LDM-639		Not Executed
		LVV-T283 LDM-538	2019-05-22 DMTR-61 LVV-P45	Passed
		LVV-T284 LDM-538	2019-06-24 DMTR-102 LVV-P10	Passed
		LVV-T286 LDM-538	2019-05-22 DMTR-61 LVV-P45	Passed
DMS-REQ-0069 LSE-61	DMS-REQ-0069-V-01 LVV-29	LVV-T15 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
		LVV-T18 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T19 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T38 LDM-639		Not Executed
		LVV-T362 LDM-639	2019-03-31 DMTR-111 LVV-P15	Passed
DMS-REQ-0070 LSE-61	DMS-REQ-0070-V-01 LVV-30	LVV-T15 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
		LVV-T19 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T41 LDM-639		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0072 LSE-61	DMS-REQ-0072-V-01 LVV-31	LVV-T15	2019-05-22	Passed
		LDM-534	DMTR-51 LVV-P43	
		LVV-T19	2019-05-22	Passed
		LDM-533	DMTR-53 LVV-P44	
		LVV-T42		Not Executed
		LDM-639		
DMS-REQ-0074 LSE-61	DMS-REQ-0074-V-01 LVV-32	LVV-T20	2019-05-22	Failed
		LDM-533	DMTR-53 LVV-P44	
		LVV-T37		Not Executed
		LDM-639		
DMS-REQ-0075 LSE-61	DMS-REQ-0075-V-01 LVV-33	LVV-T149		Not Executed
		LDM-639		
		LVV-T1085	2019-07-08	Passed
		LDM-552	DMTR-71 LVV-P46	
		LVV-T1086	2019-07-08	Passed
		LDM-552	DMTR-71 LVV-P46	
		LVV-T1087	2019-07-08	Passed
		LDM-552	DMTR-71 LVV-P46	
DMS-REQ-0077 LSE-61	DMS-REQ-0077-V-01 LVV-34	LVV-T150		Not Executed
		LDM-639		
DMS-REQ-0078 LSE-61	DMS-REQ-0078-V-01 LVV-35	LVV-T151		Not Executed
		LDM-639		
		LVV-T1232		Not Executed
		LDM-639		
DMS-REQ-0089 LSE-61	DMS-REQ-0089-V-01 LVV-36	LVV-T102		Not Executed
		LDM-639		
DMS-REQ-0094 LSE-61	DMS-REQ-0094-V-01 LVV-37	LVV-T152		Not Executed
		LDM-639		
DMS-REQ-0096 LSE-61	DMS-REQ-0096-V-01 LVV-38	LVV-T103		Not Executed
		LDM-639		

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0097 LSE-61	DMS-REQ-0097-V-01 LVV-39	LVV-T45 LDM-639		Not Executed
DMS-REQ-0098 LSE-61	DMS-REQ-0098-V-01 LVV-40	LVV-T104 LDM-639		Not Executed
DMS-REQ-0099 LSE-61	DMS-REQ-0099-V-01 LVV-41	LVV-T46 LDM-639		Not Executed
DMS-REQ-0100 LSE-61	DMS-REQ-0100-V-01 LVV-42	LVV-T105 LDM-639		Not Executed
DMS-REQ-0101 LSE-61	DMS-REQ-0101-V-01 LVV-43	LVV-T47 LDM-639		Not Executed
DMS-REQ-0102 LSE-61	DMS-REQ-0102-V-01 LVV-44	LVV-T153 LDM-639		Not Executed
DMS-REQ-0106 LSE-61	DMS-REQ-0106-V-01 LVV-46	LVV-T11 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
		LVV-T64 LDM-639		Not Executed
DMS-REQ-0119 LSE-61	DMS-REQ-0119-V-01 LVV-47	LVV-T117 LDM-639		Not Executed
DMS-REQ-0120 LSE-61	DMS-REQ-0120-V-01 LVV-48	LVV-T118 LDM-639		Not Executed
DMS-REQ-0121 LSE-61	DMS-REQ-0121-V-01 LVV-49	LVV-T119 LDM-639		Not Executed
DMS-REQ-0122 LSE-61	DMS-REQ-0122-V-01 LVV-50	LVV-T204 LDM-639		Not Executed
DMS-REQ-0123 LSE-61	DMS-REQ-0123-V-01 LVV-51	LVV-T205 LDM-639		Not Executed
DMS-REQ-0124 LSE-61	DMS-REQ-0124-V-01 LVV-52	LVV-T206 LDM-639		Not Executed
DMS-REQ-0125 LSE-61	DMS-REQ-0125-V-01 LVV-53	LVV-T120 LDM-639		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0126 LSE-61	DMS-REQ-0126-V-01 LVW-54	LVV-T207 LDM-639		Not Executed
DMS-REQ-0127 LSE-61	DMS-REQ-0127-V-01 LVW-55	LVV-T208 LDM-639		Not Executed
DMS-REQ-0128 LSE-61	DMS-REQ-0128-V-01 LVW-56	LVV-T121 LDM-639		Not Executed
DMS-REQ-0130 LSE-61	DMS-REQ-0130-V-01 LVW-57	LVV-T88 LDM-639		Not Executed
DMS-REQ-0131 LSE-61	DMS-REQ-0131-V-01 LVW-58 DMS-REQ-0131-V-02 LVW-9745	LVV-T106 LDM-639		Not Executed
DMS-REQ-0132 LSE-61	DMS-REQ-0132-V-01 LVW-59	LVV-T89 LDM-639		Not Executed
DMS-REQ-0155 LSE-61	DMS-REQ-0155-V-01 LVW-60	Verified in:	DMS-REQ-0298-V-01(LVW-129) DMS-REQ-0300-V-01(LVW-131) DMS-REQ-0299-V-01(LVW-130)	
DMS-REQ-0156 LSE-61	DMS-REQ-0156-V-01 LVW-61	Verified in:	DMS-REQ-0304-V-01(LVW-135) DMS-REQ-0303-V-01(LVW-134) DMS-REQ-0302-V-01(LVW-133)	
DMS-REQ-0158 LSE-61	DMS-REQ-0158-V-01 LVW-62	Verified in:	DMS-REQ-0305-V-01(LVW-136) DMS-REQ-0307-V-01(LVW-138) DMS-REQ-0306-V-01(LVW-137)	
		LVV-T11 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
DMS-REQ-0160 LSE-61	DMS-REQ-0160-V-01 LVW-63	LVV-T131 LDM-639		Not Executed
		LVV-T368 LDM-639	2018-12-06 DMTR-112 LVV-P16	Passed
DMS-REQ-0161 LSE-61	DMS-REQ-0161-V-01 LVW-64	LVV-T172 LDM-639		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0162 LSE-61	DMS-REQ-0162-V-01 LVW-65	LVV-T173		Not Executed
		LDM-639		
DMS-REQ-0163 LSE-61	DMS-REQ-0163-V-01 LVW-66	LVV-T287		Not Executed
		LDM-538		
DMS-REQ-0164 LSE-61	DMS-REQ-0164-V-01 LVW-67	LVV-T174		Not Executed
		LDM-639		
DMS-REQ-0165 LSE-61	DMS-REQ-0165-V-01 LVW-68	LVV-T175		Not Executed
		LDM-639		
DMS-REQ-0166 LSE-61	DMS-REQ-0166-V-01 LVW-69	LVV-T176		Not Executed
		LDM-639		
DMS-REQ-0167 LSE-61	DMS-REQ-0167-V-01 LVW-70	LVV-T177		Not Executed
		LDM-639		
DMS-REQ-0168 LSE-61	DMS-REQ-0168-V-01 LVW-71	LVV-T178		Not Executed
		LDM-639		
DMS-REQ-0169 LSE-61	DMS-REQ-0169-V-01 LVW-72	LVV-T287		Not Executed
		LDM-538		
DMS-REQ-0170 LSE-61	DMS-REQ-0170-V-01 LVW-73	LVV-T181		Not Executed
		LDM-639		
DMS-REQ-0171 LSE-61	DMS-REQ-0171-V-01 LVW-74	LVV-T1097		Not Executed
		LDM-639		
DMS-REQ-0172 LSE-61	DMS-REQ-0172-V-01 LVW-75	LVV-T182		Not Executed
		LDM-639		
DMS-REQ-0173 LSE-61	DMS-REQ-0173-V-01 LVW-76	LVV-T1097		Not Executed
		LDM-639		
DMS-REQ-0174 LSE-61	DMS-REQ-0174-V-01 LVW-77	LVV-T1168	2019-07-09	Passed
		LDM-639	DMTR-151 LVV-P47	
DMS-REQ-0175 LSE-61	DMS-REQ-0175-V-01 LVW-78	LVV-T185		Not Executed
		LDM-639		

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0173 LSE-61	DMS-REQ-0173-V-01 LVW-75	LVV-T186 LDM-639		Not Executed
DMS-REQ-0174 LSE-61	DMS-REQ-0174-V-01 LVW-76	LVV-T187 LDM-639		Not Executed
DMS-REQ-0175 LSE-61	DMS-REQ-0175-V-01 LVW-77	LVV-T188 LDM-639		Not Executed
DMS-REQ-0176 LSE-61	DMS-REQ-0176-V-01 LVW-78	LVV-T189 LDM-639		Not Executed
DMS-REQ-0178 LSE-61	DMS-REQ-0178-V-01 LVW-80	LVV-T190 LDM-639		Not Executed
DMS-REQ-0180 LSE-61	DMS-REQ-0180-V-01 LVW-81	LVV-T193 LDM-639		Not Executed
DMS-REQ-0181 LSE-61	DMS-REQ-0181-V-01 LVW-82	LVV-T194 LDM-639		Not Executed
DMS-REQ-0182 LSE-61	DMS-REQ-0182-V-01 LVW-83	LVV-T195 LDM-639		Not Executed
DMS-REQ-0183 LSE-61	DMS-REQ-0183-V-01 LVW-84	LVV-T196 LDM-639		Not Executed
DMS-REQ-0185 LSE-61	DMS-REQ-0185-V-01 LVW-85	LVV-T197 LDM-639		Not Executed
DMS-REQ-0186 LSE-61	DMS-REQ-0186-V-01 LVW-86	LVV-T198 LDM-639		Not Executed
DMS-REQ-0187 LSE-61	DMS-REQ-0187-V-01 LVW-87	LVV-T199 LDM-639		Not Executed
DMS-REQ-0188 LSE-61	DMS-REQ-0188-V-01 LVW-88	LVV-T200 LDM-639		Not Executed
DMS-REQ-0189 LSE-61	DMS-REQ-0189-V-01 LVW-89	LVV-T201 LDM-639		Not Executed
DMS-REQ-0190 LSE-61	DMS-REQ-0190-V-01 LVW-90	LVV-T202 LDM-639		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0191 LSE-61	DMS-REQ-0191-V-01 LVV-91	LVV-T203 LDM-639		Not Executed
DMS-REQ-0193 LSE-61	DMS-REQ-0193-V-01 LVV-92	LVV-T209 LDM-639		Not Executed
DMS-REQ-0194 LSE-61	DMS-REQ-0194-V-01 LVV-93	LVV-T210 LDM-639		Not Executed
DMS-REQ-0196 LSE-61	DMS-REQ-0196-V-01 LVV-94	LVV-T211 LDM-639		Not Executed
DMS-REQ-0197 LSE-61	DMS-REQ-0197-V-01 LVV-95	LVV-T212 LDM-639		Not Executed
DMS-REQ-0265 LSE-61	DMS-REQ-0265-V-01 LVV-96	LVV-T34 LDM-639		Not Executed
		LVV-T283 LDM-538	2019-05-22 DMTR-61 LVV-P45	Passed
		LVV-T284 LDM-538	2019-06-24 DMTR-102 LVV-P10	Passed
DMS-REQ-0266 LSE-61	DMS-REQ-0266-V-01 LVV-97	LVV-T48 LDM-639		Not Executed
DMS-REQ-0267 LSE-61	DMS-REQ-0267-V-01 LVV-98	LVV-T12 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
		LVV-T13 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
		LVV-T65 LDM-639		Not Executed
		LVV-T362 LDM-639	2019-03-31 DMTR-111 LVV-P15	Passed
DMS-REQ-0268 LSE-61	DMS-REQ-0268-V-01 LVV-99	LVV-T12 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
		LVV-T66 LDM-639		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0269 LSE-61	DMS-REQ-0269-V-01 LVV-100	LVV-T18 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T21 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T49 LDM-639		Not Executed
DMS-REQ-0270 LSE-61	DMS-REQ-0270-V-01 LVV-101	LVV-T21 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T50 LDM-639		Not Executed
DMS-REQ-0271 LSE-61	DMS-REQ-0271-V-01 LVV-102	LVV-T18 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T22 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T51 LDM-639		Not Executed
	DMS-REQ-0271-V-02 LVV-9742			
	DMS-REQ-0271-V-03 LVV-9743			
DMS-REQ-0272 LSE-61	DMS-REQ-0272-V-01 LVV-103	LVV-T22 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T52 LDM-639		Not Executed
DMS-REQ-0273 LSE-61	DMS-REQ-0273-V-01 LVV-104	LVV-T53 LDM-639		Not Executed
DMS-REQ-0274 LSE-61	DMS-REQ-0274-V-01 LVV-105	LVV-T54 LDM-639		Not Executed
DMS-REQ-0275 LSE-61	DMS-REQ-0275-V-01 LVV-106	LVV-T12 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T14 LDM-534	2019-05-22 DMTR-51 LVV-P43	Conditional Pass
		LVV-T67 LDM-639		Not Executed
DMS-REQ-0276 LSE-61	DMS-REQ-0276-V-01 LVV-107	LVV-T69 LDM-639		Not Executed
DMS-REQ-0277 LSE-61	DMS-REQ-0277-V-01 LVV-108			
DMS-REQ-0278 LSE-61	DMS-REQ-0278-V-01 LVV-109	LVV-T16 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
		LVV-T72 LDM-639		Not Executed
DMS-REQ-0279 LSE-61	DMS-REQ-0279-V-01 LVV-110	LVV-T12 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
		LVV-T16 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
		LVV-T73 LDM-639		Not Executed
DMS-REQ-0280 LSE-61	DMS-REQ-0280-V-01 LVV-111	LVV-T74 LDM-639		Not Executed
DMS-REQ-0281 LSE-61	DMS-REQ-0281-V-01 LVV-112	LVV-T75 LDM-639		Not Executed
DMS-REQ-0282 LSE-61	DMS-REQ-0282-V-01 LVV-113	LVV-T90 LDM-639		Not Executed
DMS-REQ-0283 LSE-61	DMS-REQ-0283-V-01 LVV-114	LVV-T91 LDM-639		Not Executed
DMS-REQ-0284 LSE-61	DMS-REQ-0284-V-01 LVV-115	LVV-T107 LDM-639		Not Executed
		LVV-T283 LDM-538	2019-05-22 DMTR-61 LVV-P45	Passed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T284 LDM-538	2019-06-24 DMTR-102 LVV-P10	Passed
		LVV-T286 LDM-538	2019-05-22 DMTR-61 LVV-P45	Passed
DMS-REQ-0285 LSE-61	DMS-REQ-0285-V-01 LVV-116	LVV-T22 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T108 LDM-639		Not Executed
		LVV-T550 LSE-419		Not Executed
DMS-REQ-0286 LSE-61	DMS-REQ-0286-V-01 LVV-117	LVV-T109 LDM-639		Not Executed
DMS-REQ-0287 LSE-61	DMS-REQ-0287-V-01 LVV-118	LVV-T110 LDM-639		Not Executed
	DMS-REQ-0287-V-02 LVV-9746			
	DMS-REQ-0287-V-03 LVV-9747			
DMS-REQ-0288 LSE-61	DMS-REQ-0288-V-01 LVV-119	LVV-T111 LDM-639		Not Executed
DMS-REQ-0289 LSE-61	DMS-REQ-0289-V-01 LVV-120	LVV-T115 LDM-639		Not Executed
DMS-REQ-0290 LSE-61	DMS-REQ-0290-V-01 LVV-121	LVV-T122 LDM-639		Not Executed
DMS-REQ-0291 LSE-61	DMS-REQ-0291-V-01 LVV-122	LVV-T96 LDM-639		Not Executed
DMS-REQ-0292 LSE-61	DMS-REQ-0292-V-01 LVV-123	LVV-T97 LDM-639		Not Executed
DMS-REQ-0293 LSE-61	DMS-REQ-0293-V-01 LVV-124	LVV-T11 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T98 LDM-639		Not Executed
DMS-REQ-0294 LSE-61	DMS-REQ-0294-V-01 LVV-125	LVV-T12 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
		LVV-T99 LDM-639		Not Executed
DMS-REQ-0295 LSE-61	DMS-REQ-0295-V-01 LVV-126	LVV-T100 LDM-639		Not Executed
DMS-REQ-0296 LSE-61	DMS-REQ-0296-V-01 LVV-127	LVV-T132 LDM-639		Not Executed
		LVV-T362	2019-03-31 DMTR-111 LVV-P15	Passed
DMS-REQ-0297 LSE-61	DMS-REQ-0297-V-01 LVV-128	LVV-T146 LDM-639		Not Executed
DMS-REQ-0298 LSE-61	DMS-REQ-0298-V-01 LVV-129	LVV-T136 LDM-639		Not Executed
		LVV-T368	2018-12-06 DMTR-112 LVV-P16	Passed
		LVV-T374	2018-12-06 DMTR-112 LVV-P16	Passed
DMS-REQ-0299 LSE-61	DMS-REQ-0299-V-01 LVV-130	LVV-T137 LDM-639		Not Executed
		LVV-T374	2018-12-06 DMTR-112 LVV-P16	Passed
DMS-REQ-0300 LSE-61	DMS-REQ-0300-V-01 LVV-131	LVV-T138 LDM-639		Not Executed
DMS-REQ-0301 LSE-61	DMS-REQ-0301-V-01 LVV-132	LVV-T147 LDM-639		Not Executed
DMS-REQ-0302 LSE-61	DMS-REQ-0302-V-01 LVV-133	LVV-T11 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T140 LDM-639		Not Executed
DMS-REQ-0303 LSE-61	DMS-REQ-0303-V-01 LVV-134	LVV-T11 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
		LVV-T141 LDM-639		Not Executed
DMS-REQ-0304 LSE-61	DMS-REQ-0304-V-01 LVV-135	LVV-T142 LDM-639		Not Executed
DMS-REQ-0305 LSE-61	DMS-REQ-0305-V-01 LVV-136	LVV-T11 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
		LVV-T144 LDM-639		Not Executed
DMS-REQ-0306 LSE-61	DMS-REQ-0306-V-01 LVV-137	LVV-T11 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
		LVV-T145 LDM-639		Not Executed
DMS-REQ-0307 LSE-61	DMS-REQ-0307-V-01 LVV-138	LVV-T148 LDM-639		Not Executed
DMS-REQ-0308 LSE-61	DMS-REQ-0308-V-01 LVV-139	LVV-T10 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
		LVV-T17 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T124 LDM-639		Not Executed
		LVV-T216 LDM-533	2018-07-17 DMTR-91 LVV-P1	Conditional Pass
		LVV-T362	2019-03-31 DMTR-111 LVV-P15	Passed
		LVV-T363	2019-03-31 DMTR-111 LVV-P15	Passed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0309 LSE-61	DMS-REQ-0309-V-01 LVV-140	LVV-T154		Not Executed
		LDM-639		
		LVV-T287		
		LDM-538		Not Executed
		LVV-T454	None	Not Executed
			DMTR-121 LVV-P32	
DMS-REQ-0310 LSE-61	DMS-REQ-0310-V-01 LVV-141	LVV-T155		Not Executed
		LDM-639		
DMS-REQ-0311 LSE-61	DMS-REQ-0311-V-01 LVV-142	LVV-T156		Not Executed
		LDM-639		
DMS-REQ-0312 LSE-61	DMS-REQ-0312-V-01 LVV-143	LVV-T157		Not Executed
		LDM-639		
DMS-REQ-0313 LSE-61	DMS-REQ-0313-V-01 LVV-144	LVV-T158		Not Executed
		LDM-639		
DMS-REQ-0314 LSE-61	DMS-REQ-0314-V-01 LVV-145	LVV-T179		Not Executed
		LDM-639		
		LVV-T287		Not Executed
		LDM-538		
DMS-REQ-0315 LSE-61	DMS-REQ-0315-V-01 LVV-146	LVV-T183		Not Executed
		LDM-639		
		LVV-T283	2019-05-22	
LDM-538	DMTR-61 LVV-P45			
		LVV-T284	2019-06-24	Passed
		LDM-538	DMTR-102 LVV-P10	
DMS-REQ-0316 LSE-61	DMS-REQ-0316-V-01 LVV-147	LVV-T191		Not Executed
		LDM-639		
DMS-REQ-0317 LSE-61	DMS-REQ-0317-V-01 LVV-148	LVV-T55		Not Executed
		LDM-639		
DMS-REQ-0318 LSE-61	DMS-REQ-0318-V-01 LVV-149	LVV-T180		Not Executed
		LDM-639		

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T287 LDM-538		Not Executed
DMS-REQ-0319 LSE-61	DMS-REQ-0319-V-01 LVV-150	LVV-T56 LDM-639		Not Executed
DMS-REQ-0320 LSE-61	DMS-REQ-0320-V-01 LVV-151	LVV-T92 LDM-639		Not Executed
DMS-REQ-0321 LSE-61	DMS-REQ-0321-V-01 LVV-152	LVV-T93 LDM-639		Not Executed
DMS-REQ-0322 LSE-61	DMS-REQ-0322-V-01 LVV-153	LVV-T94 LDM-639		Not Executed
DMS-REQ-0323 LSE-61	DMS-REQ-0323-V-01 LVV-154	LVV-T57 LDM-639		Not Executed
DMS-REQ-0324 LSE-61	DMS-REQ-0324-V-01 LVV-155	LVV-T58 LDM-639		Not Executed
DMS-REQ-0325 LSE-61	DMS-REQ-0325-V-01 LVV-156	LVV-T59 LDM-639		Not Executed
DMS-REQ-0326 LSE-61	DMS-REQ-0326-V-01 LVV-157	LVV-T23 LDM-639		Not Executed
DMS-REQ-0327 LSE-61	DMS-REQ-0327-V-01 LVV-158	LVV-T15 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed
		LVV-T19 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T43 LDM-639		Not Executed
DMS-REQ-0328 LSE-61	DMS-REQ-0328-V-01 LVV-159	LVV-T44 LDM-639		Not Executed
DMS-REQ-0329 LSE-61	DMS-REQ-0329-V-01 LVV-160	LVV-T76 LDM-639		Not Executed
DMS-REQ-0330 LSE-61	DMS-REQ-0330-V-01 LVV-161	LVV-T77 LDM-639		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0331 LSE-61	DMS-REQ-0331-V-01 LVV-162	LVV-T13	2019-05-22	Passed
		LDM-534	DMTR-51 LVV-P43	
		LVV-T14	2019-05-22	Conditional Pass
		LDM-534	DMTR-51 LVV-P43	
		LVV-T21	2019-05-22	Passed
LDM-533	DMTR-53 LVV-P44			
LVV-T22	2019-05-22	Passed		
LDM-533	DMTR-53 LVV-P44			
		LVV-T24		Not Executed
		LDM-639		
DMS-REQ-0332 LSE-61	DMS-REQ-0332-V-01 LVV-163	LVV-T25		Not Executed
		LDM-639		
DMS-REQ-0333 LSE-61	DMS-REQ-0333-V-01 LVV-164	LVV-T26		Not Executed
		LDM-639		
DMS-REQ-0334 LSE-61	DMS-REQ-0334-V-01 LVV-165	LVV-T12	2019-05-22	Passed
		LDM-534	DMTR-51 LVV-P43	
		LVV-T13	2019-05-22	Passed
		LDM-534	DMTR-51 LVV-P43	
		LVV-T14	2019-05-22	Conditional Pass
		LDM-534	DMTR-51 LVV-P43	
LVV-T15	2019-05-22	Passed		
LDM-534	DMTR-51 LVV-P43			
LVV-T16	2019-05-22	Passed		
LDM-534	DMTR-51 LVV-P43			
		LVV-T78		Not Executed
		LDM-639		
DMS-REQ-0335 LSE-61	DMS-REQ-0335-V-01 LVV-166	LVV-T79		Not Executed
		LDM-639		
DMS-REQ-0336 LSE-61	DMS-REQ-0336-V-01 LVV-167	LVV-T159		Not Executed
		LDM-639		

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0337 LSE-61	DMS-REQ-0337-V-01 LVV-168	LVV-T80 LDM-639		Not Executed
DMS-REQ-0338 LSE-61	DMS-REQ-0338-V-01 LVV-169	LVV-T81 LDM-639		Not Executed
DMS-REQ-0339 LSE-61	DMS-REQ-0339-V-01 LVV-170	LVV-T82 LDM-639		Not Executed
DMS-REQ-0340 LSE-61	DMS-REQ-0340-V-01 LVV-171	LVV-T123 LDM-639		Not Executed
DMS-REQ-0341 LSE-61	DMS-REQ-0341-V-01 LVV-172	LVV-T160 LDM-639		Not Executed
	DMS-REQ-0341-V-02 LVV-9749			
DMS-REQ-0342 LSE-61	DMS-REQ-0342-V-01 LVV-173	LVV-T112 LDM-639		Not Executed
		LVV-T218 LDM-533	2018-07-04 DMTR-91 LVV-P1	Conditional Pass
DMS-REQ-0343 LSE-61	DMS-REQ-0343-V-01 LVV-174	LVV-T113 LDM-639		Not Executed
		LVV-T218 LDM-533	2018-07-04 DMTR-91 LVV-P1	Conditional Pass
	DMS-REQ-0343-V-02 LVV-9748	LVV-T1252 LDM-639		Not Executed
DMS-REQ-0345 LSE-61	DMS-REQ-0345-V-01 LVV-176	LVV-T161 LDM-639		Not Executed
DMS-REQ-0346 LSE-61	DMS-REQ-0346-V-01 LVV-177	LVV-T27 LDM-639		Not Executed
		LVV-T286 LDM-538	2019-05-22 DMTR-61 LVV-P45	Passed
DMS-REQ-0347 LSE-61	DMS-REQ-0347-V-01 LVV-178	LVV-T13 LDM-534	2019-05-22 DMTR-51 LVV-P43	Passed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T14 LDM-534	2019-05-22 DMTR-51 LVV-P43	Conditional Pass
		LVV-T21 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T22 LDM-533	2019-05-22 DMTR-53 LVV-P44	Passed
		LVV-T28 LDM-639		Not Executed
DMS-REQ-0348 LSE-61	DMS-REQ-0348-V-01 LVV-179	LVV-T114 LDM-639		Not Executed
		LVV-T218 LDM-533	2018-07-04 DMTR-91 LVV-P1	Conditional Pass
DMS-REQ-0349 LSE-61	DMS-REQ-0349-V-01 LVV-180	LVV-T71 LDM-639		Not Executed
DMS-REQ-0350 LSE-61	DMS-REQ-0350-V-01 LVV-181	LVV-T116 LDM-639		Not Executed
DMS-REQ-0351 LSE-61	DMS-REQ-0351-V-01 LVV-182	LVV-T133 LDM-639		Not Executed
DMS-REQ-0352 LSE-61	DMS-REQ-0352-V-01 LVV-183	LVV-T192 LDM-639		Not Executed
DMS-REQ-0353 LSE-61	DMS-REQ-0353-V-01 LVV-184	LVV-T60 LDM-639		Not Executed
DMS-REQ-0354 LSE-61	DMS-REQ-0354-V-01 LVV-185	LVV-T1086 LDM-552	2019-07-08 DMTR-71 LVV-P46	Passed
		LVV-T1087 LDM-552	2019-07-08 DMTR-71 LVV-P46	Passed
		LVV-T1088 LDM-552	2019-07-09 DMTR-71 LVV-P46	Passed
		LVV-T1089 LDM-552	2019-07-09 DMTR-71 LVV-P46	Conditional Pass

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T1090 LDM-552	2019-07-09 DMTR-71 LVV-P46	Conditional Pass
DMS-REQ-0355 LSE-61	DMS-REQ-0355-V-01 LVV-186			
	DMS-REQ-0355-V-02 LVV-9784			
DMS-REQ-0356 LSE-61	DMS-REQ-0356-V-01 LVV-187			
	DMS-REQ-0356-V-02 LVV-9785			
	DMS-REQ-0356-V-03 LVV-9786	LVV-T1089 LDM-552	2019-07-09 DMTR-71 LVV-P46	Conditional Pass
		LVV-T1090 LDM-552	2019-07-09 DMTR-71 LVV-P46	Conditional Pass
	DMS-REQ-0356-V-04 LVV-9787	LVV-T1085 LDM-552	2019-07-08 DMTR-71 LVV-P46	Passed
		LVV-T1089 LDM-552	2019-07-09 DMTR-71 LVV-P46	Conditional Pass
		LVV-T1090 LDM-552	2019-07-09 DMTR-71 LVV-P46	Conditional Pass
DMS-REQ-0357 LSE-61	DMS-REQ-0357-V-01 LVV-188	LVV-T1086 LDM-552	2019-07-08 DMTR-71 LVV-P46	Passed
		LVV-T1088 LDM-552	2019-07-09 DMTR-71 LVV-P46	Passed
		LVV-T1089 LDM-552	2019-07-09 DMTR-71 LVV-P46	Conditional Pass
		LVV-T1090 LDM-552	2019-07-09 DMTR-71 LVV-P46	Conditional Pass
DMS-REQ-0363 LSE-61	DMS-REQ-0363-V-01 LVV-189	LVV-T162 LDM-639		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0364 LSE-61	DMS-REQ-0364-V-01 LVW-190	LVW-T163 LDM-639		Not Executed
	DMS-REQ-0364-V-02 LVW-9750			
DMS-REQ-0365 LSE-61	DMS-REQ-0365-V-01 LVW-191	LVW-T164 LDM-639		Not Executed
DMS-REQ-0366 LSE-61	DMS-REQ-0366-V-01 LVW-192	LVW-T165 LDM-639		Not Executed
DMS-REQ-0367 LSE-61	DMS-REQ-0367-V-01 LVW-193	LVW-T166 LDM-639		Not Executed
DMS-REQ-0368 LSE-61	DMS-REQ-0368-V-01 LVW-194	LVW-T167 LDM-639		Not Executed
DMS-REQ-0369 LSE-61	DMS-REQ-0369-V-01 LVW-195	LVW-T168 LDM-639		Not Executed
DMS-REQ-0370 LSE-61	DMS-REQ-0370-V-01 LVW-196	LVW-T169 LDM-639		Not Executed
DMS-REQ-0371 LSE-61	DMS-REQ-0371-V-01 LVW-197	LVW-T170 LDM-639		Not Executed
DMS-REQ-0377 LSE-61	DMS-REQ-0377-V-01 LVW-3394	LVW-T385 LDM-639		Not Executed
	DMS-REQ-0377-V-02 LVW-9797			
DMS-REQ-0374 LSE-61	DMS-REQ-0374-V-01 LVW-3395			
	DMS-REQ-0374-V-02 LVW-9790			
	DMS-REQ-0374-V-03 LVW-9791			
DMS-REQ-0376 LSE-61	DMS-REQ-0376-V-01 LVW-3396			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	DMS-REQ-0376-V-02 LVW-9795			
	DMS-REQ-0376-V-03 LVW-9796			
DMS-REQ-0373 LSE-61	DMS-REQ-0373-V-01 LVW-3397			
	DMS-REQ-0373-V-02 LVW-9789			
DMS-REQ-0375 LSE-61	DMS-REQ-0375-V-01 LVW-3398			
	DMS-REQ-0375-V-02 LVW-9792			
	DMS-REQ-0375-V-03 LVW-9793			
	DMS-REQ-0375-V-04 LVW-9794			
DMS-REQ-0378 LSE-61	DMS-REQ-0378-V-01 LVW-3399			
DMS-REQ-0358 LSE-61	DMS-REQ-0358-V-01 LVW-3400	LVW-T1250 LDM-639		Not Executed
	DMS-REQ-0358-V-02 LVW-9788	LVW-T1251 LDM-639		Not Executed
DMS-REQ-0359 LSE-61	DMS-REQ-0359-V-01 LVW-3401	LVW-T363 DMTR-111 LVW-P15	2019-03-31	Passed
		LVW-T377 LDM-639		Not Executed
	DMS-REQ-0359-V-02 LVW-9751	LVW-T377 LDM-639		Not Executed
	DMS-REQ-0359-V-03 LVW-9752	LVW-T377 LDM-639		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	DMS-REQ-0359-V-04 LVV-9753	LVV-T377 LDM-639		Not Executed
	DMS-REQ-0359-V-05 LVV-9754	LVV-T377 LDM-639		Not Executed
	DMS-REQ-0359-V-06 LVV-9755	LVV-T377 LDM-639		Not Executed
	DMS-REQ-0359-V-07 LVV-9756	LVV-T377 LDM-639		Not Executed
	DMS-REQ-0359-V-08 LVV-9757	LVV-T377 LDM-639		Not Executed
	DMS-REQ-0359-V-09 LVV-9758	LVV-T377 LDM-639		Not Executed
	DMS-REQ-0359-V-10 LVV-9759	LVV-T377 LDM-639		Not Executed
	DMS-REQ-0359-V-11 LVV-9760	LVV-T377 LDM-639		Not Executed
	DMS-REQ-0359-V-12 LVV-9761	LVV-T377 LDM-639		Not Executed
	DMS-REQ-0359-V-13 LVV-9762	LVV-T377 LDM-639		Not Executed
	DMS-REQ-0359-V-14 LVV-9763	LVV-T377 LDM-639		Not Executed
	DMS-REQ-0359-V-15 LVV-9764	LVV-T377 LDM-639		Not Executed
	DMS-REQ-0359-V-16 LVV-9765	LVV-T377 LDM-639		Not Executed
	DMS-REQ-0359-V-17 LVV-9766	LVV-T377 LDM-639		Not Executed
DMS-REQ-0360 LSE-61	DMS-REQ-0360-V-01 LVV-3402	LVV-T363	2019-03-31 DMTR-111 LVV-P15	Passed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T378 LDM-639		Not Executed
	DMS-REQ-0360-V-02 LVV-9767	LVV-T378 LDM-639		Not Executed
	DMS-REQ-0360-V-03 LVV-9768	LVV-T378 LDM-639		Not Executed
	DMS-REQ-0360-V-04 LVV-9769	LVV-T378 LDM-639		Not Executed
	DMS-REQ-0360-V-05 LVV-9770	LVV-T378 LDM-639		Not Executed
	DMS-REQ-0360-V-06 LVV-9771	LVV-T378 LDM-639		Not Executed
	DMS-REQ-0360-V-07 LVV-9773	LVV-T378 LDM-639		Not Executed
	DMS-REQ-0360-V-08 LVV-9774	LVV-T378 LDM-639		Not Executed
	DMS-REQ-0360-V-09 LVV-9775	LVV-T378 LDM-639		Not Executed
	DMS-REQ-0360-V-10 LVV-9776	LVV-T378 LDM-639		Not Executed
	DMS-REQ-0360-V-11 LVV-9777	LVV-T378 LDM-639		Not Executed
	DMS-REQ-0360-V-12 LVV-9778	LVV-T378 LDM-639		Not Executed
	DMS-REQ-0360-V-13 LVV-9779	LVV-T378 LDM-639		Not Executed
DMS-REQ-0361 LSE-61	DMS-REQ-0361-V-01 LVV-3403	LVV-T1088 LDM-552	2019-07-09 DMTR-71 LVV-P46	Passed
		LVV-T1089 LDM-552	2019-07-09 DMTR-71 LVV-P46	Conditional Pass

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T1090 LDM-552	2019-07-09 DMTR-71 LVV-P46	Conditional Pass
DMS-REQ-0362 LSE-61	DMS-REQ-0362-V-01 LVV-3404	LVV-T376 LDM-639		Not Executed
	DMS-REQ-0362-V-02 LVV-9780			
	DMS-REQ-0362-V-03 LVV-9781			
	DMS-REQ-0362-V-04 LVV-9782			
	DMS-REQ-0362-V-05 LVV-9783			
CA-DM-DAQ-ICD-0094 LSE-68	CA-DM-DAQ-ICD-0094-V-03 LVV-4669			
	CA-DM-DAQ-ICD-0094-V-04 LVV-4670			
CA-DM-DAQ-ICD-0082 LSE-68	CA-DM-DAQ-ICD-0082-V-03 LVV-4675			
	CA-DM-DAQ-ICD-0082-V-04 LVV-4676			
CA-DM-DAQ-ICD-0052 LSE-68	CA-DM-DAQ-ICD-0052-V-03 LVV-4681			
	CA-DM-DAQ-ICD-0052-V-04 LVV-4682			
CA-DM-DAQ-ICD-0095 LSE-68	CA-DM-DAQ-ICD-0095-V-03 LVV-4687			
	CA-DM-DAQ-ICD-0095-V-04 LVV-4688			
CA-DM-DAQ-ICD-0054 LSE-68	CA-DM-DAQ-ICD-0054-V-03 LVV-4693			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	CA-DM-DAQ-ICD-0054-V-04 LWV-4694			
CA-DM-DAQ-ICD-0089 LSE-68	CA-DM-DAQ-ICD-0089-V-03 LWV-4699			
	CA-DM-DAQ-ICD-0089-V-04 LWV-4700			
CA-DM-DAQ-ICD-0051 LSE-68	CA-DM-DAQ-ICD-0051-V-03 LWV-4705			
	CA-DM-DAQ-ICD-0051-V-04 LWV-4706			
CA-DM-DAQ-ICD-0053 LSE-68	CA-DM-DAQ-ICD-0053-V-03 LWV-4711			
	CA-DM-DAQ-ICD-0053-V-04 LWV-4712			
CA-DM-DAQ-ICD-0055 LSE-68	CA-DM-DAQ-ICD-0055-V-03 LWV-4717			
	CA-DM-DAQ-ICD-0055-V-04 LWV-4718			
CA-DM-DAQ-ICD-0056 LSE-68	CA-DM-DAQ-ICD-0056-V-03 LWV-4723			
	CA-DM-DAQ-ICD-0056-V-04 LWV-4724			
CA-DM-DAQ-ICD-0093 LSE-68	CA-DM-DAQ-ICD-0093-V-03 LWV-4729			
	CA-DM-DAQ-ICD-0093-V-04 LWV-4730			
CA-DM-DAQ-ICD-0097 LSE-68	CA-DM-DAQ-ICD-0097-V-03 LWV-4735			
	CA-DM-DAQ-ICD-0097-V-04 LWV-4736			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
CA-DM-DAQ-ICD-0058 LSE-68	CA-DM-DAQ-ICD-0058-V-03			
	LWV-4741			
CA-DM-DAQ-ICD-0058-V-04	CA-DM-DAQ-ICD-0058-V-04			
	LWV-4742			
CA-DM-DAQ-ICD-0059 LSE-68	CA-DM-DAQ-ICD-0059-V-03			
	LWV-4747			
CA-DM-DAQ-ICD-0059-V-04	CA-DM-DAQ-ICD-0059-V-04			
	LWV-4748			
CA-DM-DAQ-ICD-0060 LSE-68	CA-DM-DAQ-ICD-0060-V-03			
	LWV-4753			
CA-DM-DAQ-ICD-0060-V-04	CA-DM-DAQ-ICD-0060-V-04			
	LWV-4754			
CA-DM-DAQ-ICD-0081 LSE-68	CA-DM-DAQ-ICD-0081-V-03			
	LWV-4759			
CA-DM-DAQ-ICD-0081-V-04	CA-DM-DAQ-ICD-0081-V-04			
	LWV-4760			
CA-DM-DAQ-ICD-0047 LSE-68	CA-DM-DAQ-ICD-0047-V-03			
	LWV-4765			
CA-DM-DAQ-ICD-0047-V-04	CA-DM-DAQ-ICD-0047-V-04			
	LWV-4766			
CA-DM-DAQ-ICD-0098 LSE-68	CA-DM-DAQ-ICD-0098-V-03			
	LWV-4771			
CA-DM-DAQ-ICD-0098-V-04	CA-DM-DAQ-ICD-0098-V-04			
	LWV-4772			
CA-DM-DAQ-ICD-0100 LSE-68	CA-DM-DAQ-ICD-0100-V-03			
	LWV-4777			
CA-DM-DAQ-ICD-0100-V-04	CA-DM-DAQ-ICD-0100-V-04			
	LWV-4778			
CA-DM-DAQ-ICD-0092 LSE-68	CA-DM-DAQ-ICD-0092-V-03			
	LWV-4783			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	CA-DM-DAQ-ICD-0092-V-04 LVW-4784			
CA-DM-DAQ-ICD-0084 LSE-68	CA-DM-DAQ-ICD-0084-V-03 LVW-4789			
	CA-DM-DAQ-ICD-0084-V-04 LVW-4790			
CA-DM-DAQ-ICD-0099 LSE-68	CA-DM-DAQ-ICD-0099-V-03 LVW-4795			
	CA-DM-DAQ-ICD-0099-V-04 LVW-4796			
CA-DM-DAQ-ICD-0085 LSE-68	CA-DM-DAQ-ICD-0085-V-03 LVW-4801			
	CA-DM-DAQ-ICD-0085-V-04 LVW-4802			
CA-DM-DAQ-ICD-0086 LSE-68	CA-DM-DAQ-ICD-0086-V-03 LVW-4807			
	CA-DM-DAQ-ICD-0086-V-04 LVW-4808			
CA-DM-DAQ-ICD-0057 LSE-68	CA-DM-DAQ-ICD-0057-V-03 LVW-4813			
	CA-DM-DAQ-ICD-0057-V-04 LVW-4814			
CA-DM-DAQ-ICD-0091 LSE-68	CA-DM-DAQ-ICD-0091-V-03 LVW-4819			
	CA-DM-DAQ-ICD-0091-V-04 LVW-4820			
CA-DM-DAQ-ICD-0075 LSE-68	CA-DM-DAQ-ICD-0075-V-03 LVW-4825			
	CA-DM-DAQ-ICD-0075-V-04 LVW-4826			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
CA-DM-DAQ-ICD-0080 LSE-68	CA-DM-DAQ-ICD-0080-V-03			
	LWV-4831			
	CA-DM-DAQ-ICD-0080-V-04			
	LWV-4832			
CA-DM-DAQ-ICD-0096 LSE-68	CA-DM-DAQ-ICD-0096-V-03			
	LWV-4837			
	CA-DM-DAQ-ICD-0096-V-04			
	LWV-4838			
CA-DM-CON-ICD-0003 LSE-69	CA-DM-CON-ICD-0003-V-03			
	LWV-4843			
	CA-DM-CON-ICD-0003-V-04			
	LWV-4844			
CA-DM-CON-ICD-0004 LSE-69	CA-DM-CON-ICD-0004-V-03			
	LWV-4849			
	CA-DM-CON-ICD-0004-V-04			
	LWV-4850			
CA-DM-CON-ICD-0019 LSE-69	CA-DM-CON-ICD-0019-V-03			
	LWV-4855			
	CA-DM-CON-ICD-0019-V-04			
	LWV-4856			
CA-DM-CON-ICD-0008 LSE-69	CA-DM-CON-ICD-0008-V-03			
	LWV-4861			
	CA-DM-CON-ICD-0008-V-04			
	LWV-4862			
CA-DM-CON-ICD-0017 LSE-69	CA-DM-CON-ICD-0017-V-03			
	LWV-4867			
	CA-DM-CON-ICD-0017-V-04			
	LWV-4868			
CA-DM-CON-ICD-0002 LSE-69	CA-DM-CON-ICD-0002-V-03			
	LWV-4873			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	CA-DM-CON-ICD-0002-V-04 LWV-4874			
CA-DM-CON-ICD-0005 LSE-69	CA-DM-CON-ICD-0005-V-03 LWV-4879			
	CA-DM-CON-ICD-0005-V-04 LWV-4880			
CA-DM-CON-ICD-0001 LSE-69	CA-DM-CON-ICD-0001-V-03 LWV-4885			
	CA-DM-CON-ICD-0001-V-04 LWV-4886			
CA-DM-CON-ICD-0011 LSE-69	CA-DM-CON-ICD-0011-V-03 LWV-4891			
	CA-DM-CON-ICD-0011-V-04 LWV-4892			
CA-DM-CON-ICD-0018 LSE-69	CA-DM-CON-ICD-0018-V-03 LWV-4897			
	CA-DM-CON-ICD-0018-V-04 LWV-4898			
CA-DM-CON-ICD-0007 LSE-69	CA-DM-CON-ICD-0007-V-03 LWV-4903			
	CA-DM-CON-ICD-0007-V-04 LWV-4904			
CA-DM-CON-ICD-0016 LSE-69	CA-DM-CON-ICD-0016-V-03 LWV-4909			
	CA-DM-CON-ICD-0016-V-04 LWV-4910			
CA-DM-CON-ICD-0014 LSE-69	CA-DM-CON-ICD-0014-V-03 LWV-4915			
	CA-DM-CON-ICD-0014-V-04 LWV-4916			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
CA-DM-CON-ICD-0015 LSE-69	CA-DM-CON-ICD-0015-V-03			
	LWV-4921			
	CA-DM-CON-ICD-0015-V-04			
	LWV-4922			
OCS-DM-COM-ICD-0040 LSE-72	OCS-DM-COM-ICD-0040-V-01			
	LWV-5237			
	OCS-DM-COM-ICD-0040-V-02			
	LWV-5238			
OCS-DM-COM-ICD-0009 LSE-72	OCS-DM-COM-ICD-0009-V-01			
	LWV-5243			
	OCS-DM-COM-ICD-0009-V-02			
	LWV-5244			
OCS-DM-COM-ICD-0013 LSE-72	OCS-DM-COM-ICD-0013-V-01			
	LWV-5249			
	OCS-DM-COM-ICD-0013-V-02			
	LWV-5250			
OCS-DM-COM-ICD-0015 LSE-72	OCS-DM-COM-ICD-0015-V-01			
	LWV-5255			
	OCS-DM-COM-ICD-0015-V-02			
	LWV-5256			
OCS-DM-COM-ICD-0014 LSE-72	OCS-DM-COM-ICD-0014-V-01			
	LWV-5261			
	OCS-DM-COM-ICD-0014-V-02			
	LWV-5262			
OCS-DM-COM-ICD-0038 LSE-72	OCS-DM-COM-ICD-0038-V-01			
	LWV-5267			
	OCS-DM-COM-ICD-0038-V-02			
	LWV-5268			
OCS-DM-COM-ICD-0039 LSE-72	OCS-DM-COM-ICD-0039-V-01			
	LWV-5273			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	OCS-DM-COM-ICD-0039-V-02 LWV-5274			
OCS-DM-COM-ICD-0037 LSE-72	OCS-DM-COM-ICD-0037-V-01 LWV-5279			
	OCS-DM-COM-ICD-0037-V-02 LWV-5280			
OCS-DM-COM-ICD-0036 LSE-72	OCS-DM-COM-ICD-0036-V-01 LWV-5285			
	OCS-DM-COM-ICD-0036-V-02 LWV-5286			
OCS-DM-COM-ICD-0012 LSE-72	OCS-DM-COM-ICD-0012-V-01 LWV-5291			
	OCS-DM-COM-ICD-0012-V-02 LWV-5292			
OCS-DM-COM-ICD-0003 LSE-72	OCS-DM-COM-ICD-0003-V-01 LWV-5297			
	OCS-DM-COM-ICD-0003-V-02 LWV-5298			
OCS-DM-COM-ICD-0034 LSE-72	OCS-DM-COM-ICD-0034-V-01 LWV-5303			
	OCS-DM-COM-ICD-0034-V-02 LWV-5304			
OCS-DM-COM-ICD-0032 LSE-72	OCS-DM-COM-ICD-0032-V-01 LWV-5309			
	OCS-DM-COM-ICD-0032-V-02 LWV-5310			
OCS-DM-COM-ICD-0006 LSE-72	OCS-DM-COM-ICD-0006-V-01 LWV-5315			
	OCS-DM-COM-ICD-0006-V-02 LWV-5316			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
OCS-DM-COM-ICD-0004 LSE-72	OCS-DM-COM-ICD-0004-V-01			
	LWV-5321			
OCS-DM-COM-ICD-0008 LSE-72	OCS-DM-COM-ICD-0004-V-02			
	LWV-5322			
OCS-DM-COM-ICD-0008 LSE-72	OCS-DM-COM-ICD-0008-V-01			
	LWV-5327			
OCS-DM-COM-ICD-0008 LSE-72	OCS-DM-COM-ICD-0008-V-02			
	LWV-5328			
OCS-DM-COM-ICD-0033 LSE-72	OCS-DM-COM-ICD-0033-V-01			
	LWV-5333			
OCS-DM-COM-ICD-0033 LSE-72	OCS-DM-COM-ICD-0033-V-02			
	LWV-5334			
OCS-DM-COM-ICD-0005 LSE-72	OCS-DM-COM-ICD-0005-V-01			
	LWV-5339			
OCS-DM-COM-ICD-0005 LSE-72	OCS-DM-COM-ICD-0005-V-02			
	LWV-5340			
OCS-DM-COM-ICD-0035 LSE-72	OCS-DM-COM-ICD-0035-V-01			
	LWV-5345			
OCS-DM-COM-ICD-0035 LSE-72	OCS-DM-COM-ICD-0035-V-02			
	LWV-5346			
OCS-DM-COM-ICD-0007 LSE-72	OCS-DM-COM-ICD-0007-V-01			
	LWV-5351			
OCS-DM-COM-ICD-0007 LSE-72	OCS-DM-COM-ICD-0007-V-02			
	LWV-5352			
OCS-DM-COM-ICD-0048 LSE-72	OCS-DM-COM-ICD-0048-V-01			
	LWV-5357			
OCS-DM-COM-ICD-0048 LSE-72	OCS-DM-COM-ICD-0048-V-02			
	LWV-5358			
OCS-DM-COM-ICD-0055 LSE-72	OCS-DM-COM-ICD-0055-V-01			
	LWV-5363			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	OCS-DM-COM-ICD-0055-V-02			
	LWV-5364			
OCS-DM-COM-ICD-0054	OCS-DM-COM-ICD-0054-V-01			
LSE-72	LWV-5369			
	OCS-DM-COM-ICD-0054-V-02			
	LWV-5370			
OCS-DM-COM-ICD-0019	OCS-DM-COM-ICD-0019-V-01			
LSE-72	LWV-5375			
	OCS-DM-COM-ICD-0019-V-02			
	LWV-5376			
OCS-DM-COM-ICD-0017	OCS-DM-COM-ICD-0017-V-01			
LSE-72	LWV-5381			
	OCS-DM-COM-ICD-0017-V-02			
	LWV-5382			
OCS-DM-COM-ICD-0018	OCS-DM-COM-ICD-0018-V-01			
LSE-72	LWV-5387			
	OCS-DM-COM-ICD-0018-V-02			
	LWV-5388			
OCS-DM-COM-ICD-0021	OCS-DM-COM-ICD-0021-V-01			
LSE-72	LWV-5393			
	OCS-DM-COM-ICD-0021-V-02			
	LWV-5394			
OCS-DM-COM-ICD-0020	OCS-DM-COM-ICD-0020-V-01			
LSE-72	LWV-5399			
	OCS-DM-COM-ICD-0020-V-02			
	LWV-5400			
OCS-DM-COM-ICD-0047	OCS-DM-COM-ICD-0047-V-01			
LSE-72	LWV-5405			
	OCS-DM-COM-ICD-0047-V-02			
	LWV-5406			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
OCS-DM-COM-ICD-0046 LSE-72	OCS-DM-COM-ICD-0046-V-01			
	LWV-5411			
OCS-DM-COM-ICD-0045 LSE-72	OCS-DM-COM-ICD-0045-V-01			
	LWV-5417			
OCS-DM-COM-ICD-0043 LSE-72	OCS-DM-COM-ICD-0043-V-01			
	LWV-5423			
OCS-DM-COM-ICD-0044 LSE-72	OCS-DM-COM-ICD-0044-V-01			
	LWV-5429			
OCS-DM-COM-ICD-0052 LSE-72	OCS-DM-COM-ICD-0052-V-01			
	LWV-5435			
OCS-DM-COM-ICD-0051 LSE-72	OCS-DM-COM-ICD-0051-V-01			
	LWV-5441			
OCS-DM-COM-ICD-0056 LSE-72	OCS-DM-COM-ICD-0056-V-01			
	LWV-5447			
OCS-DM-COM-ICD-0050 LSE-72	OCS-DM-COM-ICD-0050-V-01			
	LWV-5453			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	OCS-DM-COM-ICD-0050-V-02			
	LVW-5454			
OCS-DM-COM-ICD-0053	OCS-DM-COM-ICD-0053-V-01			
LSE-72	LVW-5459			
	OCS-DM-COM-ICD-0053-V-02			
	LVW-5460			
OCS-DM-COM-ICD-0022	OCS-DM-COM-ICD-0022-V-01			
LSE-72	LVW-5465			
	OCS-DM-COM-ICD-0022-V-02			
	LVW-5466			
OCS-DM-COM-ICD-0049	OCS-DM-COM-ICD-0049-V-01			
LSE-72	LVW-5471			
	OCS-DM-COM-ICD-0049-V-02			
	LVW-5472			
OCS-DM-COM-ICD-0023	OCS-DM-COM-ICD-0023-V-01			
LSE-72	LVW-5477			
	OCS-DM-COM-ICD-0023-V-02			
	LVW-5478			
OCS-DM-COM-ICD-0025	OCS-DM-COM-ICD-0025-V-01			
LSE-72	LVW-5483			
	OCS-DM-COM-ICD-0025-V-02			
	LVW-5484			
OCS-DM-COM-ICD-0029	OCS-DM-COM-ICD-0029-V-01			
LSE-72	LVW-5489			
	OCS-DM-COM-ICD-0029-V-02			
	LVW-5490			
OCS-DM-COM-ICD-0042	OCS-DM-COM-ICD-0042-V-01			
LSE-72	LVW-5495			
	OCS-DM-COM-ICD-0042-V-02			
	LVW-5496			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
OCS-DM-COM-ICD-0030 LSE-72	OCS-DM-COM-ICD-0030-V-01			
	LVW-5501			
OCS-DM-COM-ICD-0030	OCS-DM-COM-ICD-0030-V-02			
	LVW-5502			
OCS-DM-COM-ICD-0026 LSE-72	OCS-DM-COM-ICD-0026-V-01			
	LVW-5507			
OCS-DM-COM-ICD-0026	OCS-DM-COM-ICD-0026-V-02			
	LVW-5508			
OCS-DM-COM-ICD-0028 LSE-72	OCS-DM-COM-ICD-0028-V-01			
	LVW-5513			
OCS-DM-COM-ICD-0028	OCS-DM-COM-ICD-0028-V-02			
	LVW-5514			
OCS-DM-COM-ICD-0041 LSE-72	OCS-DM-COM-ICD-0041-V-01			
	LVW-5519			
OCS-DM-COM-ICD-0041	OCS-DM-COM-ICD-0041-V-02			
	LVW-5520			
OCS-DM-COM-ICD-0027 LSE-72	OCS-DM-COM-ICD-0027-V-01			
	LVW-5525			
OCS-DM-COM-ICD-0027	OCS-DM-COM-ICD-0027-V-02			
	LVW-5526			
OCS-DM-COM-ICD-0031 LSE-72	OCS-DM-COM-ICD-0031-V-01			
	LVW-5531			
OCS-DM-COM-ICD-0031	OCS-DM-COM-ICD-0031-V-02			
	LVW-5532			
OCS-DM-COM-ICD-0002 LSE-72	OCS-DM-COM-ICD-0002-V-01			
	LVW-5537			
OCS-DM-COM-ICD-0002	OCS-DM-COM-ICD-0002-V-02			
	LVW-5538			
OCS-DM-COM-ICD-0001 LSE-72	OCS-DM-COM-ICD-0001-V-01			
	LVW-5543			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	OCS-DM-COM-ICD-0001-V-02 LVW-5544			
DM-TS-CON-ICD-0003 LSE-75	DM-TS-CON-ICD-0003-V-01 LVW-5628			
	DM-TS-CON-ICD-0003-V-02 LVW-5629			
DM-TS-CON-ICD-0010 LSE-75	DM-TS-CON-ICD-0010-V-01 LVW-5634			
	DM-TS-CON-ICD-0010-V-02 LVW-5635			
DM-TS-CON-ICD-0011 LSE-75	DM-TS-CON-ICD-0011-V-01 LVW-5640			
	DM-TS-CON-ICD-0011-V-02 LVW-5641			
DM-TS-CON-ICD-0002 LSE-75	DM-TS-CON-ICD-0002-V-01 LVW-5646			
	DM-TS-CON-ICD-0002-V-02 LVW-5647			
DM-TS-CON-ICD-0006 LSE-75	DM-TS-CON-ICD-0006-V-01 LVW-5652			
	DM-TS-CON-ICD-0006-V-02 LVW-5653			
DM-TS-CON-ICD-0007 LSE-75	DM-TS-CON-ICD-0007-V-01 LVW-5658			
	DM-TS-CON-ICD-0007-V-02 LVW-5659			
DM-TS-CON-ICD-0009 LSE-75	DM-TS-CON-ICD-0009-V-01 LVW-5664			
	DM-TS-CON-ICD-0009-V-02 LVW-5665			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DM-TS-CON-ICD-0008 LSE-75	DM-TS-CON-ICD-0008-V-01			
	LWV-5670			
DM-TS-CON-ICD-0008	DM-TS-CON-ICD-0008-V-02			
	LWV-5671			
DM-TS-CON-ICD-0004 LSE-75	DM-TS-CON-ICD-0004-V-01			
	LWV-5676			
DM-TS-CON-ICD-0004	DM-TS-CON-ICD-0004-V-02			
	LWV-5677			
CA-DM-SUP-ICD-0026 LSE-130	CA-DM-SUP-ICD-0026-V-03			
	LWV-6140			
CA-DM-SUP-ICD-0026	CA-DM-SUP-ICD-0026-V-04			
	LWV-6141			
CA-DM-SUP-ICD-0027 LSE-130	CA-DM-SUP-ICD-0027-V-03			
	LWV-6146			
CA-DM-SUP-ICD-0027	CA-DM-SUP-ICD-0027-V-04			
	LWV-6147			
CA-DM-SUP-ICD-0024 LSE-130	CA-DM-SUP-ICD-0024-V-03			
	LWV-6152			
CA-DM-SUP-ICD-0024	CA-DM-SUP-ICD-0024-V-04			
	LWV-6153			
CA-DM-SUP-ICD-0023 LSE-130	CA-DM-SUP-ICD-0023-V-03			
	LWV-6158			
CA-DM-SUP-ICD-0023	CA-DM-SUP-ICD-0023-V-04			
	LWV-6159			
CA-DM-SUP-ICD-0025 LSE-130	CA-DM-SUP-ICD-0025-V-03			
	LWV-6164			
CA-DM-SUP-ICD-0025	CA-DM-SUP-ICD-0025-V-04			
	LWV-6165			
CA-DM-SUP-ICD-0022 LSE-130	CA-DM-SUP-ICD-0022-V-03			
	LWV-6170			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	CA-DM-SUP-ICD-0022-V-04 LWV-6171			
CA-DM-SUP-ICD-0021 LSE-130	CA-DM-SUP-ICD-0021-V-03 LWV-6176			
	CA-DM-SUP-ICD-0021-V-04 LWV-6177			
CA-DM-SUP-ICD-0028 LSE-130	CA-DM-SUP-ICD-0028-V-03 LWV-6182			
	CA-DM-SUP-ICD-0028-V-04 LWV-6183			
CA-DM-SUP-ICD-0029 LSE-130	CA-DM-SUP-ICD-0029-V-03 LWV-6188			
	CA-DM-SUP-ICD-0029-V-04 LWV-6189			
CA-DM-SUP-ICD-0031 LSE-130	CA-DM-SUP-ICD-0031-V-03 LWV-6194			
	CA-DM-SUP-ICD-0031-V-04 LWV-6195			
CA-DM-SUP-ICD-0030 LSE-130	CA-DM-SUP-ICD-0030-V-03 LWV-6200			
	CA-DM-SUP-ICD-0030-V-04 LWV-6201			
CA-DM-SUP-ICD-0008 LSE-130	CA-DM-SUP-ICD-0008-V-03 LWV-6206			
	CA-DM-SUP-ICD-0008-V-04 LWV-6207			
CA-DM-SUP-ICD-0007 LSE-130	CA-DM-SUP-ICD-0007-V-03 LWV-6212			
	CA-DM-SUP-ICD-0007-V-04 LWV-6213			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
CA-DM-SUP-ICD-0009 LSE-130	CA-DM-SUP-ICD-0009-V-03 LWV-6218			
	CA-DM-SUP-ICD-0009-V-04 LWV-6219			
CA-DM-SUP-ICD-0010 LSE-130	CA-DM-SUP-ICD-0010-V-03 LWV-6224			
	CA-DM-SUP-ICD-0010-V-04 LWV-6225			
CA-DM-SUP-ICD-0020 LSE-130	CA-DM-SUP-ICD-0020-V-03 LWV-6230			
	CA-DM-SUP-ICD-0020-V-04 LWV-6231			
CA-DM-SUP-ICD-0019 LSE-130	CA-DM-SUP-ICD-0019-V-03 LWV-6236			
	CA-DM-SUP-ICD-0019-V-04 LWV-6237			
CA-DM-SUP-ICD-0005 LSE-130	CA-DM-SUP-ICD-0005-V-03 LWV-6242			
	CA-DM-SUP-ICD-0005-V-04 LWV-6243			
CA-DM-SUP-ICD-0006 LSE-130	CA-DM-SUP-ICD-0006-V-03 LWV-6248			
	CA-DM-SUP-ICD-0006-V-04 LWV-6249			
CA-DM-SUP-ICD-0002 LSE-130	CA-DM-SUP-ICD-0002-V-03 LWV-6254			
	CA-DM-SUP-ICD-0002-V-04 LWV-6255			
CA-DM-SUP-ICD-0003 LSE-130	CA-DM-SUP-ICD-0003-V-03 LWV-6260			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	CA-DM-SUP-ICD-0003-V-04 LWV-6261			
CA-DM-SUP-ICD-0004 LSE-130	CA-DM-SUP-ICD-0004-V-03 LWV-6266			
	CA-DM-SUP-ICD-0004-V-04 LWV-6267			
CA-DM-SUP-ICD-0016 LSE-130	CA-DM-SUP-ICD-0016-V-03 LWV-6272			
	CA-DM-SUP-ICD-0016-V-04 LWV-6273			
CA-DM-SUP-ICD-0015 LSE-130	CA-DM-SUP-ICD-0015-V-03 LWV-6278			
	CA-DM-SUP-ICD-0015-V-04 LWV-6279			
CA-DM-SUP-ICD-0017 LSE-130	CA-DM-SUP-ICD-0017-V-03 LWV-6284			
	CA-DM-SUP-ICD-0017-V-04 LWV-6285			
CA-DM-SUP-ICD-0014 LSE-130	CA-DM-SUP-ICD-0014-V-03 LWV-6290			
	CA-DM-SUP-ICD-0014-V-04 LWV-6291			
CA-DM-SUP-ICD-0013 LSE-130	CA-DM-SUP-ICD-0013-V-03 LWV-6296			
	CA-DM-SUP-ICD-0013-V-04 LWV-6297			
CA-DM-SUP-ICD-0011 LSE-130	CA-DM-SUP-ICD-0011-V-03 LWV-6302			
	CA-DM-SUP-ICD-0011-V-04 LWV-6303			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
CA-DM-SUP-ICD-0012 LSE-130	CA-DM-SUP-ICD-0012-V-03 LWV-6308			
	CA-DM-SUP-ICD-0012-V-04 LWV-6309			
CA-DM-SUP-ICD-0018 LSE-130	CA-DM-SUP-ICD-0018-V-03 LWV-6314			
	CA-DM-SUP-ICD-0018-V-04 LWV-6315			
CA-DM-SUP-ICD-0001 LSE-130	CA-DM-SUP-ICD-0001-V-03 LWV-6320			
	CA-DM-SUP-ICD-0001-V-04 LWV-6321			
EP-DM-CON-ICD-0004 LSE-131	EP-DM-CON-ICD-0004-V-01 LWV-6324			
	EP-DM-CON-ICD-0004-V-02 LWV-6325			
EP-DM-CON-ICD-0021 LSE-131	EP-DM-CON-ICD-0021-V-01 LWV-6330			
	EP-DM-CON-ICD-0021-V-02 LWV-6331			
EP-DM-CON-ICD-0009 LSE-131	EP-DM-CON-ICD-0009-V-01 LWV-6342			
	EP-DM-CON-ICD-0009-V-02 LWV-6343			
EP-DM-CON-ICD-0034 LSE-131	EP-DM-CON-ICD-0034-V-01 LWV-6348			
	EP-DM-CON-ICD-0034-V-02 LWV-6349			
EP-DM-CON-ICD-0031 LSE-131	EP-DM-CON-ICD-0031-V-01 LWV-6360			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	EP-DM-CON-ICD-0031-V-02 LWV-6361			
EP-DM-CON-ICD-0019 LSE-131	EP-DM-CON-ICD-0019-V-01 LWV-6372			
	EP-DM-CON-ICD-0019-V-02 LWV-6373			
EP-DM-CON-ICD-0002 LSE-131	EP-DM-CON-ICD-0002-V-03 LWV-6378			
	EP-DM-CON-ICD-0002-V-04 LWV-6379			
EP-DM-CON-ICD-0033 LSE-131	EP-DM-CON-ICD-0033-V-01 LWV-6384			
	EP-DM-CON-ICD-0033-V-02 LWV-6385			
EP-DM-CON-ICD-0032 LSE-131	EP-DM-CON-ICD-0032-V-01 LWV-6390			
	EP-DM-CON-ICD-0032-V-02 LWV-6391			
EP-DM-CON-ICD-0020 LSE-131	EP-DM-CON-ICD-0020-V-03 LWV-6402			
	EP-DM-CON-ICD-0020-V-04 LWV-6403			
DM-TS-AUX-ICD-0020 LSE-140	DM-TS-AUX-ICD-0020-V-01 LWV-6420			
	DM-TS-AUX-ICD-0020-V-02 LWV-6421			
DM-TS-AUX-ICD-0029 LSE-140	DM-TS-AUX-ICD-0029-V-01 LWV-6426			
	DM-TS-AUX-ICD-0029-V-02 LWV-6427			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DM-TS-AUX-ICD-0027 LSE-140	DM-TS-AUX-ICD-0027-V-01 LVW-6432			
	DM-TS-AUX-ICD-0027-V-02 LVW-6433			
DM-TS-AUX-ICD-0022 LSE-140	DM-TS-AUX-ICD-0022-V-01 LVW-6438			
	DM-TS-AUX-ICD-0022-V-02 LVW-6439			
DM-TS-AUX-ICD-0023 LSE-140	DM-TS-AUX-ICD-0023-V-01 LVW-6444			
	DM-TS-AUX-ICD-0023-V-02 LVW-6445			
DM-TS-AUX-ICD-0021 LSE-140	DM-TS-AUX-ICD-0021-V-01 LVW-6450			
	DM-TS-AUX-ICD-0021-V-02 LVW-6451			
DM-TS-AUX-ICD-0025 LSE-140	DM-TS-AUX-ICD-0025-V-01 LVW-6456			
	DM-TS-AUX-ICD-0025-V-02 LVW-6457			
DM-TS-AUX-ICD-0026 LSE-140	DM-TS-AUX-ICD-0026-V-01 LVW-6462			
	DM-TS-AUX-ICD-0026-V-02 LVW-6463			
DM-TS-AUX-ICD-0024 LSE-140	DM-TS-AUX-ICD-0024-V-01 LVW-6468			
	DM-TS-AUX-ICD-0024-V-02 LVW-6469			
DM-TS-AUX-ICD-0037 LSE-140	DM-TS-AUX-ICD-0037-V-01 LVW-6474			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	DM-TS-AUX-ICD-0037-V-02 LVW-6475			
DM-TS-AUX-ICD-0002 LSE-140	DM-TS-AUX-ICD-0002-V-01 LVW-6480			
	DM-TS-AUX-ICD-0002-V-02 LVW-6481			
DM-TS-AUX-ICD-0001 LSE-140	DM-TS-AUX-ICD-0001-V-01 LVW-6486			
	DM-TS-AUX-ICD-0001-V-02 LVW-6487			
DM-TS-AUX-ICD-0007 LSE-140	DM-TS-AUX-ICD-0007-V-01 LVW-6492			
	DM-TS-AUX-ICD-0007-V-02 LVW-6493			
DM-TS-AUX-ICD-0008 LSE-140	DM-TS-AUX-ICD-0008-V-01 LVW-6498			
	DM-TS-AUX-ICD-0008-V-02 LVW-6499			
DM-TS-AUX-ICD-0010 LSE-140	DM-TS-AUX-ICD-0010-V-01 LVW-6504			
	DM-TS-AUX-ICD-0010-V-02 LVW-6505			
DM-TS-AUX-ICD-0009 LSE-140	DM-TS-AUX-ICD-0009-V-01 LVW-6510			
	DM-TS-AUX-ICD-0009-V-02 LVW-6511			
DM-TS-AUX-ICD-0011 LSE-140	DM-TS-AUX-ICD-0011-V-01 LVW-6516			
	DM-TS-AUX-ICD-0011-V-02 LVW-6517			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DM-TS-AUX-ICD-0006 LSE-140	DM-TS-AUX-ICD-0006-V-01			
	LVW-6522			
	DM-TS-AUX-ICD-0006-V-02			
	LVW-6523			
DM-TS-AUX-ICD-0004 LSE-140	DM-TS-AUX-ICD-0004-V-01			
	LVW-6528			
	DM-TS-AUX-ICD-0004-V-02			
	LVW-6529			
DM-TS-AUX-ICD-0003 LSE-140	DM-TS-AUX-ICD-0003-V-01			
	LVW-6534			
	DM-TS-AUX-ICD-0003-V-02			
	LVW-6535			
DM-TS-AUX-ICD-0034 LSE-140	DM-TS-AUX-ICD-0034-V-01			
	LVW-6540			
	DM-TS-AUX-ICD-0034-V-02			
	LVW-6541			
DM-TS-AUX-ICD-0036 LSE-140	DM-TS-AUX-ICD-0036-V-01			
	LVW-6546			
	DM-TS-AUX-ICD-0036-V-02			
	LVW-6547			
DM-TS-AUX-ICD-0019 LSE-140	DM-TS-AUX-ICD-0019-V-01			
	LVW-6552			
	DM-TS-AUX-ICD-0019-V-02			
	LVW-6553			
DM-TS-AUX-ICD-0018 LSE-140	DM-TS-AUX-ICD-0018-V-01			
	LVW-6558			
	DM-TS-AUX-ICD-0018-V-02			
	LVW-6559			
DM-TS-AUX-ICD-0014 LSE-140	DM-TS-AUX-ICD-0014-V-01			
	LVW-6564			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	DM-TS-AUX-ICD-0014-V-02 LVW-6565			
DM-TS-AUX-ICD-0012 LSE-140	DM-TS-AUX-ICD-0012-V-01 LVW-6570			
	DM-TS-AUX-ICD-0012-V-02 LVW-6571			
DM-TS-AUX-ICD-0028 LSE-140	DM-TS-AUX-ICD-0028-V-01 LVW-6576			
	DM-TS-AUX-ICD-0028-V-02 LVW-6577			
DM-TS-AUX-ICD-0030 LSE-140	DM-TS-AUX-ICD-0030-V-01 LVW-6582			
	DM-TS-AUX-ICD-0030-V-02 LVW-6583			
DM-TS-AUX-ICD-0031 LSE-140	DM-TS-AUX-ICD-0031-V-01 LVW-6588			
	DM-TS-AUX-ICD-0031-V-02 LVW-6589			
DM-TS-AUX-ICD-0035 LSE-140	DM-TS-AUX-ICD-0035-V-01 LVW-6594			
	DM-TS-AUX-ICD-0035-V-02 LVW-6595			
DM-TS-AUX-ICD-0033 LSE-140	DM-TS-AUX-ICD-0033-V-01 LVW-6600			
	DM-TS-AUX-ICD-0033-V-02 LVW-6601			
DM-TS-AUX-ICD-0032 LSE-140	DM-TS-AUX-ICD-0032-V-01 LVW-6606			
	DM-TS-AUX-ICD-0032-V-02 LVW-6607			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
EP-DM-CON-ICD-0036 LSE-131	EP-DM-CON-ICD-0036-V-01			
	LWV-6751			
EP-DM-CON-ICD-0036	EP-DM-CON-ICD-0036-V-02			
	LWV-6752			
EP-DM-CON-ICD-0035 LSE-131	EP-DM-CON-ICD-0035-V-01			
	LWV-6757			
EP-DM-CON-ICD-0035	EP-DM-CON-ICD-0035-V-02			
	LWV-6758			
EP-DM-CON-ICD-0037 LSE-131	EP-DM-CON-ICD-0037-V-01			
	LWV-6763			
EP-DM-CON-ICD-0037	EP-DM-CON-ICD-0037-V-02			
	LWV-6764			
SYS-ALL-COM-ICD-0047 LSE-70	SYS-ALL-COM-ICD-0047-V-06			
	LWV-6771			
SYS-ALL-COM-ICD-0047	SYS-ALL-COM-ICD-0047-V-07			
	LWV-6772			
SYS-ALL-COM-ICD-0048 LSE-70	SYS-ALL-COM-ICD-0048-V-06			
	LWV-6777			
SYS-ALL-COM-ICD-0048	SYS-ALL-COM-ICD-0048-V-07			
	LWV-6778			
SYS-ALL-COM-ICD-0043 LSE-70	SYS-ALL-COM-ICD-0043-V-06			
	LWV-6783			
SYS-ALL-COM-ICD-0043	SYS-ALL-COM-ICD-0043-V-07			
	LWV-6784			
SYS-ALL-COM-ICD-0046 LSE-70	SYS-ALL-COM-ICD-0046-V-06			
	LWV-6789			
SYS-ALL-COM-ICD-0046	SYS-ALL-COM-ICD-0046-V-07			
	LWV-6790			
SYS-ALL-COM-ICD-0044 LSE-70	SYS-ALL-COM-ICD-0044-V-06			
	LWV-6795			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	SYS-ALL-COM-ICD-0044-V-07 LVW-6796			
SYS-ALL-COM-ICD-0045 LSE-70	SYS-ALL-COM-ICD-0045-V-06 LVW-6801			
	SYS-ALL-COM-ICD-0045-V-07 LVW-6802			
SYS-ALL-COM-ICD-0042 LSE-70	SYS-ALL-COM-ICD-0042-V-06 LVW-6807			
	SYS-ALL-COM-ICD-0042-V-07 LVW-6808			
SYS-ALL-COM-ICD-0029 LSE-70	SYS-ALL-COM-ICD-0029-V-06 LVW-6813			
	SYS-ALL-COM-ICD-0029-V-07 LVW-6814			
SYS-ALL-COM-ICD-0028 LSE-70	SYS-ALL-COM-ICD-0028-V-06 LVW-6819			
	SYS-ALL-COM-ICD-0028-V-07 LVW-6820			
SYS-ALL-COM-ICD-0005 LSE-70	SYS-ALL-COM-ICD-0005-V-06 LVW-6825			
	SYS-ALL-COM-ICD-0005-V-07 LVW-6826			
SYS-ALL-COM-ICD-0030 LSE-70	SYS-ALL-COM-ICD-0030-V-06 LVW-6831			
	SYS-ALL-COM-ICD-0030-V-07 LVW-6832			
SYS-ALL-COM-ICD-0026 LSE-70	SYS-ALL-COM-ICD-0026-V-06 LVW-6837			
	SYS-ALL-COM-ICD-0026-V-07 LVW-6838			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
SYS-ALL-COM-ICD-0027 LSE-70	SYS-ALL-COM-ICD-0027-V-06 LVW-6843			
	SYS-ALL-COM-ICD-0027-V-07 LVW-6844			
SYS-ALL-COM-ICD-0050 LSE-70	SYS-ALL-COM-ICD-0050-V-06 LVW-6849			
	SYS-ALL-COM-ICD-0050-V-07 LVW-6850			
SYS-ALL-COM-ICD-0049 LSE-70	SYS-ALL-COM-ICD-0049-V-06 LVW-6855			
	SYS-ALL-COM-ICD-0049-V-07 LVW-6856			
SYS-ALL-COM-ICD-0031 LSE-70	SYS-ALL-COM-ICD-0031-V-06 LVW-6861			
	SYS-ALL-COM-ICD-0031-V-07 LVW-6862			
SYS-ALL-COM-ICD-0033 LSE-70	SYS-ALL-COM-ICD-0033-V-06 LVW-6867			
	SYS-ALL-COM-ICD-0033-V-07 LVW-6868			
SYS-ALL-COM-ICD-0035 LSE-70	SYS-ALL-COM-ICD-0035-V-06 LVW-6873			
	SYS-ALL-COM-ICD-0035-V-07 LVW-6874			
SYS-ALL-COM-ICD-0037 LSE-70	SYS-ALL-COM-ICD-0037-V-06 LVW-6879			
	SYS-ALL-COM-ICD-0037-V-07 LVW-6880			
SYS-ALL-COM-ICD-0040 LSE-70	SYS-ALL-COM-ICD-0040-V-06 LVW-6885			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	SYS-ALL-COM-ICD-0040-V-07 LVW-6886			
SYS-ALL-COM-ICD-0036 LSE-70	SYS-ALL-COM-ICD-0036-V-06 LVW-6891			
	SYS-ALL-COM-ICD-0036-V-07 LVW-6892			
SYS-ALL-COM-ICD-0041 LSE-70	SYS-ALL-COM-ICD-0041-V-06 LVW-6897			
	SYS-ALL-COM-ICD-0041-V-07 LVW-6898			
SYS-ALL-COM-ICD-0038 LSE-70	SYS-ALL-COM-ICD-0038-V-06 LVW-6903			
	SYS-ALL-COM-ICD-0038-V-07 LVW-6904			
SYS-ALL-COM-ICD-0034 LSE-70	SYS-ALL-COM-ICD-0034-V-06 LVW-6909			
	SYS-ALL-COM-ICD-0034-V-07 LVW-6910			
SYS-ALL-COM-ICD-0032 LSE-70	SYS-ALL-COM-ICD-0032-V-06 LVW-6915			
	SYS-ALL-COM-ICD-0032-V-07 LVW-6916			
SYS-ALL-COM-ICD-0039 LSE-70	SYS-ALL-COM-ICD-0039-V-06 LVW-6921			
	SYS-ALL-COM-ICD-0039-V-07 LVW-6922			
CPT-OCS-INT-ICD-0001 LSE-209	CPT-OCS-INT-ICD-0001-V-06 LVW-6927			
	CPT-OCS-INT-ICD-0001-V-07 LVW-6928			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
CPT-OCS-INT-ICD-0005 LSE-209	CPT-OCS-INT-ICD-0005-V-06			
	LWV-6933			
CPT-OCS-INT-ICD-0005	CPT-OCS-INT-ICD-0005-V-07			
	LWV-6934			
CPT-OCS-INT-ICD-0006 LSE-209	CPT-OCS-INT-ICD-0006-V-06			
	LWV-6939			
CPT-OCS-INT-ICD-0006	CPT-OCS-INT-ICD-0006-V-07			
	LWV-6940			
CPT-OCS-INT-ICD-0008 LSE-209	CPT-OCS-INT-ICD-0008-V-06			
	LWV-6945			
CPT-OCS-INT-ICD-0008	CPT-OCS-INT-ICD-0008-V-07			
	LWV-6946			
CPT-OCS-INT-ICD-0040 LSE-209	CPT-OCS-INT-ICD-0040-V-06			
	LWV-6951			
CPT-OCS-INT-ICD-0040	CPT-OCS-INT-ICD-0040-V-07			
	LWV-6952			
CPT-OCS-INT-ICD-0041 LSE-209	CPT-OCS-INT-ICD-0041-V-06			
	LWV-6957			
CPT-OCS-INT-ICD-0041	CPT-OCS-INT-ICD-0041-V-07			
	LWV-6958			
CPT-OCS-INT-ICD-0042 LSE-209	CPT-OCS-INT-ICD-0042-V-06			
	LWV-6963			
CPT-OCS-INT-ICD-0042	CPT-OCS-INT-ICD-0042-V-07			
	LWV-6964			
CPT-OCS-INT-ICD-0002 LSE-209	CPT-OCS-INT-ICD-0002-V-06			
	LWV-6969			
CPT-OCS-INT-ICD-0002	CPT-OCS-INT-ICD-0002-V-07			
	LWV-6970			
CPT-OCS-INT-ICD-0003 LSE-209	CPT-OCS-INT-ICD-0003-V-06			
	LWV-6975			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	CPT-OCS-INT-ICD-0003-V-07 LVW-6976			
CPT-OCS-INT-ICD-0009 LSE-209	CPT-OCS-INT-ICD-0009-V-06 LVW-6981			
	CPT-OCS-INT-ICD-0009-V-07 LVW-6982			
CPT-OCS-INT-ICD-0072 LSE-209	CPT-OCS-INT-ICD-0072-V-06 LVW-6987			
	CPT-OCS-INT-ICD-0072-V-07 LVW-6988			
CPT-OCS-INT-ICD-0010 LSE-209	CPT-OCS-INT-ICD-0010-V-06 LVW-6993			
	CPT-OCS-INT-ICD-0010-V-07 LVW-6994			
CPT-OCS-INT-ICD-0012 LSE-209	CPT-OCS-INT-ICD-0012-V-06 LVW-6999			
	CPT-OCS-INT-ICD-0012-V-07 LVW-7000			
CPT-OCS-INT-ICD-0004 LSE-209	CPT-OCS-INT-ICD-0004-V-06 LVW-7005			
	CPT-OCS-INT-ICD-0004-V-07 LVW-7006			
CPT-OCS-INT-ICD-0007 LSE-209	CPT-OCS-INT-ICD-0007-V-06 LVW-7011			
	CPT-OCS-INT-ICD-0007-V-07 LVW-7012			
CPT-OCS-INT-ICD-0011 LSE-209	CPT-OCS-INT-ICD-0011-V-06 LVW-7017			
	CPT-OCS-INT-ICD-0011-V-07 LVW-7018			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
CPT-OCS-INT-ICD-0049 LSE-209	CPT-OCS-INT-ICD-0049-V-06			
	LWV-7023			
CPT-OCS-INT-ICD-0049	CPT-OCS-INT-ICD-0049-V-07			
	LWV-7024			
CPT-OCS-INT-ICD-0071 LSE-209	CPT-OCS-INT-ICD-0071-V-06			
	LWV-7029			
CPT-OCS-INT-ICD-0071	CPT-OCS-INT-ICD-0071-V-07			
	LWV-7030			
CPT-OCS-INT-ICD-0046 LSE-209	CPT-OCS-INT-ICD-0046-V-06			
	LWV-7035			
CPT-OCS-INT-ICD-0046	CPT-OCS-INT-ICD-0046-V-07			
	LWV-7036			
CPT-OCS-INT-ICD-0045 LSE-209	CPT-OCS-INT-ICD-0045-V-06			
	LWV-7041			
CPT-OCS-INT-ICD-0045	CPT-OCS-INT-ICD-0045-V-07			
	LWV-7042			
CPT-OCS-INT-ICD-0048 LSE-209	CPT-OCS-INT-ICD-0048-V-06			
	LWV-7047			
CPT-OCS-INT-ICD-0048	CPT-OCS-INT-ICD-0048-V-07			
	LWV-7048			
CPT-OCS-INT-ICD-0043 LSE-209	CPT-OCS-INT-ICD-0043-V-06			
	LWV-7053			
CPT-OCS-INT-ICD-0043	CPT-OCS-INT-ICD-0043-V-07			
	LWV-7054			
CPT-OCS-INT-ICD-0044 LSE-209	CPT-OCS-INT-ICD-0044-V-06			
	LWV-7059			
CPT-OCS-INT-ICD-0044	CPT-OCS-INT-ICD-0044-V-07			
	LWV-7060			
CPT-OCS-INT-ICD-0047 LSE-209	CPT-OCS-INT-ICD-0047-V-06			
	LWV-7065			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	CPT-OCS-INT-ICD-0047-V-07 LVW-7066			
CPT-OCS-INT-ICD-0061 LSE-209	CPT-OCS-INT-ICD-0061-V-06 LVW-7071			
	CPT-OCS-INT-ICD-0061-V-07 LVW-7072			
CPT-OCS-INT-ICD-0057 LSE-209	CPT-OCS-INT-ICD-0057-V-06 LVW-7077			
	CPT-OCS-INT-ICD-0057-V-07 LVW-7078			
CPT-OCS-INT-ICD-0052 LSE-209	CPT-OCS-INT-ICD-0052-V-06 LVW-7083			
	CPT-OCS-INT-ICD-0052-V-07 LVW-7084			
CPT-OCS-INT-ICD-0050 LSE-209	CPT-OCS-INT-ICD-0050-V-06 LVW-7089			
	CPT-OCS-INT-ICD-0050-V-07 LVW-7090			
CPT-OCS-INT-ICD-0053 LSE-209	CPT-OCS-INT-ICD-0053-V-06 LVW-7095			
	CPT-OCS-INT-ICD-0053-V-07 LVW-7096			
CPT-OCS-INT-ICD-0054 LSE-209	CPT-OCS-INT-ICD-0054-V-06 LVW-7101			
	CPT-OCS-INT-ICD-0054-V-07 LVW-7102			
CPT-OCS-INT-ICD-0055 LSE-209	CPT-OCS-INT-ICD-0055-V-06 LVW-7107			
	CPT-OCS-INT-ICD-0055-V-07 LVW-7108			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
CPT-OCS-INT-ICD-0051 LSE-209	CPT-OCS-INT-ICD-0051-V-06			
	LWV-7113			
CPT-OCS-INT-ICD-0051	CPT-OCS-INT-ICD-0051-V-07			
	LWV-7114			
CPT-OCS-INT-ICD-0073 LSE-209	CPT-OCS-INT-ICD-0073-V-06			
	LWV-7119			
CPT-OCS-INT-ICD-0073	CPT-OCS-INT-ICD-0073-V-07			
	LWV-7120			
CPT-OCS-INT-ICD-0058 LSE-209	CPT-OCS-INT-ICD-0058-V-06			
	LWV-7125			
CPT-OCS-INT-ICD-0058	CPT-OCS-INT-ICD-0058-V-07			
	LWV-7126			
CPT-OCS-INT-ICD-0059 LSE-209	CPT-OCS-INT-ICD-0059-V-06			
	LWV-7131			
CPT-OCS-INT-ICD-0059	CPT-OCS-INT-ICD-0059-V-07			
	LWV-7132			
CPT-OCS-INT-ICD-0060 LSE-209	CPT-OCS-INT-ICD-0060-V-06			
	LWV-7137			
CPT-OCS-INT-ICD-0060	CPT-OCS-INT-ICD-0060-V-07			
	LWV-7138			
CPT-OCS-INT-ICD-0056 LSE-209	CPT-OCS-INT-ICD-0056-V-06			
	LWV-7143			
CPT-OCS-INT-ICD-0056	CPT-OCS-INT-ICD-0056-V-07			
	LWV-7144			
CPT-OCS-INT-ICD-0063 LSE-209	CPT-OCS-INT-ICD-0063-V-06			
	LWV-7149			
CPT-OCS-INT-ICD-0063	CPT-OCS-INT-ICD-0063-V-07			
	LWV-7150			
CPT-OCS-INT-ICD-0064 LSE-209	CPT-OCS-INT-ICD-0064-V-06			
	LWV-7155			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
	CPT-OCS-INT-ICD-0064-V-07 LWV-7156			
CPT-OCS-INT-ICD-0065 LSE-209	CPT-OCS-INT-ICD-0065-V-06 LWV-7161			
	CPT-OCS-INT-ICD-0065-V-07 LWV-7162			
CPT-OCS-INT-ICD-0066 LSE-209	CPT-OCS-INT-ICD-0066-V-06 LWV-7167			
	CPT-OCS-INT-ICD-0066-V-07 LWV-7168			
CPT-OCS-INT-ICD-0062 LSE-209	CPT-OCS-INT-ICD-0062-V-06 LWV-7173			
	CPT-OCS-INT-ICD-0062-V-07 LWV-7174			
CPT-OCS-INT-ICD-0067 LSE-209	CPT-OCS-INT-ICD-0067-V-06 LWV-7179			
	CPT-OCS-INT-ICD-0067-V-07 LWV-7180			
CPT-OCS-INT-ICD-0068 LSE-209	CPT-OCS-INT-ICD-0068-V-06 LWV-7185			
	CPT-OCS-INT-ICD-0068-V-07 LWV-7186			
CPT-OCS-INT-ICD-0069 LSE-209	CPT-OCS-INT-ICD-0069-V-06 LWV-7191			
	CPT-OCS-INT-ICD-0069-V-07 LWV-7192			
CPT-OCS-INT-ICD-0070 LSE-209	CPT-OCS-INT-ICD-0070-V-06 LWV-7197			
	CPT-OCS-INT-ICD-0070-V-07 LWV-7198			

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0372 LSE-61	DMS-REQ-0372-V-01 LVV-9637	LVV-T1264 LDM-639		Not Executed
DMS-REQ-0344 LSE-61	DMS-REQ-0344-V-02 LVV-9744			
DMS-LSP-REQ-0007 LDM-554	DMS-LSP-REQ-0007-V-01 LVV-9806	LVV-T605 LDM-540		Not Executed
DMS-LSP-REQ-0001 LDM-554	DMS-LSP-REQ-0001-V-01 LVV-9807	LVV-T2 LDM-540	2019-05-20 DMTR-52 LVV-P42	Passed
		LVV-T598 LDM-540		Not Executed
DMS-LSP-REQ-0004 LDM-554	DMS-LSP-REQ-0004-V-01 LVV-9808	LVV-T3 LDM-540	2019-05-20 DMTR-52 LVV-P42	Passed
		LVV-T602 LDM-540		Not Executed
DMS-LSP-REQ-0005 LDM-554	DMS-LSP-REQ-0005-V-01 LVV-9809	LVV-T2 LDM-540	2019-05-20 DMTR-52 LVV-P42	Passed
		LVV-T603 LDM-540		Not Executed
DMS-LSP-REQ-0003 LDM-554	DMS-LSP-REQ-0003-V-01 LVV-9810	LVV-T601 LDM-540		Not Executed
DMS-LSP-REQ-0002 LDM-554	DMS-LSP-REQ-0002-V-01 LVV-9811	LVV-T5 LDM-540	2019-05-20 DMTR-52 LVV-P42	Passed
		LVV-T600 LDM-540		Not Executed
DMS-LSP-REQ-0006 LDM-554	DMS-LSP-REQ-0006-V-01 LVV-9812	LVV-T604 LDM-540		Not Executed
DMS-LSP-REQ-0009 LDM-554	DMS-LSP-REQ-0009-V-01 LVV-9813	LVV-T607 LDM-540		Not Executed
DMS-LSP-REQ-0008 LDM-554	DMS-LSP-REQ-0008-V-01 LVV-9814	LVV-T8 LDM-540		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T9 LDM-540	2019-05-20 DMTR-52 LVV-P42	Passed
		LVV-T606 LDM-540		Not Executed
DMS-LSP-REQ-0010 LDM-554	DMS-LSP-REQ-0010-V-01 LVV-9815	LVV-T608 LDM-540		Not Executed
DMS-LSP-REQ-0012 LDM-554	DMS-LSP-REQ-0012-V-01 LVV-9816	LVV-T610 LDM-540		Not Executed
DMS-LSP-REQ-0011 LDM-554	DMS-LSP-REQ-0011-V-01 LVV-9817	LVV-T609 LDM-540		Not Executed
DMS-LSP-REQ-0013 LDM-554	DMS-LSP-REQ-0013-V-01 LVV-9818	LVV-T611 LDM-540		Not Executed
DMS-LSP-REQ-0014 LDM-554	DMS-LSP-REQ-0014-V-01 LVV-9819	LVV-T5 LDM-540	2019-05-20 DMTR-52 LVV-P42	Passed
		LVV-T6 LDM-540	2019-05-20 DMTR-52 LVV-P42	Conditional Pass
		LVV-T7 LDM-540		Not Executed
		LVV-T612 LDM-540		Not Executed
DMS-LSP-REQ-0018 LDM-554	DMS-LSP-REQ-0018-V-01 LVV-9820	LVV-T7 LDM-540		Not Executed
		LVV-T616 LDM-540		Not Executed
DMS-LSP-REQ-0017 LDM-554	DMS-LSP-REQ-0017-V-01 LVV-9821	LVV-T6 LDM-540	2019-05-20 DMTR-52 LVV-P42	Conditional Pass
		LVV-T615 LDM-540		Not Executed
DMS-LSP-REQ-0016 LDM-554	DMS-LSP-REQ-0016-V-01 LVV-9822	LVV-T614 LDM-540		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-LSP-REQ-0015 LDM-554	DMS-LSP-REQ-0015-V-01 LVV-9823	LVV-T613 LDM-540		Not Executed
DMS-LSP-REQ-0028 LDM-554	DMS-LSP-REQ-0028-V-01 LVV-9824	LVV-T4 LDM-540	2019-05-20 DMTR-52 LVV-P42	Passed
		LVV-T617 LDM-540		Not Executed
DMS-LSP-REQ-0029 LDM-554	DMS-LSP-REQ-0029-V-01 LVV-9825	LVV-T4 LDM-540	2019-05-20 DMTR-52 LVV-P42	Passed
		LVV-T618 LDM-540		Not Executed
DMS-LSP-REQ-0030 LDM-554	DMS-LSP-REQ-0030-V-01 LVV-9826	LVV-T619 LDM-540		Not Executed
DMS-LSP-REQ-0031 LDM-554	DMS-LSP-REQ-0031-V-01 LVV-9827	LVV-T620 LDM-540		Not Executed
DMS-LSP-REQ-0019 LDM-554	DMS-LSP-REQ-0019-V-01 LVV-9828	LVV-T621 LDM-540		Not Executed
DMS-LSP-REQ-0025 LDM-554	DMS-LSP-REQ-0025-V-01 LVV-9829	LVV-T627 LDM-540		Not Executed
DMS-LSP-REQ-0020 LDM-554	DMS-LSP-REQ-0020-V-01 LVV-9830	LVV-T622 LDM-540		Not Executed
DMS-LSP-REQ-0022 LDM-554	DMS-LSP-REQ-0022-V-01 LVV-9831	LVV-T624 LDM-540		Not Executed
DMS-LSP-REQ-0021 LDM-554	DMS-LSP-REQ-0021-V-01 LVV-9832	LVV-T623 LDM-540		Not Executed
DMS-LSP-REQ-0027 LDM-554	DMS-LSP-REQ-0027-V-01 LVV-9833	LVV-T629 LDM-540		Not Executed
DMS-LSP-REQ-0023 LDM-554	DMS-LSP-REQ-0023-V-01 LVV-9834	LVV-T625 LDM-540		Not Executed
DMS-LSP-REQ-0024 LDM-554	DMS-LSP-REQ-0024-V-01 LVV-9835	LVV-T626 LDM-540		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-LSP-REQ-0026 LDM-554	DMS-LSP-REQ-0026-V-01 LVV-9836	LVV-T628 LDM-540		Not Executed
DMS-LSP-REQ-0033 LDM-554	DMS-LSP-REQ-0033-V-01 LVV-9837	LVV-T631 LDM-540		Not Executed
DMS-LSP-REQ-0034 LDM-554	DMS-LSP-REQ-0034-V-01 LVV-9838	LVV-T632 LDM-540		Not Executed
DMS-LSP-REQ-0032 LDM-554	DMS-LSP-REQ-0032-V-01 LVV-9839	LVV-T630 LDM-540		Not Executed
DMS-LSP-REQ-0035 LDM-554	DMS-LSP-REQ-0035-V-01 LVV-9840	LVV-T633 LDM-540		Not Executed
DMS-PRTL-REQ-0001 LDM-554	DMS-PRTL-REQ-0001-V-01 LVV-9841	LVV-T634 LDM-540		Not Executed
DMS-PRTL-REQ-0005 LDM-554	DMS-PRTL-REQ-0005-V-01 LVV-9842	LVV-T638 LDM-540		Not Executed
DMS-PRTL-REQ-0007 LDM-554	DMS-PRTL-REQ-0007-V-01 LVV-9843	LVV-T640 LDM-540		Not Executed
DMS-PRTL-REQ-0008 LDM-554	DMS-PRTL-REQ-0008-V-01 LVV-9844	LVV-T641 LDM-540		Not Executed
DMS-PRTL-REQ-0006 LDM-554	DMS-PRTL-REQ-0006-V-01 LVV-9845	LVV-T639 LDM-540		Not Executed
DMS-PRTL-REQ-0003 LDM-554	DMS-PRTL-REQ-0003-V-01 LVV-9846	LVV-T636 LDM-540		Not Executed
DMS-PRTL-REQ-0002 LDM-554	DMS-PRTL-REQ-0002-V-01 LVV-9847	LVV-T635 LDM-540		Not Executed
DMS-PRTL-REQ-0004 LDM-554	DMS-PRTL-REQ-0004-V-01 LVV-9848	LVV-T8 LDM-540		Not Executed
		LVV-T637 LDM-540		Not Executed
DMS-PRTL-REQ-0010 LDM-554	DMS-PRTL-REQ-0010-V-01 LVV-9849	LVV-T643 LDM-540		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-PRTL-REQ-0013 LDM-554	DMS-PRTL-REQ-0013-V-01 LVV-9850	LVV-T646 LDM-540		Not Executed
DMS-PRTL-REQ-0012 LDM-554	DMS-PRTL-REQ-0012-V-01 LVV-9851	LVV-T645 LDM-540		Not Executed
DMS-PRTL-REQ-0014 LDM-554	DMS-PRTL-REQ-0014-V-01 LVV-9852	LVV-T647 LDM-540		Not Executed
DMS-PRTL-REQ-0011 LDM-554	DMS-PRTL-REQ-0011-V-01 LVV-9853	LVV-T644 LDM-540		Not Executed
DMS-PRTL-REQ-0009 LDM-554	DMS-PRTL-REQ-0009-V-01 LVV-9854	LVV-T642 LDM-540		Not Executed
DMS-PRTL-REQ-0017 LDM-554	DMS-PRTL-REQ-0017-V-01 LVV-9855	LVV-T650 LDM-540		Not Executed
DMS-PRTL-REQ-0016 LDM-554	DMS-PRTL-REQ-0016-V-01 LVV-9856	LVV-T5 LDM-540	2019-05-20 DMTR-52 LVV-P42	Passed
		LVV-T649 LDM-540		Not Executed
DMS-PRTL-REQ-0015 LDM-554	DMS-PRTL-REQ-0015-V-01 LVV-9857	LVV-T648 LDM-540		Not Executed
DMS-PRTL-REQ-0018 LDM-554	DMS-PRTL-REQ-0018-V-01 LVV-9858	LVV-T651 LDM-540		Not Executed
DMS-PRTL-REQ-0028 LDM-554	DMS-PRTL-REQ-0028-V-01 LVV-9859	LVV-T5 LDM-540	2019-05-20 DMTR-52 LVV-P42	Passed
		LVV-T652 LDM-540		Not Executed
DMS-PRTL-REQ-0029 LDM-554	DMS-PRTL-REQ-0029-V-01 LVV-9860	LVV-T653 LDM-540		Not Executed
DMS-PRTL-REQ-0030 LDM-554	DMS-PRTL-REQ-0030-V-01 LVV-9861	LVV-T654 LDM-540		Not Executed
DMS-PRTL-REQ-0022 LDM-554	DMS-PRTL-REQ-0022-V-01 LVV-9862	LVV-T5 LDM-540	2019-05-20 DMTR-52 LVV-P42	Passed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T657 LDM-540		Not Executed
DMS-PRTL-REQ-0023 LDM-554	DMS-PRTL-REQ-0023-V-01 LVV-9863	LVV-T658 LDM-540		Not Executed
DMS-PRTL-REQ-0024 LDM-554	DMS-PRTL-REQ-0024-V-01 LVV-9864	LVV-T659 LDM-540		Not Executed
DMS-PRTL-REQ-0021 LDM-554	DMS-PRTL-REQ-0021-V-01 LVV-9865	LVV-T5 LDM-540	2019-05-20 DMTR-52 LVV-P42	Passed
		LVV-T656 LDM-540		Not Executed
DMS-PRTL-REQ-0020 LDM-554	DMS-PRTL-REQ-0020-V-01 LVV-9866	LVV-T655 LDM-540		Not Executed
DMS-PRTL-REQ-0025 LDM-554	DMS-PRTL-REQ-0025-V-01 LVV-9867	LVV-T660 LDM-540		Not Executed
DMS-PRTL-REQ-0027 LDM-554	DMS-PRTL-REQ-0027-V-01 LVV-9868	LVV-T5 LDM-540	2019-05-20 DMTR-52 LVV-P42	Passed
		LVV-T662 LDM-540		Not Executed
DMS-PRTL-REQ-0026 LDM-554	DMS-PRTL-REQ-0026-V-01 LVV-9869	LVV-T5 LDM-540	2019-05-20 DMTR-52 LVV-P42	Passed
		LVV-T661 LDM-540		Not Executed
DMS-PRTL-REQ-0019 LDM-554	DMS-PRTL-REQ-0019-V-01 LVV-9870	LVV-T663 LDM-540		Not Executed
DMS-PRTL-REQ-0034 LDM-554	DMS-PRTL-REQ-0034-V-01 LVV-9871	LVV-T668 LDM-540		Not Executed
DMS-PRTL-REQ-0033 LDM-554	DMS-PRTL-REQ-0033-V-01 LVV-9872	LVV-T667 LDM-540		Not Executed
DMS-PRTL-REQ-0032 LDM-554	DMS-PRTL-REQ-0032-V-01 LVV-9873	LVV-T666 LDM-540		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-PRTL-REQ-0031 LDM-554	DMS-PRTL-REQ-0031-V-01 LVV-9874	LVV-T664 LDM-540		Not Executed
DMS-PRTL-REQ-0039 LDM-554	DMS-PRTL-REQ-0039-V-01 LVV-9875	LVV-T673 LDM-540		Not Executed
DMS-PRTL-REQ-0037 LDM-554	DMS-PRTL-REQ-0037-V-01 LVV-9876	LVV-T671 LDM-540		Not Executed
DMS-PRTL-REQ-0036 LDM-554	DMS-PRTL-REQ-0036-V-01 LVV-9877	LVV-T670 LDM-540		Not Executed
DMS-PRTL-REQ-0035 LDM-554	DMS-PRTL-REQ-0035-V-01 LVV-9878	LVV-T669 LDM-540		Not Executed
DMS-PRTL-REQ-0038 LDM-554	DMS-PRTL-REQ-0038-V-01 LVV-9879	LVV-T672 LDM-540		Not Executed
DMS-PRTL-REQ-0041 LDM-554	DMS-PRTL-REQ-0041-V-01 LVV-9880	LVV-T7 LDM-540		Not Executed
		LVV-T674 LDM-540		Not Executed
DMS-PRTL-REQ-0040 LDM-554	DMS-PRTL-REQ-0040-V-01 LVV-9881	LVV-T7 LDM-540		Not Executed
		LVV-T675 LDM-540		Not Executed
DMS-PRTL-REQ-0044 LDM-554	DMS-PRTL-REQ-0044-V-01 LVV-9882	LVV-T679 LDM-540		Not Executed
DMS-PRTL-REQ-0043 LDM-554	DMS-PRTL-REQ-0043-V-01 LVV-9883	LVV-T678 LDM-540		Not Executed
DMS-PRTL-REQ-0042 LDM-554	DMS-PRTL-REQ-0042-V-01 LVV-9884	LVV-T677 LDM-540		Not Executed
DMS-PRTL-REQ-0045 LDM-554	DMS-PRTL-REQ-0045-V-01 LVV-9885	LVV-T680 LDM-540		Not Executed
DMS-PRTL-REQ-0046 LDM-554	DMS-PRTL-REQ-0046-V-01 LVV-9886	LVV-T681 LDM-540		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-PRTL-REQ-0048 LDM-554	DMS-PRTL-REQ-0048-V-01 LVV-9887	LVV-T683 LDM-540		Not Executed
DMS-PRTL-REQ-0047 LDM-554	DMS-PRTL-REQ-0047-V-01 LVV-9888	LVV-T682 LDM-540		Not Executed
DMS-PRTL-REQ-0050 LDM-554	DMS-PRTL-REQ-0050-V-01 LVV-9889	LVV-T6 LDM-540	2019-05-20 DMTR-52 LVV-P42	Conditional Pass
		LVV-T685 LDM-540		Not Executed
DMS-PRTL-REQ-0052 LDM-554	DMS-PRTL-REQ-0052-V-01 LVV-9890	LVV-T687 LDM-540		Not Executed
DMS-PRTL-REQ-0049 LDM-554	DMS-PRTL-REQ-0049-V-01 LVV-9891	LVV-T6 LDM-540	2019-05-20 DMTR-52 LVV-P42	Conditional Pass
		LVV-T684 LDM-540		Not Executed
DMS-PRTL-REQ-0051 LDM-554	DMS-PRTL-REQ-0051-V-01 LVV-9892	LVV-T686 LDM-540		Not Executed
DMS-PRTL-REQ-0054 LDM-554	DMS-PRTL-REQ-0054-V-01 LVV-9893	LVV-T6 LDM-540	2019-05-20 DMTR-52 LVV-P42	Conditional Pass
		LVV-T689 LDM-540		Not Executed
DMS-PRTL-REQ-0053 LDM-554	DMS-PRTL-REQ-0053-V-01 LVV-9894	LVV-T6 LDM-540	2019-05-20 DMTR-52 LVV-P42	Conditional Pass
		LVV-T688 LDM-540		Not Executed
DMS-PRTL-REQ-0056 LDM-554	DMS-PRTL-REQ-0056-V-01 LVV-9895	LVV-T6 LDM-540	2019-05-20 DMTR-52 LVV-P42	Conditional Pass
		LVV-T691 LDM-540		Not Executed
DMS-PRTL-REQ-0061 LDM-554	DMS-PRTL-REQ-0061-V-01 LVV-9896	LVV-T696 LDM-540		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-PRTL-REQ-0059 LDM-554	DMS-PRTL-REQ-0059-V-01 LVV-9897	LVV-T694 LDM-540		Not Executed
DMS-PRTL-REQ-0058 LDM-554	DMS-PRTL-REQ-0058-V-01 LVV-9898	LVV-T693 LDM-540		Not Executed
DMS-PRTL-REQ-0060 LDM-554	DMS-PRTL-REQ-0060-V-01 LVV-9899	LVV-T695 LDM-540		Not Executed
DMS-PRTL-REQ-0057 LDM-554	DMS-PRTL-REQ-0057-V-01 LVV-9900	LVV-T692 LDM-540		Not Executed
DMS-PRTL-REQ-0055 LDM-554	DMS-PRTL-REQ-0055-V-01 LVV-9901	LVV-T6 LDM-540	2019-05-20 DMTR-52 LVV-P42	Conditional Pass
		LVV-T690 LDM-540		Not Executed
DMS-PRTL-REQ-0067 LDM-554	DMS-PRTL-REQ-0067-V-01 LVV-9902	LVV-T701 LDM-540		Not Executed
DMS-PRTL-REQ-0066 LDM-554	DMS-PRTL-REQ-0066-V-01 LVV-9903	LVV-T700 LDM-540		Not Executed
DMS-PRTL-REQ-0065 LDM-554	DMS-PRTL-REQ-0065-V-01 LVV-9904	LVV-T699 LDM-540		Not Executed
DMS-PRTL-REQ-0062 LDM-554	DMS-PRTL-REQ-0062-V-01 LVV-9905	LVV-T676 LDM-540		Not Executed
DMS-PRTL-REQ-0063 LDM-554	DMS-PRTL-REQ-0063-V-01 LVV-9906	LVV-T697 LDM-540		Not Executed
DMS-PRTL-REQ-0064 LDM-554	DMS-PRTL-REQ-0064-V-01 LVV-9907	LVV-T698 LDM-540		Not Executed
DMS-PRTL-REQ-0068 LDM-554	DMS-PRTL-REQ-0068-V-01 LVV-9908	LVV-T702 LDM-540		Not Executed
DMS-PRTL-REQ-0069 LDM-554	DMS-PRTL-REQ-0069-V-01 LVV-9909	LVV-T703 LDM-540		Not Executed
DMS-PRTL-REQ-0074 LDM-554	DMS-PRTL-REQ-0074-V-01 LVV-9910	LVV-T708 LDM-540		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-PRTL-REQ-0071 LDM-554	DMS-PRTL-REQ-0071-V-01 LVV-9911	LVV-T705 LDM-540		Not Executed
DMS-PRTL-REQ-0072 LDM-554	DMS-PRTL-REQ-0072-V-01 LVV-9912	LVV-T706 LDM-540		Not Executed
DMS-PRTL-REQ-0073 LDM-554	DMS-PRTL-REQ-0073-V-01 LVV-9913	LVV-T707 LDM-540		Not Executed
DMS-PRTL-REQ-0070 LDM-554	DMS-PRTL-REQ-0070-V-01 LVV-9914	LVV-T704 LDM-540		Not Executed
DMS-PRTL-REQ-0075 LDM-554	DMS-PRTL-REQ-0075-V-01 LVV-9915	LVV-T709 LDM-540		Not Executed
DMS-PRTL-REQ-0077 LDM-554	DMS-PRTL-REQ-0077-V-01 LVV-9916	LVV-T711 LDM-540		Not Executed
DMS-PRTL-REQ-0076 LDM-554	DMS-PRTL-REQ-0076-V-01 LVV-9917	LVV-T710 LDM-540		Not Executed
DMS-PRTL-REQ-0078 LDM-554	DMS-PRTL-REQ-0078-V-01 LVV-9918	LVV-T712 LDM-540		Not Executed
DMS-PRTL-REQ-0081 LDM-554	DMS-PRTL-REQ-0081-V-01 LVV-9919	LVV-T715 LDM-540		Not Executed
DMS-PRTL-REQ-0080 LDM-554	DMS-PRTL-REQ-0080-V-01 LVV-9920	LVV-T714 LDM-540		Not Executed
DMS-PRTL-REQ-0082 LDM-554	DMS-PRTL-REQ-0082-V-01 LVV-9921	LVV-T716 LDM-540		Not Executed
DMS-PRTL-REQ-0079 LDM-554	DMS-PRTL-REQ-0079-V-01 LVV-9922	LVV-T713 LDM-540		Not Executed
DMS-PRTL-REQ-0087 LDM-554	DMS-PRTL-REQ-0087-V-01 LVV-9923	LVV-T721 LDM-540		Not Executed
DMS-PRTL-REQ-0083 LDM-554	DMS-PRTL-REQ-0083-V-01 LVV-9924	LVV-T717 LDM-540		Not Executed
DMS-PRTL-REQ-0086 LDM-554	DMS-PRTL-REQ-0086-V-01 LVV-9925	LVV-T720 LDM-540		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-PRTL-REQ-0085 LDM-554	DMS-PRTL-REQ-0085-V-01 LVV-9926	LVV-T719 LDM-540		Not Executed
DMS-PRTL-REQ-0088 LDM-554	DMS-PRTL-REQ-0088-V-01 LVV-9927	LVV-T722 LDM-540		Not Executed
DMS-PRTL-REQ-0084 LDM-554	DMS-PRTL-REQ-0084-V-01 LVV-9928	LVV-T718 LDM-540		Not Executed
DMS-PRTL-REQ-0091 LDM-554	DMS-PRTL-REQ-0091-V-01 LVV-9929	LVV-T725 LDM-540		Not Executed
DMS-PRTL-REQ-0093 LDM-554	DMS-PRTL-REQ-0093-V-01 LVV-9930	LVV-T727 LDM-540		Not Executed
DMS-PRTL-REQ-0092 LDM-554	DMS-PRTL-REQ-0092-V-01 LVV-9931	LVV-T726 LDM-540		Not Executed
DMS-PRTL-REQ-0095 LDM-554	DMS-PRTL-REQ-0095-V-01 LVV-9932	LVV-T729 LDM-540		Not Executed
DMS-PRTL-REQ-0090 LDM-554	DMS-PRTL-REQ-0090-V-01 LVV-9933	LVV-T724 LDM-540		Not Executed
DMS-PRTL-REQ-0089 LDM-554	DMS-PRTL-REQ-0089-V-01 LVV-9934	LVV-T723 LDM-540		Not Executed
DMS-PRTL-REQ-0094 LDM-554	DMS-PRTL-REQ-0094-V-01 LVV-9935	LVV-T728 LDM-540		Not Executed
DMS-PRTL-REQ-0096 LDM-554	DMS-PRTL-REQ-0096-V-01 LVV-9936	LVV-T730 LDM-540		Not Executed
DMS-PRTL-REQ-0097 LDM-554	DMS-PRTL-REQ-0097-V-01 LVV-9937	LVV-T731 LDM-540		Not Executed
DMS-PRTL-REQ-0105 LDM-554	DMS-PRTL-REQ-0105-V-01 LVV-9938	LVV-T739 LDM-540		Not Executed
DMS-PRTL-REQ-0107 LDM-554	DMS-PRTL-REQ-0107-V-01 LVV-9939	LVV-T741 LDM-540		Not Executed
DMS-PRTL-REQ-0102 LDM-554	DMS-PRTL-REQ-0102-V-01 LVV-9940	LVV-T736 LDM-540		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-PRTL-REQ-0106 LDM-554	DMS-PRTL-REQ-0106-V-01 LVV-9941	LVV-T740 LDM-540		Not Executed
DMS-PRTL-REQ-0098 LDM-554	DMS-PRTL-REQ-0098-V-01 LVV-9942	LVV-T732 LDM-540		Not Executed
DMS-PRTL-REQ-0099 LDM-554	DMS-PRTL-REQ-0099-V-01 LVV-9943	LVV-T733 LDM-540		Not Executed
DMS-PRTL-REQ-0100 LDM-554	DMS-PRTL-REQ-0100-V-01 LVV-9944	LVV-T734 LDM-540		Not Executed
DMS-PRTL-REQ-0101 LDM-554	DMS-PRTL-REQ-0101-V-01 LVV-9945	LVV-T735 LDM-540		Not Executed
DMS-PRTL-REQ-0104 LDM-554	DMS-PRTL-REQ-0104-V-01 LVV-9946	LVV-T738 LDM-540		Not Executed
DMS-PRTL-REQ-0108 LDM-554	DMS-PRTL-REQ-0108-V-01 LVV-9947	LVV-T742 LDM-540		Not Executed
DMS-PRTL-REQ-0103 LDM-554	DMS-PRTL-REQ-0103-V-01 LVV-9948	LVV-T737 LDM-540		Not Executed
DMS-PRTL-REQ-0109 LDM-554	DMS-PRTL-REQ-0109-V-01 LVV-9949	LVV-T743 LDM-540		Not Executed
DMS-PRTL-REQ-0113 LDM-554	DMS-PRTL-REQ-0113-V-01 LVV-9950	LVV-T747 LDM-540		Not Executed
DMS-PRTL-REQ-0111 LDM-554	DMS-PRTL-REQ-0111-V-01 LVV-9951	LVV-T745 LDM-540		Not Executed
DMS-PRTL-REQ-0114 LDM-554	DMS-PRTL-REQ-0114-V-01 LVV-9952	LVV-T748 LDM-540		Not Executed
DMS-PRTL-REQ-0112 LDM-554	DMS-PRTL-REQ-0112-V-01 LVV-9953	LVV-T746 LDM-540		Not Executed
DMS-PRTL-REQ-0110 LDM-554	DMS-PRTL-REQ-0110-V-01 LVV-9954	LVV-T744 LDM-540		Not Executed
DMS-PRTL-REQ-0115 LDM-554	DMS-PRTL-REQ-0115-V-01 LVV-9955	LVV-T749 LDM-540		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-PRTL-REQ-0117 LDM-554	DMS-PRTL-REQ-0117-V-01 LVV-9956	LVV-T751 LDM-540		Not Executed
DMS-PRTL-REQ-0118 LDM-554	DMS-PRTL-REQ-0118-V-01 LVV-9957	LVV-T752 LDM-540		Not Executed
DMS-PRTL-REQ-0116 LDM-554	DMS-PRTL-REQ-0116-V-01 LVV-9958	LVV-T750 LDM-540		Not Executed
DMS-PRTL-REQ-0127 LDM-554	DMS-PRTL-REQ-0127-V-01 LVV-9959	LVV-T756 LDM-540		Not Executed
DMS-PRTL-REQ-0119 LDM-554	DMS-PRTL-REQ-0119-V-01 LVV-9960	LVV-T753 LDM-540		Not Executed
DMS-PRTL-REQ-0120 LDM-554	DMS-PRTL-REQ-0120-V-01 LVV-9961	LVV-T754 LDM-540		Not Executed
DMS-PRTL-REQ-0121 LDM-554	DMS-PRTL-REQ-0121-V-01 LVV-9962	LVV-T755 LDM-540		Not Executed
DMS-PRTL-REQ-0122 LDM-554	DMS-PRTL-REQ-0122-V-01 LVV-9963	LVV-T757 LDM-540		Not Executed
DMS-PRTL-REQ-0124 LDM-554	DMS-PRTL-REQ-0124-V-01 LVV-9964	LVV-T759 LDM-540		Not Executed
DMS-PRTL-REQ-0123 LDM-554	DMS-PRTL-REQ-0123-V-01 LVV-9965	LVV-T758 LDM-540		Not Executed
DMS-PRTL-REQ-0126 LDM-554	DMS-PRTL-REQ-0126-V-01 LVV-9966	LVV-T761 LDM-540		Not Executed
DMS-PRTL-REQ-0125 LDM-554	DMS-PRTL-REQ-0125-V-01 LVV-9967	LVV-T760 LDM-540		Not Executed
DMS-NB-REQ-0010 LDM-554	DMS-NB-REQ-0010-V-01 LVV-9968	LVV-T767 LDM-540		Not Executed
DMS-NB-REQ-0009 LDM-554	DMS-NB-REQ-0009-V-01 LVV-9969	LVV-T766 LDM-540		Not Executed
DMS-NB-REQ-0014 LDM-554	DMS-NB-REQ-0014-V-01 LVV-9970	LVV-T771 LDM-540		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-NB-REQ-0005 LDM-554	DMS-NB-REQ-0005-V-01 LVV-9971	LVV-T762 LDM-540		Not Executed
DMS-NB-REQ-0015 LDM-554	DMS-NB-REQ-0015-V-01 LVV-9972	LVV-T772 LDM-540		Not Executed
DMS-NB-REQ-0013 LDM-554	DMS-NB-REQ-0013-V-01 LVV-9973	LVV-T770 LDM-540		Not Executed
DMS-NB-REQ-0007 LDM-554	DMS-NB-REQ-0007-V-01 LVV-9974	LVV-T764 LDM-540		Not Executed
DMS-NB-REQ-0008 LDM-554	DMS-NB-REQ-0008-V-01 LVV-9975	LVV-T765 LDM-540		Not Executed
DMS-NB-REQ-0006 LDM-554	DMS-NB-REQ-0006-V-01 LVV-9976	LVV-T763 LDM-540		Not Executed
DMS-NB-REQ-0012 LDM-554	DMS-NB-REQ-0012-V-01 LVV-9977	LVV-T769 LDM-540		Not Executed
DMS-NB-REQ-0011 LDM-554	DMS-NB-REQ-0011-V-01 LVV-9978	LVV-T768 LDM-540		Not Executed
DMS-NB-REQ-0023 LDM-554	DMS-NB-REQ-0023-V-01 LVV-9979	LVV-T780 LDM-540		Not Executed
DMS-NB-REQ-0017 LDM-554	DMS-NB-REQ-0017-V-01 LVV-9980	LVV-T774 LDM-540		Not Executed
DMS-NB-REQ-0021 LDM-554	DMS-NB-REQ-0021-V-01 LVV-9981	LVV-T778 LDM-540		Not Executed
DMS-NB-REQ-0022 LDM-554	DMS-NB-REQ-0022-V-01 LVV-9982	LVV-T779 LDM-540		Not Executed
DMS-NB-REQ-0016 LDM-554	DMS-NB-REQ-0016-V-01 LVV-9983	LVV-T773 LDM-540		Not Executed
DMS-NB-REQ-0020 LDM-554	DMS-NB-REQ-0020-V-01 LVV-9984	LVV-T777 LDM-540		Not Executed
DMS-NB-REQ-0018 LDM-554	DMS-NB-REQ-0018-V-01 LVV-9985	LVV-T775 LDM-540		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-NB-REQ-0019 LDM-554	DMS-NB-REQ-0019-V-01 LVV-9986	LVV-T776 LDM-540		Not Executed
DMS-NB-REQ-0025 LDM-554	DMS-NB-REQ-0025-V-01 LVV-9987	LVV-T782 LDM-540		Not Executed
DMS-NB-REQ-0024 LDM-554	DMS-NB-REQ-0024-V-01 LVV-9988	LVV-T781 LDM-540		Not Executed
DMS-NB-REQ-0026 LDM-554	DMS-NB-REQ-0026-V-01 LVV-9989	LVV-T783 LDM-540		Not Executed
DMS-NB-REQ-0032 LDM-554	DMS-NB-REQ-0032-V-01 LVV-9990	LVV-T784 LDM-540		Not Executed
DMS-NB-REQ-0033 LDM-554	DMS-NB-REQ-0033-V-01 LVV-9991	LVV-T785 LDM-540		Not Executed
DMS-NB-REQ-0035 LDM-554	DMS-NB-REQ-0035-V-01 LVV-9992	LVV-T787 LDM-540		Not Executed
DMS-NB-REQ-0034 LDM-554	DMS-NB-REQ-0034-V-01 LVV-9993	LVV-T786 LDM-540		Not Executed
DMS-NB-REQ-0036 LDM-554	DMS-NB-REQ-0036-V-01 LVV-9994	LVV-T788 LDM-540		Not Executed
DMS-NB-REQ-0030 LDM-554	DMS-NB-REQ-0030-V-01 LVV-9995	LVV-T790 LDM-540		Not Executed
DMS-NB-REQ-0029 LDM-554	DMS-NB-REQ-0029-V-01 LVV-9996	LVV-T789 LDM-540		Not Executed
DMS-NB-REQ-0031 LDM-554	DMS-NB-REQ-0031-V-01 LVV-9997	LVV-T791 LDM-540		Not Executed
DMS-NB-REQ-0002 LDM-554	DMS-NB-REQ-0002-V-01 LVV-9998	LVV-T793 LDM-540		Not Executed
DMS-NB-REQ-0003 LDM-554	DMS-NB-REQ-0003-V-01 LVV-9999	LVV-T794 LDM-540		Not Executed
DMS-NB-REQ-0001 LDM-554	DMS-NB-REQ-0001-V-01 LVV-10000	LVV-T792 LDM-540		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-NB-REQ-0004 LDM-554	DMS-NB-REQ-0004-V-01 LVV-10001	LVV-T795 LDM-540		Not Executed
DMS-API-REQ-0023 LDM-554	DMS-API-REQ-0023-V-01 LVV-10002	LVV-T798 LDM-540		Not Executed
DMS-API-REQ-0022 LDM-554	DMS-API-REQ-0022-V-01 LVV-10003	LVV-T797 LDM-540		Not Executed
DMS-API-REQ-0028 LDM-554	DMS-API-REQ-0028-V-01 LVV-10004	LVV-T803 LDM-540		Not Executed
DMS-API-REQ-0024 LDM-554	DMS-API-REQ-0024-V-01 LVV-10005	LVV-T799 LDM-540		Not Executed
DMS-API-REQ-0026 LDM-554	DMS-API-REQ-0026-V-01 LVV-10006	LVV-T801 LDM-540		Not Executed
DMS-API-REQ-0027 LDM-554	DMS-API-REQ-0027-V-01 LVV-10007	LVV-T802 LDM-540		Not Executed
DMS-API-REQ-0030 LDM-554	DMS-API-REQ-0030-V-01 LVV-10008	LVV-T805 LDM-540		Not Executed
DMS-API-REQ-0025 LDM-554	DMS-API-REQ-0025-V-01 LVV-10009	LVV-T800 LDM-540		Not Executed
DMS-API-REQ-0029 LDM-554	DMS-API-REQ-0029-V-01 LVV-10010	LVV-T804 LDM-540		Not Executed
DMS-API-REQ-0021 LDM-554	DMS-API-REQ-0021-V-01 LVV-10011	LVV-T796 LDM-540		Not Executed
DMS-API-REQ-0009 LDM-554	DMS-API-REQ-0009-V-01 LVV-10012	LVV-T809 LDM-540		Not Executed
DMS-API-REQ-0008 LDM-554	DMS-API-REQ-0008-V-01 LVV-10013	LVV-T808 LDM-540		Not Executed
DMS-API-REQ-0007 LDM-554	DMS-API-REQ-0007-V-01 LVV-10014	LVV-T807 LDM-540		Not Executed
DMS-API-REQ-0006 LDM-554	DMS-API-REQ-0006-V-01 LVV-10015	LVV-T806 LDM-540		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-API-REQ-0016 LDM-554	DMS-API-REQ-0016-V-01 LVV-10016	LVV-T810 LDM-540		Not Executed
DMS-API-REQ-0018 LDM-554	DMS-API-REQ-0018-V-01 LVV-10017	LVV-T812 LDM-540		Not Executed
DMS-API-REQ-0017 LDM-554	DMS-API-REQ-0017-V-01 LVV-10018	LVV-T811 LDM-540		Not Executed
DMS-API-REQ-0039 LDM-554	DMS-API-REQ-0039-V-01 LVV-10019	LVV-T814 LDM-540		Not Executed
DMS-API-REQ-0038 LDM-554	DMS-API-REQ-0038-V-01 LVV-10020	LVV-T813 LDM-540		Not Executed
DMS-API-REQ-0040 LDM-554	DMS-API-REQ-0040-V-01 LVV-10021	LVV-T815 LDM-540		Not Executed
DMS-API-REQ-0034 LDM-554	DMS-API-REQ-0034-V-01 LVV-10022	LVV-T816 LDM-540		Not Executed
DMS-API-REQ-0019 LDM-554	DMS-API-REQ-0019-V-01 LVV-10023	LVV-T817 LDM-540		Not Executed
DMS-API-REQ-0020 LDM-554	DMS-API-REQ-0020-V-01 LVV-10024	LVV-T818 LDM-540		Not Executed
DMS-API-REQ-0014 LDM-554	DMS-API-REQ-0014-V-01 LVV-10025	LVV-T823 LDM-540		Not Executed
DMS-API-REQ-0013 LDM-554	DMS-API-REQ-0013-V-01 LVV-10026	LVV-T822 LDM-540		Not Executed
DMS-API-REQ-0015 LDM-554	DMS-API-REQ-0015-V-01 LVV-10027	LVV-T824 LDM-540		Not Executed
DMS-API-REQ-0012 LDM-554	DMS-API-REQ-0012-V-01 LVV-10028	LVV-T821 LDM-540		Not Executed
DMS-API-REQ-0010 LDM-554	DMS-API-REQ-0010-V-01 LVV-10029	LVV-T819 LDM-540		Not Executed
DMS-API-REQ-0011 LDM-554	DMS-API-REQ-0011-V-01 LVV-10030	LVV-T820 LDM-540		Not Executed

Table 2: DM VCD Table.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-API-REQ-0033 LDM-554	DMS-API-REQ-0033-V-01 LVV-10031	LVV-T827 LDM-540		Not Executed
DMS-API-REQ-0031 LDM-554	DMS-API-REQ-0031-V-01 LVV-10032	LVV-T825 LDM-540		Not Executed
DMS-API-REQ-0032 LDM-554	DMS-API-REQ-0032-V-01 LVV-10033	LVV-T826 LDM-540		Not Executed
DMS-API-REQ-0003 LDM-554	DMS-API-REQ-0003-V-01 LVV-10034	LVV-T829 LDM-540		Not Executed
DMS-API-REQ-0004 LDM-554	DMS-API-REQ-0004-V-01 LVV-10035	LVV-T830 LDM-540		Not Executed
DMS-API-REQ-0005 LDM-554	DMS-API-REQ-0005-V-01 LVV-10036	LVV-T831 LDM-540		Not Executed
DMS-API-REQ-0001 LDM-554	DMS-API-REQ-0001-V-01 LVV-10037	LVV-T828 LDM-540		Not Executed
DMS-API-REQ-0035 LDM-554	DMS-API-REQ-0035-V-01 LVV-10038	LVV-T832 LDM-540		Not Executed
DMS-API-REQ-0037 LDM-554	DMS-API-REQ-0037-V-01 LVV-10039	LVV-T835 LDM-540		Not Executed
DMS-API-REQ-0002 LDM-554	DMS-API-REQ-0002-V-01 LVV-10040	LVV-T833 LDM-540		Not Executed
DMS-API-REQ-0036 LDM-554	DMS-API-REQ-0036-V-01 LVV-10041	LVV-T834 LDM-540		Not Executed

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B Acronyms

Acronym	Description
API	Application Programming Interface
CA-DM-CON-ICD	Requirements for Interface between the Camera and Data Management (LSE-69)
CA-DM-DAQ-ICD	Requirements for Camera Data Acquisition Interface (LSE-68)
CA-DM-SUP-ICD	Requirements for Support-Data Exchanges between Data Management and Camera (LSE-130)
CPT-OCS-INT-ICD	Summit Computer Room Requirements (LSE-209)
DAQ	Data Acquisition System
DM	Data Management
DM-TS-AUX-ICD	Requirements for Auxiliary Instrumentation Interface between Data Management and Telescope (LSE-140)
DM-TS-CON-ICD	Requirements for Control System Interfaces between the Telescope & Data Management (LSE-75)
DMS	Data Management Subsystem
DMS-API-REQ	LSP API Aspect requirements (LDM-554)

DMS-LSP-REQ	Top level LSP requirements (LDM-554)
DMS-NB-REQ	LSP Notebook Aspect requirements (LDM-554)
DMS-PTL-REQ	LSP Portal Aspect requirements (LDM-554)
DMS-REQ	Data Management top level requirements (LSE-61)
DMTR	DM Test (Plan and) Report
Data Management Subsystem	The subsystems within Data Management may contain a defined combination of hardware, a software stack, a set of running processes, and the people who manage them: they are a major component of the DM System operations. Examples include the 'Archive Operations Subsystem' and the 'Data Processing Subsystem'."
Data Management System	The computing infrastructure, middleware, and applications that process, store, and enable information extraction from the LSST dataset; the DMS will process peta-scale data volume, convert raw images into a faithful representation of the universe, and archive the results in a useful form. The infrastructure layer consists of the computing, storage, networking hardware, and system software. The middleware layer handles distributed processing, data access, user interface, and system operations services. The applications layer includes the data pipelines and the science data archives' products and services.
Document	Any object (in any application supported by DocuShare or design archives such as PDMWorks or GIT) that supports project management or records milestones and deliverables of the LSST Project
EP-DM-CON-ICD	Requirements for Interface between Data Management and EPO (LSE-131)
ICD	Interface Control Document
LDM	LSST Data Management (Document Handle)
LSE	LSST Systems Engineering (Document Handle)
LSP	LSST Science Platform
LSST	Large Synoptic Survey Telescope
LVV	LSST Verification and Validation (Jira project)
OCS	Observatory Control System
OCS-DM-COM-ICD	Requirements for OCS - Data Management Software Communication Interface (LSE-72)

Requirement	A declaration of a specified function or quantitative performance that the delivered system or subsystem must meet. It is a statement that identifies a necessary attribute, capability, characteristic, or quality of a system in order for the delivered system or subsystem to meet a derived or higher requirement, constraint, or function.
SYS-ALL-COM-ICD	Requirements for LSST Observatory Control System Communication Architecture and Protocol (LSE-70)
Specification	One or more performance parameter(s) being established by a requirement that the delivered system or subsystem must meet
Subsystem	A set of elements comprising a system within the larger LSST system that is responsible for a key technical deliverable of the project.
TS	Test Specification
VCD	Verification Control Document
Verification	The process of evaluating the design, including hardware and software - to ensure the requirements have been met; verification (of requirements) is performed by test, analysis, inspection, and/or demonstration