



Vera C. Rubin Observatory  
Data Management

# DM Verification Control Document

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LDM-692

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## Abstract

The DM Verification Control Document is an extract from the LSST-wide systems engineering verification database (based on Jira) which, provides an overview of the verification status of the Data Management Subsystem with respect to its requirements.

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# DM Verification Control Document

## 1 Introduction

The requirements on the LSST Data Management System are described in LSE-61 and in the various lower-level requirements documents (e.g. LDM-554, LDM-555, LDM-636, etc), all of which are derived from LSE-61. These requirements are verified by running tests, and recording the results of those tests in the LSST Jira system. The general approach to testing and verification in DM is described in more detail in LDM-503; note, in particular, the concepts and vocabulary described in LDM-503 §2.3.2.

This Verification Control Document (VCD) provides a summary of the status of requirements verification: which requirements are covered by which tests, and how many of those tests have been executed successfully. Section 2 presents a brief overview; the remainder of the document provides a matrix displaying all of the DM requirements, their corresponding verification elements, the applicable tests, and whether those tests have been executed.

The matrix is fully linked: any given test case mentioned in the matrix such as, LWV-T362, provides a link to the Jira element defining that test case. Similarly, relevant documentation such as test reports like DMTR-111 are linked to the bibliography and thence to the report itself<sup>1</sup>.

### 1.1 Applicable Documents

LSE-61	LSST DM Subsystem Requirements
LDM-503	LSST DM Test Plan
LDM-639	DM Acceptance Test Specification

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<sup>1</sup>As such, you may find it useful to remember the appropriate keyboard shortcuts — often ⌘+[ (macOS) or ctrl+← (Windows/Linux) — to return to your previous position.

## 2 Summary Information

Table 1 provides an overview of the requirements and verification elements coverage.

	<i>Priority</i>	<i>Fully Verified</i> <sup>2.1.1</sup>	<i>Partially Verified</i> <sup>2.1.2</sup>	<i>With Failures</i> <sup>2.1.3</sup>	<i>Not Verified</i> <sup>2.1.4</sup>	<i>Not Covered</i> <sup>2.1.5</sup>	<i>Total</i>
<b>DM Requirements</b>	(All)	124	87	1	328	237	<b>777</b>
LSE-61	1a	17	14		25	2	<b>58</b>
	1b	16	18	1	73	3	<b>111</b>
	2	3	4		44	3	<b>54</b>
	3				6		<b>6</b>
	Not Set					1	<b>1</b>
	(All)	36	36	1	148	9	<b>230</b>
LSE-68	Not Set					19	<b>19</b>
LSE-69	Not Set					16	<b>16</b>
LSE-72	Not Set					52	<b>52</b>
LSE-75	Not Set					9	<b>9</b>
LSE-130	Not Set					31	<b>31</b>
LSE-131	Not Set					12	<b>12</b>
LSE-140	Not Set					30	<b>30</b>
LDM-554	Not Set	13	51		172		<b>236</b>
LSE-400	Not Set					15	<b>15</b>
LDM-556	1a	50			8	28	<b>86</b>
	1b	1				4	<b>5</b>
	2	2				2	<b>4</b>
	3	1					<b>1</b>
	Not Set	21				10	<b>31</b>
	(All)	75			8	44	<b>127</b>
<b>DM Verification E.</b>	(All)	144	86	1	366	407	<b>1004</b>

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Table 1: Summary overview of all DM requirements and verification elements.

Table 2 provides the Test Cases result summary.

	<i>Passed</i> <sup>2.2.1</sup>	<i>P. w/Dev.</i> <sup>2.2.2</sup>	<i>Failed</i> <sup>2.2.3</sup>	<i>Not Ex.</i> <sup>2.2.4</sup>	<i>Total</i>
Test Cases Results	153	12	1	467	<b>633</b>

Table 2: Summary overview of DM test cases executions.

Note that test cases may be associated with multiple requirements or verification elements, and requirements or verification elements may be associated with multiple test cases.

## 2.1 Coverage Description

The coverage of each Verification Element and Requirement is characterized by one of the statuses defined below.

### 2.1.1 Fully Verified

These are the Verification Elements and Requirements for which all of the associated Test Cases have been successfully executed. The last execution status for all test cases shall be **Passed** or **Passed w/Deviation**.

### 2.1.2 Partially Verified

These are the Verification Elements and Requirements for which at least one of the associated Test Cases have been successfully executed. The last execution status of the test cases shall

be **Not Executed**, **Passed** or **Passed w/Deviation**.

### 2.1.3 With Failures

These are the Verification Elements and Requirements for which at least one of the associated Test Cases has a **Failure** as execution result.

### 2.1.4 Not Verified

These are the Verification Elements and Requirements for which none of the associated Test Cases have been executed.

### 2.1.5 Not Covered

These are the Verification Elements and Requirements for which there are not any associated Test Cases.

## 2.2 Test Executions Description

### 2.2.1 Passed

Test cases that have been executed without any problems. Issues may have been found during the execution, and linked to the test case, but they do not affect the verification process.

### 2.2.2 Passed with Deviation

Test cases for which the execution can be considered successful, but a deviation to the requirement is needed. The deviation shall be recorded in a Jira issue, type *Deviation* and linked to the test.

### 2.2.3 Failed

The test case execution failed. One or more Jira issues shall be filed and related with the test.



## 2.2.4 Not Executed

The test case has not been executed yet.

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### 3 Verification Control

In the following subsections, a detailed overview of the requirements coverage for each specification is provided.

Where available, the priority is reported in round brackets, under the requirement or verification element identifier. For example (p.1a) denotes a 1a priority requirement. Requirements and verification elements for which no priority (p.) is specified inherit the priority for the higher-level requirement from which they are derived. For a full description of the prioritization levels, see LSE-61.

#### 3.1 LSE-61 Requirements Coverage

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0002 LSE-61 (p. 1b)	LVV-3 LVV-3 (p. 1b)	LVV-T101 LDM-639		Not Executed
		LVV-T217 LDM-639	2018-07-04	Pass w/Deviation
DMS-REQ-0008 LSE-61 (p. 1b)	LVV-5 LVV-5 (p. 1b)	LVV-T171 LDM-639		Not Executed
		LVV-T287 LDM-639		Not Executed
DMS-REQ-0009 LSE-61 (p. 1b)	LVV-6 LVV-6 (p. 1b)	LVV-T125 LDM-639	2020-11-03 LVV-P71	Passed
DMS-REQ-0010 LSE-61 (p. 1b)	LVV-7 LVV-7 (p. 1b)	LVV-T36 LDM-639	2020-10-02 LVV-P71	Passed
DMS-REQ-0018 LSE-61 (p. 1a)	LVV-8 LVV-8 (p. 1a)	LVV-T29 LDM-639		Not Executed
		LVV-T283 LDM-639	2019-05-22 LVV-P45	Passed
		LVV-T284 LDM-639	2019-06-24 LVV-P10	Passed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T1549 LDM-639	2020-08-20 LVV-P50	Passed
		LVV-T1550 LDM-639	LVV-P54	Not Executed
		LVV-T1556 LDM-639	2020-09-03 LVV-P55	Passed
		LVV-T1934 LDM-639	2020-10-28 LVV-P76	Passed
DMS-REQ-0020 LSE-61 (p. 1a)	LVV-9 LVV-9 (p. 1a)	LVV-T30 LDM-639		Not Executed
		LVV-T283 LDM-639	2019-05-22 LVV-P45	Passed
		LVV-T284 LDM-639	2019-06-24 LVV-P10	Passed
		LVV-T1549 LDM-639	2020-08-20 LVV-P50	Passed
		LVV-T1556 LDM-639	2020-09-03 LVV-P55	Passed
DMS-REQ-0022 LSE-61 (p. 2)	LVV-10 LVV-10 (p. Not Set)			
DMS-REQ-0024 LSE-61 (p. 1a)	LVV-11 LVV-11 (p. 1a)	LVV-T32 LDM-639	2022-11-09 LVV-P99	Passed
		LVV-T283 LDM-639	2019-05-22 LVV-P45	Passed
		LVV-T284 LDM-639	2019-06-24 LVV-P10	Passed
		LVV-T1549 LDM-639	2020-08-20 LVV-P50	Passed
		LVV-T1556 LDM-639	2020-09-03 LVV-P55	Passed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T1934	2020-10-28 LVV-P76	Passed
DMS-REQ-0029 LSE-61 (p. 1b)	LVV-12 LVV-12 (p. 1b)	LVV-T15 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T19 LDM-639	2019-05-22 LVV-P44	Passed
		LVV-T39 LDM-639	2020-11-03 LVV-P71	Passed
DMS-REQ-0030 LSE-61 (p. 1a)	LVV-13 LVV-13 (p. 1a)	LVV-T15 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T19 LDM-639	2019-05-22 LVV-P44	Passed
		LVV-T40 LDM-639	2020-08-19 LVV-P71	Passed
	LVV-9741 LVV-9741 (p. 1a)	LVV-T1240 LDM-639	2020-01-28 LVV-P65	Passed
DMS-REQ-0032 LSE-61 (p. 1b)	LVV-14 LVV-14 (p. 1b)	LVV-T126 LDM-639	2022-08-30 LVV-P99	Passed
DMS-REQ-0033 LSE-61 (p. 1a)	LVV-15 LVV-15 (p. 1a)	LVV-T127 LDM-639	2022-08-23 LVV-P99	Passed
DMS-REQ-0034 LSE-61 (p. 1a)	LVV-16 LVV-16 (p. 1a)	LVV-T61 LDM-639		Not Executed
DMS-REQ-0042 LSE-61 (p. 1b)	LVV-17 LVV-17 (p. 1b)	LVV-T128 LDM-639		Not Executed
DMS-REQ-0043 LSE-61 (p. 1a)	LVV-18 LVV-18 (p. 1a)	LVV-T21 LDM-639	2019-05-22 LVV-P44	Passed
		LVV-T22 LDM-639	2019-05-22 LVV-P44	Passed
		LVV-T129 LDM-639	2022-12-15 LVV-P99	Not Executed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0046 LSE-61 (p. 2)	LVV-19 LVV-19 (p. 2)	LVV-T68 LDM-639		Not Executed
DMS-REQ-0047 LSE-61 (p. 1b)	LVV-20 LVV-20 (p. 1b)	LVV-T16 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T62 LDM-639	2020-01-23 LVV-P65	Passed
DMS-REQ-0052 LSE-61 (p. 1b)	LVV-21 LVV-21 (p. 1b)	LVV-T130 LDM-639		Not Executed
DMS-REQ-0059 LSE-61 (p. 1a)	LVV-22 LVV-22 (p. 1a)	LVV-T83 LDM-639		Not Executed
DMS-REQ-0060 LSE-61 (p. 1a)	LVV-23 LVV-23 (p. 1a)	LVV-T84 LDM-639	2022-08-29 LVV-P99	Passed
DMS-REQ-0061 LSE-61 (p. 1a)	LVV-24 LVV-24 (p. 1a)	LVV-T85 LDM-639	LVV-P72	Not Executed
DMS-REQ-0062 LSE-61 (p. 1b)	LVV-25 LVV-25 (p. 1b)	LVV-T86 LDM-639		Not Executed
DMS-REQ-0063 LSE-61 (p. 1b)	LVV-26 LVV-26 (p. 1b)	LVV-T87 LDM-639		Not Executed
DMS-REQ-0065 LSE-61 (p. 1b)	LVV-27 LVV-27 (p. 1b)	LVV-T134 LDM-639		Not Executed
DMS-REQ-0068 LSE-61 (p. 1a)	LVV-28 LVV-28 (p. 1a)	LVV-T33 LDM-639	2023-01-09 LVV-P99	Not Executed
		LVV-T283 LDM-639	2019-05-22 LVV-P45	Passed
		LVV-T284 LDM-639	2019-06-24 LVV-P10	Passed
		LVV-T286 LDM-639	2019-05-22 LVV-P45	Passed
		LVV-T1549 LDM-639	2020-08-20 LVV-P50	Passed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T1550 LDM-639	LVV-P54	Not Executed
		LVV-T1556 LDM-639	2020-09-03 LVV-P55	Passed
DMS-REQ-0069 LSE-61 (p. 1a)	LVV-29 LVV-29 (p. 1a)	LVV-T15 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T18 LDM-639	2019-05-22 LVV-P44	Passed
		LVV-T19 LDM-639	2019-05-22 LVV-P44	Passed
		LVV-T38 LDM-639	2020-11-04 LVV-P71	Passed
		LVV-T2334 LDM-639		Not Executed
DMS-REQ-0070 LSE-61 (p. 1b)	LVV-30 LVV-30 (p. 1b)	LVV-T15 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T19 LDM-639	2019-05-22 LVV-P44	Passed
		LVV-T41 LDM-639	2020-02-04 LVV-P65	Passed
DMS-REQ-0072 LSE-61 (p. 1a)	LVV-31 LVV-31 (p. 1a)	LVV-T15 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T19 LDM-639	2019-05-22 LVV-P44	Passed
		LVV-T42 LDM-639	2022-08-30 LVV-P99	Passed
DMS-REQ-0074 LSE-61 (p. 1b)	LVV-32 LVV-32 (p. 1b)	LVV-T20 LDM-639	2019-05-22 LVV-P44	Failed
		LVV-T37 LDM-639		Not Executed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0075 LSE-61 (p. 1a)	LVV-33 LVV-33 (p. 1a)	LVV-T149 LDM-639		Not Executed
DMS-REQ-0077 LSE-61 (p. 1b)	LVV-34 LVV-34 (p. 1b)	LVV-T150 LDM-639		Not Executed
DMS-REQ-0078 LSE-61 (p. 1a)	LVV-35 LVV-35 (p. 1a)	LVV-T151 LDM-639	2022-08-24 LVV-P99	Passed
		LVV-T1232 LDM-639	2022-08-24 LVV-P99	Passed
DMS-REQ-0089 LSE-61 (p. 1b)	LVV-36 LVV-36 (p. 1b)	LVV-T102 LDM-639		Not Executed
DMS-REQ-0094 LSE-61 (p. 1b)	LVV-37 LVV-37 (p. 1b)	LVV-T152 LDM-639		Not Executed
DMS-REQ-0096 LSE-61 (p. 1a)	LVV-38 LVV-38 (p. 1a)	LVV-T103 LDM-639		Not Executed
DMS-REQ-0097 LSE-61 (p. 1a)	LVV-39 LVV-39 (p. 1a)	LVV-T45 LDM-639	LVV-P71	Not Executed
DMS-REQ-0098 LSE-61 (p. 1b)	LVV-40 LVV-40 (p. 1b)	LVV-T104 LDM-639		Not Executed
DMS-REQ-0099 LSE-61 (p. 1b)	LVV-41 LVV-41 (p. 1b)	LVV-T46 LDM-639	LVV-P71	Not Executed
DMS-REQ-0100 LSE-61 (p. 1b)	LVV-42 LVV-42 (p. 1b)	LVV-T105 LDM-639		Not Executed
DMS-REQ-0101 LSE-61 (p. 1a)	LVV-43 LVV-43 (p. 1a)	LVV-T47 LDM-639		Not Executed
DMS-REQ-0102 LSE-61 (p. 1a)	LVV-44 LVV-44 (p. 1a)	LVV-T153 LDM-639		Not Executed
DMS-REQ-0103 LSE-61 (p. 1b)	LVV-45 LVV-45 (p. 1b)	LVV-T63 LDM-639		Not Executed
DMS-REQ-0106 LSE-61 (p. 1b)	LVV-46 LVV-46 (p. 1b)	LVV-T11 LDM-639	2019-05-22 LVV-P43	Passed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T64 LDM-639		Not Executed
DMS-REQ-0119 LSE-61 (p. 2)	LVV-47 LVV-47 (p. 2)	LVV-T117 LDM-639		Not Executed
DMS-REQ-0120 LSE-61 (p. 2)	LVV-48 LVV-48 (p. 2)	LVV-T118 LDM-639		Not Executed
DMS-REQ-0121 LSE-61 (p. 2)	LVV-49 LVV-49 (p. 2)	LVV-T119 LDM-639		Not Executed
DMS-REQ-0122 LSE-61 (p. 2)	LVV-50 LVV-50 (p. 2)	LVV-T204 LDM-639		Not Executed
DMS-REQ-0123 LSE-61 (p. 2)	LVV-51 LVV-51 (p. 2)	LVV-T205 LDM-639		Not Executed
DMS-REQ-0124 LSE-61 (p. 2)	LVV-52 LVV-52 (p. 2)	LVV-T206 LDM-639		Not Executed
DMS-REQ-0125 LSE-61 (p. 2)	LVV-53 LVV-53 (p. 2)	LVV-T120 LDM-639		Not Executed
DMS-REQ-0126 LSE-61 (p. 2)	LVV-54 LVV-54 (p. 2)	LVV-T207 LDM-639		Not Executed
DMS-REQ-0127 LSE-61 (p. 2)	LVV-55 LVV-55 (p. 2)	LVV-T208 LDM-639		Not Executed
DMS-REQ-0128 LSE-61 (p. 2)	LVV-56 LVV-56 (p. 2)	LVV-T121 LDM-639		Not Executed
DMS-REQ-0130 LSE-61 (p. 1a)	LVV-57 LVV-57 (p. 1a)	LVV-T88 LDM-639	LVV-P72	Not Executed
DMS-REQ-0131 LSE-61 (p. 2)	LVV-58 LVV-58 (p. 2)	LVV-T106 LDM-639		Not Executed
	LVV-9745 LVV-9745 (p. 2)	LVV-T1277 LDM-639		Not Executed
DMS-REQ-0132 LSE-61 (p. 1a)	LVV-59 LVV-59 (p. 1a)	LVV-T89 LDM-639		Not Executed



Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0155 LSE-61 (p. 1a)	LVV-60 LVV-60 (p. 1a)			
DMS-REQ-0156 LSE-61 (p. 1a)	LVV-61 LVV-61 (p. 1a)			
DMS-REQ-0158 LSE-61 (p. 1a)	LVV-62 LVV-62 (p. 1a)	LVV-T11 LDM-639	2019-05-22 LVV-P43	Passed
DMS-REQ-0160 LSE-61 (p. 1b)	LVV-63 LVV-63 (p. 1b)	LVV-T131 LDM-639		Not Executed
		LVV-T368 LDM-639		Not Executed
DMS-REQ-0161 LSE-61 (p. 1b)	LVV-64 LVV-64 (p. 1b)	LVV-T172 LDM-639		Not Executed
DMS-REQ-0162 LSE-61 (p. 1b)	LVV-65 LVV-65 (p. 1b)	LVV-T173 LDM-639		Not Executed
		LVV-T287 LDM-639		Not Executed
DMS-REQ-0163 LSE-61 (p. 1b)	LVV-66 LVV-66 (p. 1b)	LVV-T174 LDM-639		Not Executed
DMS-REQ-0164 LSE-61 (p. 1b)	LVV-67 LVV-67 (p. 1b)	LVV-T175 LDM-639		Not Executed
DMS-REQ-0165 LSE-61 (p. 1b)	LVV-68 LVV-68 (p. 1b)	LVV-T176 LDM-639	LVV-P4	Not Executed
		LVV-T287 LDM-639		Not Executed
DMS-REQ-0166 LSE-61 (p. 1b)	LVV-69 LVV-69 (p. 1b)	LVV-T177 LDM-639		Not Executed
DMS-REQ-0167 LSE-61 (p. 2)	LVV-70 LVV-70 (p. 2)	LVV-T178 LDM-639		Not Executed
		LVV-T287 LDM-639		Not Executed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0168 LSE-61 (p. 1a)	LVV-71 LVV-71 (p. 1a)	LVV-T1097		Not Executed
		LDM-639		
		LVV-T2338	LVV-P90	Not Executed
DMS-REQ-0170 LSE-61 (p. 1b)	LVV-72 LVV-72 (p. 1b)	LVV-T182 LDM-639		Not Executed
DMS-REQ-0171 LSE-61 (p. 1a)	LVV-73 LVV-73 (p. 1a)	LVV-T1168	2019-07-09	Passed
		LDM-639	LVV-P47	
		LVV-T1612 LDM-639		Not Executed
DMS-REQ-0172 LSE-61 (p. 1b)	LVV-74 LVV-74 (p. 1b)	LVV-T185 LDM-639		Not Executed
DMS-REQ-0173 LSE-61 (p. 1b)	LVV-75 LVV-75 (p. 1b)	LVV-T186 LDM-639		Not Executed
DMS-REQ-0174 LSE-61 (p. 1b)	LVV-76 LVV-76 (p. 1b)	LVV-T187 LDM-639		Not Executed
DMS-REQ-0175 LSE-61 (p. 1b)	LVV-77 LVV-77 (p. 1b)	LVV-T188 LDM-639		Not Executed
DMS-REQ-0176 LSE-61 (p. 1a)	LVV-78 LVV-78 (p. 1a)	LVV-T189 LDM-639		Not Executed
DMS-REQ-0177 LSE-61	LVV-79 LVV-79 (p. Not Set)			
DMS-REQ-0178 LSE-61 (p. 1b)	LVV-80 LVV-80 (p. 1b)	LVV-T190 LDM-639	2022-08-25 LVV-P99	Passed
DMS-REQ-0180 LSE-61 (p. 1b)	LVV-81 LVV-81 (p. 1b)	LVV-T193 LDM-639	LVV-P73	Not Executed
DMS-REQ-0181 LSE-61 (p. 1b)	LVV-82 LVV-82 (p. 1b)	LVV-T194 LDM-639	LVV-P73	Not Executed
DMS-REQ-0182 LSE-61 (p. 1b)	LVV-83 LVV-83 (p. 1b)	LVV-T195 LDM-639	LVV-P73	Not Executed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0183 LSE-61 (p. 1b)	LVV-84 LVV-84 (p. 1b)	LVV-T196 LDM-639	LVV-P73	Not Executed
DMS-REQ-0185 LSE-61 (p. 1a)	LVV-85 LVV-85 (p. 1a)	LVV-T197 LDM-639		Not Executed
DMS-REQ-0186 LSE-61 (p. 1a)	LVV-86 LVV-86 (p. 1a)	LVV-T198 LDM-639		Not Executed
DMS-REQ-0187 LSE-61 (p. 1b)	LVV-87 LVV-87 (p. 1b)	LVV-T199 LDM-639	2022-08-25 LVV-P99	Passed
DMS-REQ-0188 LSE-61 (p. 1b)	LVV-88 LVV-88 (p. 1b)	LVV-T200 LDM-639		Not Executed
DMS-REQ-0189 LSE-61 (p. 1b)	LVV-89 LVV-89 (p. 1b)	LVV-T201 LDM-639		Not Executed
DMS-REQ-0190 LSE-61 (p. 1b)	LVV-90 LVV-90 (p. 1b)	LVV-T202 LDM-639		Not Executed
DMS-REQ-0191 LSE-61 (p. 1b)	LVV-91 LVV-91 (p. 1b)	LVV-T203 LDM-639		Not Executed
DMS-REQ-0193 LSE-61 (p. 1b)	LVV-92 LVV-92 (p. 1b)	LVV-T209 LDM-639		Not Executed
DMS-REQ-0194 LSE-61 (p. 1b)	LVV-93 LVV-93 (p. 1b)	LVV-T210 LDM-639		Not Executed
DMS-REQ-0196 LSE-61 (p. 1b)	LVV-94 LVV-94 (p. 1b)	LVV-T211 LDM-639		Not Executed
DMS-REQ-0197 LSE-61 (p. 2)	LVV-95 LVV-95 (p. 2)	LVV-T212 LDM-639		Not Executed
DMS-REQ-0265 LSE-61 (p. 1a)	LVV-96 LVV-96 (p. 1a)	LVV-T34 LDM-639		Not Executed
		LVV-T283 LDM-639	2019-05-22 LVV-P45	Passed
		LVV-T284 LDM-639	2019-06-24 LVV-P10	Passed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0266 LSE-61 (p. 1a)	LVV-97 LVV-97 (p. 1a)	LVV-T48 LDM-639		Not Executed
DMS-REQ-0267 LSE-61 (p. 1b)	LVV-98 LVV-98 (p. 1b)	LVV-T12 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T13 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T65 LDM-639		Not Executed
DMS-REQ-0268 LSE-61 (p. 1b)	LVV-99 LVV-99 (p. 1b)	LVV-T66 LDM-639	2022-09-28 LVV-P99	Passed
DMS-REQ-0269 LSE-61 (p. 1b)	LVV-100 LVV-100 (p. 1b)	LVV-T18 LDM-639	2019-05-22 LVV-P44	Passed
		LVV-T21 LDM-639	2019-05-22 LVV-P44	Passed
		LVV-T49 LDM-639		Not Executed
DMS-REQ-0270 LSE-61 (p. 2)	LVV-101 LVV-101 (p. 2)	LVV-T21 LDM-639	2019-05-22 LVV-P44	Passed
		LVV-T50 LDM-639		Not Executed
DMS-REQ-0271 LSE-61 (p. 1b)	LVV-102 LVV-102 (p. 1b)	LVV-T18 LDM-639	2019-05-22 LVV-P44	Passed
		LVV-T22 LDM-639	2019-05-22 LVV-P44	Passed
		LVV-T51 LDM-639		Not Executed
	LVV-9742 LVV-9742 (p. 1b)	LVV-T2304 LDM-639		Not Executed
	LVV-9743 LVV-9743 (p. 1b)	LVV-T2305 LDM-639		Not Executed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0272 LSE-61 (p. 1b)	LVV-103 LVV-103 (p. 1b)	LVV-T22	2019-05-22	Passed
		LDM-639	LVV-P44	
		LVV-T52		Not Executed
		LDM-639		
DMS-REQ-0273 LSE-61 (p. 2)	LVV-104 LVV-104 (p. 2)	LVV-T53 LDM-639		Not Executed
DMS-REQ-0274 LSE-61 (p. 1b)	LVV-105 LVV-105 (p. 1b)	LVV-T54 LDM-639		Not Executed
DMS-REQ-0275 LSE-61 (p. 1b)	LVV-106 LVV-106 (p. 1b)	LVV-T12	2019-05-22	Passed
		LDM-639	LVV-P43	
		LVV-T14	2019-05-22	Pass w/Deviation
		LDM-639	LVV-P43	
		LVV-T67		Not Executed
		LDM-639		
DMS-REQ-0276 LSE-61 (p. 1b)	LVV-107 LVV-107 (p. 1b)	LVV-T69 LDM-639		Not Executed
DMS-REQ-0277 LSE-61 (p. 1b)	LVV-108 LVV-108 (p. 1b)			
DMS-REQ-0278 LSE-61 (p. 1b)	LVV-109 LVV-109 (p. 1b)	LVV-T16	2019-05-22	Passed
		LDM-639	LVV-P43	
		LVV-T72	2022-10-19	Passed
		LDM-639	LVV-P99	
DMS-REQ-0279 LSE-61 (p. 1b)	LVV-110 LVV-110 (p. 1b)	LVV-T12	2019-05-22	Passed
		LDM-639	LVV-P43	
		LVV-T16	2019-05-22	Passed
		LDM-639	LVV-P43	
		LVV-T73		Not Executed
		LDM-639		
DMS-REQ-0280 LSE-61 (p. 1b)	LVV-111 LVV-111 (p. 1b)	LVV-T74 LDM-639	2022-08-22	Passed
			LVV-P99	

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0281 LSE-61 (p. 1b)	LVV-112 LVV-112 (p. 1b)	LVV-T75 LDM-639		Not Executed
DMS-REQ-0282 LSE-61 (p. 1a)	LVV-113 LVV-113 (p. 1a)	LVV-T90 LDM-639	2022-08-29 LVV-P99	Passed
	LVV-18881 LVV-18881 (p. 1a)	LVV-T1862 LDM-639		Not Executed
DMS-REQ-0283 LSE-61 (p. 1b)	LVV-114 LVV-114 (p. 1b)	LVV-T91 LDM-639	2022-08-29 LVV-P99	Passed
DMS-REQ-0284 LSE-61 (p. 1b)	LVV-115 LVV-115 (p. 1b)	LVV-T107 LDM-639		Not Executed
		LVV-T283 LDM-639	2019-05-22 LVV-P45	Passed
		LVV-T284 LDM-639	2019-06-24 LVV-P10	Passed
		LVV-T286 LDM-639	2019-05-22 LVV-P45	Passed
DMS-REQ-0285 LSE-61 (p. 1b)	LVV-116 LVV-116 (p. 1b)	LVV-T22 LDM-639	2019-05-22 LVV-P44	Passed
		LVV-T108 LDM-639		Not Executed
		LVV-T550 ?		Not Executed
DMS-REQ-0286 LSE-61 (p. 2)	LVV-117 LVV-117 (p. 2)	LVV-T109 LDM-639		Not Executed
DMS-REQ-0287 LSE-61 (p. 1b)	LVV-118 LVV-118 (p. 1b)	LVV-T110 LDM-639		Not Executed
	LVV-9746 LVV-9746 (p. 1b)	LVV-T110 LDM-639		Not Executed
	LVV-9747 LVV-9747 (p. 1b)	LVV-T110 LDM-639		Not Executed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0288 LSE-61 (p. 2)	LVV-119 LVV-119 (p. 2)	LVV-T111 LDM-639		Not Executed
DMS-REQ-0289 LSE-61 (p. 1a)	LVV-120 LVV-120 (p. 1a)	LVV-T115 LDM-639	LVV-P72	Not Executed
		LVV-T1935 LDM-639	2020-10-28 LVV-P76	Passed
		LVV-T1987 LDM-639	2022-04-07 LVV-P77	Passed
DMS-REQ-0290 LSE-61 (p. 2)	LVV-121 LVV-121 (p. 2)	LVV-T122 LDM-639		Not Executed
DMS-REQ-0291 LSE-61 (p. 1b)	LVV-122 LVV-122 (p. 1b)	LVV-T96 LDM-639		Not Executed
DMS-REQ-0292 LSE-61 (p. 1a)	LVV-123 LVV-123 (p. 1a)	LVV-T97 LDM-639		Not Executed
DMS-REQ-0293 LSE-61 (p. 1a)	LVV-124 LVV-124 (p. 1a)	LVV-T11 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T98 LDM-639		Not Executed
		LVV-T1986		Not Executed
DMS-REQ-0294 LSE-61 (p. 1b)	LVV-125 LVV-125 (p. 1b)	LVV-T12 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T99 LDM-639		Not Executed
		LVV-T1986		Not Executed
DMS-REQ-0295 LSE-61 (p. 2)	LVV-126 LVV-126 (p. 2)	LVV-T100 LDM-639		Not Executed
DMS-REQ-0296 LSE-61 (p. 1a)	LVV-127 LVV-127 (p. 1a)	LVV-T132 LDM-639	2020-02-06 LVV-P65	Passed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0297 LSE-61 (p. 1a)	LVV-128 LVV-128 (p. 1a)	LVV-T146 LDM-639	LVV-P71	Not Executed
DMS-REQ-0298 LSE-61 (p. 1a)	LVV-129 LVV-129 (p. 1a)	LVV-T136 LDM-639	2022-10-11 LVV-P99	Passed
		LVV-T368 LDM-639		Not Executed
		LVV-T374 LDM-639	2018-12-06 LVV-P16	Passed
		LVV-T2692 LDM-639	2022-10-11 LVV-P99	Passed
		LVV-T2693 LDM-639		Not Executed
	LVV-20528 LVV-20528 (p. 1a)	LVV-T2697 LDM-639	2022-10-11 LVV-P99	Passed
		LVV-T2698 LDM-639	2022-10-11 LVV-P99	Passed
		LVV-T2699 LDM-639		Not Executed
DMS-REQ-0299 LSE-61 (p. 1a)	LVV-130 LVV-130 (p. 1a)	LVV-T137 LDM-639	2022-08-29 LVV-P99	Passed
		LVV-T374 LDM-639	2018-12-06 LVV-P16	Passed
		LVV-T1934 LDM-639	2020-10-28 LVV-P76	Passed
		LVV-T1935 LDM-639	2020-10-28 LVV-P76	Passed
		LVV-T1985 LDM-639	2022-05-13 LVV-P77	Passed
DMS-REQ-0300 LSE-61 (p. 1b)	LVV-131 LVV-131 (p. 1b)	LVV-T138 LDM-639		Not Executed



Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0301 LSE-61 (p. 1b)	LVV-132 LVV-132 (p. 1b)	LVV-T147 LDM-639		Not Executed
DMS-REQ-0302 LSE-61 (p. 1a)	LVV-133 LVV-133 (p. 1a)	LVV-T140 LDM-639	2022-09-28 LVV-P99	Passed
DMS-REQ-0303 LSE-61 (p. 1a)	LVV-134 LVV-134 (p. 1a)	LVV-T141 LDM-639	2022-09-28 LVV-P99	Passed
DMS-REQ-0304 LSE-61 (p. 1a)	LVV-135 LVV-135 (p. 1a)	LVV-T142 LDM-639		Not Executed
DMS-REQ-0305 LSE-61 (p. 1a)	LVV-136 LVV-136 (p. 1a)	LVV-T11 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T144 LDM-639	2022-08-30 LVV-P99	Passed
DMS-REQ-0306 LSE-61 (p. 1a)	LVV-137 LVV-137 (p. 1a)	LVV-T11 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T145 LDM-639	2022-08-30 LVV-P99	Passed
DMS-REQ-0307 LSE-61 (p. 2)	LVV-138 LVV-138 (p. 2)	LVV-T148 LDM-639		Not Executed
DMS-REQ-0308 LSE-61 (p. 1b)	LVV-139 LVV-139 (p. 1b)	LVV-T10 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T17 LDM-639	2019-05-22 LVV-P44	Passed
		LVV-T124 LDM-639		Not Executed
		LVV-T216 LDM-639	2018-07-17	Pass w/Deviation
		LVV-T362 LDM-639	2022-03-20	Pass w/Deviation
		LVV-T363 LDM-639	2022-03-19	Passed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0309 LSE-61 (p. 1a)	LVV-140 LVV-140 (p. 1a)	LVV-T154		Not Executed
		LDM-639		
		LVV-T287		Not Executed
		LVV-T454	2019-09-16	Passed
		LDM-639	LVV-P32	
DMS-REQ-0310 LSE-61 (p. 1b)	LVV-141 LVV-141 (p. 1b)	LVV-T155 LDM-639		Not Executed
DMS-REQ-0311 LSE-61 (p. 1b)	LVV-142 LVV-142 (p. 1b)	LVV-T156 LDM-639		Not Executed
DMS-REQ-0312 LSE-61 (p. 1b)	LVV-143 LVV-143 (p. 1b)	LVV-T157 LDM-639		Not Executed
DMS-REQ-0313 LSE-61 (p. 1b)	LVV-144 LVV-144 (p. 1b)	LVV-T158 LDM-639		Not Executed
DMS-REQ-0314 LSE-61 (p. 1b)	LVV-145 LVV-145 (p. 1b)	LVV-T179		Not Executed
		LDM-639		
		LVV-T287		Not Executed
		LDM-639		
DMS-REQ-0315 LSE-61 (p. 1a)	LVV-146 LVV-146 (p. 1a)	LVV-T183		Not Executed
		LDM-639		
		LVV-T283	2019-05-22	Passed
		LDM-639	LVV-P45	
		LVV-T284	2019-06-24	Passed
		LDM-639	LVV-P10	
		LVV-T1549	2020-08-20	Passed
		LDM-639	LVV-P50	
		LVV-T1556	2020-09-03	Passed
		LDM-639	LVV-P55	
DMS-REQ-0316 LSE-61 (p. 1a)	LVV-147 LVV-147 (p. 1a)	LVV-T191 LDM-639	LVV-P72	Not Executed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0317 LSE-61 (p. 2)	LVV-148 LVV-148 (p. 2)	LVV-T55 LDM-639	2022-10-24 LVV-P99	Passed
DMS-REQ-0318 LSE-61 (p. 1b)	LVV-149 LVV-149 (p. 1b)	LVV-T180 LDM-639		Not Executed
		LVV-T287 LDM-639		Not Executed
DMS-REQ-0319 LSE-61 (p. 1b)	LVV-150 LVV-150 (p. 1b)	LVV-T56 LDM-639		Not Executed
DMS-REQ-0320 LSE-61 (p. 2)	LVV-151 LVV-151 (p. 2)	LVV-T92 LDM-639		Not Executed
DMS-REQ-0321 LSE-61 (p. 2)	LVV-152 LVV-152 (p. 2)	LVV-T93 LDM-639		Not Executed
DMS-REQ-0322 LSE-61 (p. 1b)	LVV-153 LVV-153 (p. 1b)	LVV-T94 LDM-639		Not Executed
DMS-REQ-0323 LSE-61 (p. 3)	LVV-154 LVV-154 (p. 3)	LVV-T57 LDM-639		Not Executed
DMS-REQ-0324 LSE-61 (p. 1b)	LVV-155 LVV-155 (p. 1b)	LVV-T58 LDM-639		Not Executed
DMS-REQ-0325 LSE-61 (p. 2)	LVV-156 LVV-156 (p. 2)	LVV-T59 LDM-639		Not Executed
DMS-REQ-0326 LSE-61 (p. 2)	LVV-157 LVV-157 (p. 2)	LVV-T23 LDM-639		Not Executed
DMS-REQ-0327 LSE-61 (p. 1b)	LVV-158 LVV-158 (p. 1b)	LVV-T15 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T19 LDM-639	2019-05-22 LVV-P44	Passed
		LVV-T43 LDM-639	2022-08-31 LVV-P99	Passed
DMS-REQ-0328 LSE-61 (p. 1b)	LVV-159 LVV-159 (p. 1b)	LVV-T44 LDM-639		Not Executed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0329 LSE-61 (p. 2)	LVV-160 LVV-160 (p. 2)	LVV-T76 LDM-639		Not Executed
DMS-REQ-0330 LSE-61 (p. 2)	LVV-161 LVV-161 (p. 2)	LVV-T77 LDM-639	2022-08-30 LVV-P99	Passed
DMS-REQ-0331 LSE-61 (p. 1b)	LVV-162 LVV-162 (p. 1b)	LVV-T13 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T14 LDM-639	2019-05-22 LVV-P43	Pass w/Deviation
		LVV-T21 LDM-639	2019-05-22 LVV-P44	Passed
		LVV-T22 LDM-639	2019-05-22 LVV-P44	Passed
		LVV-T24 LDM-639		Not Executed
DMS-REQ-0332 LSE-61 (p. 2)	LVV-163 LVV-163 (p. 2)	LVV-T25 LDM-639		Not Executed
DMS-REQ-0333 LSE-61 (p. 1b)	LVV-164 LVV-164 (p. 1b)	LVV-T26 LDM-639		Not Executed
DMS-REQ-0334 LSE-61 (p. 1b)	LVV-165 LVV-165 (p. 1b)	LVV-T12 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T13 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T14 LDM-639	2019-05-22 LVV-P43	Pass w/Deviation
		LVV-T15 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T16 LDM-639	2019-05-22 LVV-P43	Passed
		LVV-T78 LDM-639	2022-08-30 LVV-P99	Passed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0335 LSE-61 (p. 2)	LVV-166 LVV-166 (p. 2)	LVV-T79 LDM-639	2022-10-24 LVV-P99	Passed
DMS-REQ-0336 LSE-61 (p. 1b)	LVV-167 LVV-167 (p. 1b)	LVV-T159 LDM-639		Not Executed
DMS-REQ-0337 LSE-61 (p. 2)	LVV-168 LVV-168 (p. 2)	LVV-T80 LDM-639		Not Executed
DMS-REQ-0338 LSE-61 (p. 2)	LVV-169 LVV-169 (p. 2)	LVV-T81 LDM-639		Not Executed
DMS-REQ-0339 LSE-61 (p. 1a)	LVV-170 LVV-170 (p. 1a)	LVV-T82 LDM-639		Not Executed
DMS-REQ-0340 LSE-61 (p. 2)	LVV-171 LVV-171 (p. 2)	LVV-T123 LDM-639		Not Executed
DMS-REQ-0341 LSE-61 (p. 1b)	LVV-172 LVV-172 (p. 1b)	LVV-T160 LDM-639		Not Executed
	LVV-9749 LVV-9749 (p. 1b)	LVV-T2331 LDM-639		Not Executed
DMS-REQ-0342 LSE-61 (p. 2)	LVV-173 LVV-173 (p. 2)	LVV-T112 LDM-639		Not Executed
		LVV-T218 LDM-639	2018-07-04	Pass w/Deviation
DMS-REQ-0343 LSE-61 (p. 2)	LVV-174 LVV-174 (p. 2)	LVV-T113 LDM-639		Not Executed
		LVV-T218 LDM-639	2018-07-04	Pass w/Deviation
		LVV-9748 LVV-9748 (p. 2)	LVV-T1252 LDM-639	
DMS-REQ-0004 LSE-61 (p. 1b)	LVV-175 LVV-175 (p. 1b)	LVV-T35 LDM-639		Not Executed
		LVV-T95 LDM-639		Not Executed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
	LVV-9740	LVV-T1276		Not Executed
	LVV-9740 (p. 1b)	LDM-639		
	LVV-9803	LVV-T102		Not Executed
	LVV-9803 (p. 1b)	LDM-639		
DMS-REQ-0345 LSE-61 (p. 2)	LVV-176 LVV-176 (p. 2)	LVV-T161 LDM-639		Not Executed
DMS-REQ-0346 LSE-61 (p. 1b)	LVV-177 LVV-177 (p. 1b)	LVV-T27		Not Executed
		LVV-T286	2019-05-22	Passed
		LDM-639	LVV-P45	
		LVV-T1934	2020-10-28	Passed
			LVV-P76	
DMS-REQ-0347 LSE-61 (p. 1b)	LVV-178 LVV-178 (p. 1b)	LVV-T28	2022-08-17	Passed
		LDM-639	LVV-P99	
		LVV-T1946	2022-08-17	Passed
		LDM-639	LVV-P99	
		LVV-T1947	2022-08-17	Passed
		LDM-639	LVV-P99	
DMS-REQ-0348 LSE-61 (p. 2)	LVV-179 LVV-179 (p. 2)	LVV-T114		Not Executed
		LDM-639		
		LVV-T218	2018-07-04	Pass w/Deviation
		LDM-639		
DMS-REQ-0349 LSE-61 (p. 2)	LVV-180 LVV-180 (p. 2)	LVV-T71 LDM-639		Not Executed
DMS-REQ-0350 LSE-61 (p. 2)	LVV-181 LVV-181 (p. 2)	LVV-T116 LDM-639		Not Executed
DMS-REQ-0351 LSE-61 (p. 1a)	LVV-182 LVV-182 (p. 1a)	LVV-T133 LDM-639	2020-09-29 LVV-P71	Passed
DMS-REQ-0352 LSE-61 (p. 2)	LVV-183 LVV-183 (p. 2)	LVV-T192 LDM-639		Not Executed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
	LVV-18491 LVV-18491 (p. 2)	LVV-T181 LDM-639		Not Executed
DMS-REQ-0353 LSE-61 (p. 1b)	LVV-184 LVV-184 (p. 1b)	LVV-T60 LDM-639		Not Executed
DMS-REQ-0354 LSE-61 (p. 1b)	LVV-185 LVV-185 (p. 1b)	LVV-T1086 LDM-639	2019-07-08 LVV-P46	Passed
		LVV-T1087 LDM-639	2019-07-08 LVV-P46	Passed
		LVV-T1088 LDM-639	2019-07-09 LVV-P46	Passed
		LVV-T1089 LDM-639	2019-07-09 LVV-P46	Pass w/Deviation
		LVV-T1090 LDM-639	2019-07-09 LVV-P46	Pass w/Deviation
		LVV-T2700 LDM-639		Not Executed
DMS-REQ-0355 LSE-61 (p. 1b)	LVV-186 LVV-186 (p. 1b)	LVV-T2332 LDM-639		Not Executed
	LVV-9784 LVV-9784 (p. 1b)	LVV-T2333 LDM-639		Not Executed
DMS-REQ-0356 LSE-61 (p. 1b)	LVV-187 LVV-187 (p. 1b)			
	LVV-9785 LVV-9785 (p. 1b)			
	LVV-9786 LVV-9786 (p. 1b)	LVV-T1089 LDM-639	2019-07-09 LVV-P46	Pass w/Deviation
		LVV-T1090 LDM-639	2019-07-09 LVV-P46	Pass w/Deviation
	LVV-9787 LVV-9787 (p. 1b)	LVV-T1085 LDM-639	2019-07-08 LVV-P46	Passed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T1089 LDM-639	2019-07-09 LVV-P46	Pass w/Deviation
		LVV-T1090 LDM-639	2019-07-09 LVV-P46	Pass w/Deviation
DMS-REQ-0357 LSE-61 (p. 1b)	LVV-188 LVV-188 (p. 1b)	LVV-T1086 LDM-639	2019-07-08 LVV-P46	Passed
		LVV-T1088 LDM-639	2019-07-09 LVV-P46	Passed
		LVV-T1089 LDM-639	2019-07-09 LVV-P46	Pass w/Deviation
		LVV-T1090 LDM-639	2019-07-09 LVV-P46	Pass w/Deviation
		LVV-T2724 LDM-639		Not Executed
DMS-REQ-0363 LSE-61 (p. 3)	LVV-189 LVV-189 (p. 3)	LVV-T162 LDM-639		Not Executed
DMS-REQ-0364 LSE-61 (p. 3)	LVV-190 LVV-190 (p. 3)	LVV-T163 LDM-639		Not Executed
	LVV-9750 LVV-9750 (p. 3)			
DMS-REQ-0365 LSE-61 (p. 2)	LVV-191 LVV-191 (p. 2)	LVV-T164 LDM-639		Not Executed
DMS-REQ-0366 LSE-61 (p. 2)	LVV-192 LVV-192 (p. 2)	LVV-T165 LDM-639		Not Executed
DMS-REQ-0367 LSE-61 (p. 2)	LVV-193 LVV-193 (p. 2)	LVV-T166 LDM-639		Not Executed
DMS-REQ-0368 LSE-61 (p. 3)	LVV-194 LVV-194 (p. 3)	LVV-T167 LDM-639		Not Executed
DMS-REQ-0369 LSE-61 (p. 1b)	LVV-195 LVV-195 (p. 1b)	LVV-T168 LDM-639		Not Executed



Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0370 LSE-61 (p. 3)	LVV-196 LVV-196 (p. 3)	LVV-T169 LDM-639		<b>Not Executed</b>
DMS-REQ-0371 LSE-61 (p. 3)	LVV-197 LVV-197 (p. 3)	LVV-T170 LDM-639		<b>Not Executed</b>
DMS-REQ-0377 LSE-61 (p. 1b)	LVV-3394 LVV-3394 (p. 1b)	LVV-T385 LDM-639		<b>Not Executed</b>
	LVV-9797 LVV-9797 (p. 1b)	LVV-T1332 LDM-639		<b>Not Executed</b>
DMS-REQ-0374 LSE-61 (p. 1b)	LVV-3395 LVV-3395 (p. 1b)			
	LVV-9790 LVV-9790 (p. 1b)			
	LVV-9791 LVV-9791 (p. 1b)			
DMS-REQ-0376 LSE-61 (p. 1b)	LVV-3396 LVV-3396 (p. 1b)			
	LVV-9795 LVV-9795 (p. 1b)			
	LVV-9796 LVV-9796 (p. 1b)			
DMS-REQ-0373 LSE-61 (p. 2)	LVV-3397 LVV-3397 (p. 2)			
	LVV-9789 LVV-9789 (p. 2)			
DMS-REQ-0375 LSE-61 (p. 2)	LVV-3398 LVV-3398 (p. 2)			
	LVV-9792 LVV-9792 (p. 2)			
	LVV-9793 LVV-9793 (p. 2)			

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
	LVV-9794 LVV-9794 (p. 2)	LVV-T2302 LDM-639		Not Executed
DMS-REQ-0378 LSE-61 (p. 2)	LVV-3399 LVV-3399 (p. 2)			
DMS-REQ-0358 LSE-61 (p. 1a)	LVV-3400 LVV-3400 (p. 1a)	LVV-T1250 LDM-639		Not Executed
	LVV-9788 LVV-9788 (p. 1a)	LVV-T1251 LDM-639		Not Executed
DMS-REQ-0359 LSE-61 (p. 1a)	LVV-3401 LVV-3401 (p. 1a)	LVV-T1756 LDM-639	2022-10-03 LVV-P99	Passed
	LVV-9751 LVV-9751 (p. 1a)	LVV-T377 LDM-639		Not Executed
		LVV-T1847 LDM-639		Not Executed
	LVV-9752 LVV-9752 (p. 1a)	LVV-T1758 LDM-639	2020-09-29 LVV-P71	Not Executed
		LVV-T1759 LDM-639	2020-09-29 LVV-P71	Passed
	LVV-9753 LVV-9753 (p. 1a)	LVV-T377 LDM-639		Not Executed
		LVV-T1846 LDM-639		Not Executed
	LVV-9754 LVV-9754 (p. 1a)	LVV-T1759 LDM-639	2020-09-29 LVV-P71	Passed
	LVV-9755 LVV-9755 (p. 1a)	LVV-T377 LDM-639		Not Executed
		LVV-T1845 LDM-639		Not Executed
	LVV-9756 LVV-9756 (p. 1a)	LVV-T377 LDM-639		Not Executed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T1844 LDM-639		Not Executed
LVV-9757 LVV-9757 (p. 1a)		LVV-T377 LDM-639		Not Executed
		LVV-T1843 LDM-639		Not Executed
LVV-9758 LVV-9758 (p. 1a)		LVV-T1758 LDM-639	2020-09-29 LVV-P71	Not Executed
LVV-9759 LVV-9759 (p. 1a)		LVV-T1757 LDM-639	2020-09-29 LVV-P71	Passed
LVV-9760 LVV-9760 (p. 1a)		LVV-T377 LDM-639		Not Executed
		LVV-T1842 LDM-639		Not Executed
LVV-9761 LVV-9761 (p. 1a)		LVV-T377 LDM-639		Not Executed
		LVV-T1841 LDM-639		Not Executed
LVV-9762 LVV-9762 (p. 1a)		LVV-T377 LDM-639		Not Executed
		LVV-T1840 LDM-639		Not Executed
LVV-9763 LVV-9763 (p. 1a)		LVV-T377 LDM-639		Not Executed
		LVV-T1839 LDM-639		Not Executed
LVV-9764 LVV-9764 (p. 1a)		LVV-T377 LDM-639		Not Executed
		LVV-T1838 LDM-639		Not Executed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
	LVV-9765	LVV-T377		Not Executed
	LVV-9765 (p. 1a)	LDM-639		
		LVV-T1837		Not Executed
		LDM-639		
	LVV-9766	LVV-T377		Not Executed
	LVV-9766 (p. 1a)	LDM-639		
		LVV-T1836		Not Executed
		LDM-639		
	LVV-18339	LVV-T2202		Not Executed
	LVV-18339 (p. 1a)	LDM-639		
DMS-REQ-0360 LSE-61 (p. 1a)	LVV-3402	LVV-T363	2022-03-19	Passed
	LVV-3402 (p. 1a)	LDM-639		
		LVV-T1745	2020-02-06	Passed
		LDM-639	LVV-P65	
	LVV-9767	LVV-T1746	2020-02-06	Passed
	LVV-9767 (p. 1a)	LDM-639	LVV-P65	
	LVV-9768	LVV-T1747	2020-02-06	Passed
	LVV-9768 (p. 1a)	LDM-639	LVV-P65	
	LVV-9769	LVV-T1748	2020-02-06	Passed
	LVV-9769 (p. 1a)	LDM-639	LVV-P65	
	LVV-9770	LVV-T1749	2020-02-06	Passed
	LVV-9770 (p. 1a)	LDM-639	LVV-P65	
	LVV-9771	LVV-T1750	2020-02-06	Passed
	LVV-9771 (p. 1a)	LDM-639	LVV-P65	
	LVV-9773	LVV-T1746	2020-02-06	Passed
	LVV-9773 (p. 1a)	LDM-639	LVV-P65	
	LVV-9774	LVV-T1751	2020-02-06	Passed
	LVV-9774 (p. 1a)	LDM-639	LVV-P65	
	LVV-9775	LVV-T1752	2020-02-06	Passed
	LVV-9775 (p. 1a)	LDM-639	LVV-P65	

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status	
	LVV-9776	LVV-T1749	2020-02-06	Passed	
	LVV-9776 (p. 1a)	LDM-639	LVV-P65		
	LVV-9777	LVV-T1750	2020-02-06		
	LVV-9777 (p. 1a)	LDM-639	LVV-P65		
	LVV-9778	LVV-T1753	2020-02-06	Passed	
	LVV-9778 (p. 1a)	LDM-639	LVV-P65		
	LVV-9779	LVV-T1752	2020-02-06		
	LVV-9779 (p. 1a)	LDM-639	LVV-P65		
DMS-REQ-0361 LSE-61 (p. 1b)	LVV-3403 LVV-3403 (p. 1b)	LVV-T1088	2019-07-09	Passed	
		LDM-639	LVV-P46		
		LVV-T1089	2019-07-09	Pass w/Deviation	
		LDM-639	LVV-P46		
		LVV-T1090	2019-07-09	Pass w/Deviation	
		LDM-639	LVV-P46		
DMS-REQ-0362 LSE-61 (p. 1b)	LVV-3404 LVV-3404 (p. 1b)	LVV-T376		Not Executed	
		LDM-639			
			LVV-T1754	2020-02-06	Passed
			LDM-639	LVV-P65	
	LVV-9780	LVV-T376		Not Executed	
	LVV-9780 (p. 1b)	LDM-639			
	LVV-9781	LVV-T2176		Not Executed	
LVV-9781 (p. 1b)	LDM-639				
LVV-9782	LVV-9782 (p. 1b)	LVV-T1755	2020-02-07	Passed	
		LDM-639	LVV-P65		
	LVV-9783	LVV-T2177		Not Executed	
	LVV-9783 (p. 1b)	LDM-639			
	DMS-REQ-0372 LSE-61 (p. 1a)	LVV-9637 LVV-9637 (p. 1a)	LVV-T1264 LDM-639	2020-11-07 LVV-P71	Passed
	DMS-REQ-0344 LSE-61 (p. 2)	LVV-9744 LVV-9744 (p. 2)	LVV-T1866 LDM-639		Not Executed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
	LVV-18229 LVV-18229 (p. 2)	LVV-T1865 LDM-639		<b>Not Executed</b>
DMS-REQ-0384 LSE-61 (p. 1b)	LVV-18222 LVV-18222 (p. 1b)	LVV-T1524 LDM-639		<b>Not Executed</b>
DMS-REQ-0381 LSE-61 (p. 2)	LVV-18223 LVV-18223 (p. 2)	LVV-T1525 LDM-639		<b>Not Executed</b>
DMS-REQ-0380 LSE-61 (p. 1b)	LVV-18224 LVV-18224 (p. 1b)	LVV-T2716 LDM-540		<b>Not Executed</b>
	LVV-20578 LVV-20578 (p. 2)			
	LVV-20579 LVV-20579 (p. 2)			
DMS-REQ-0382 LSE-61 (p. 1b)	LVV-18225 LVV-18225 (p. 1b)	LVV-T2716 LDM-540		<b>Not Executed</b>
	LVV-20584 LVV-20584			
DMS-REQ-0385 LSE-61 (p. 1b)	LVV-18226 LVV-18226 (p. 1b)	LVV-T1528 LDM-639		<b>Not Executed</b>
DMS-REQ-0379 LSE-61 (p. 1b)	LVV-18227 LVV-18227 (p. 1b)	LVV-T2716 LDM-540		<b>Not Executed</b>
DMS-REQ-0383 LSE-61 (p. 1b)	LVV-18228 LVV-18228 (p. 1b)	LVV-T1530 LDM-639		<b>Not Executed</b>
DMS-REQ-0386 LSE-61 (p. 1b)	LVV-18230 LVV-18230 (p. 1b)	LVV-T1560 LDM-639		<b>Not Executed</b>
DMS-REQ-0387 LSE-61 (p. 1b)	LVV-18231 LVV-18231 (p. 1b)	LVV-T1561 LDM-639		<b>Not Executed</b>
DMS-REQ-0388 LSE-61 (p. 1b)	LVV-18232 LVV-18232 (p. 1b)	LVV-T1562 LDM-639		<b>Not Executed</b>
DMS-REQ-0390 LSE-61 (p. 1b)	LVV-18233 LVV-18233 (p. 1b)	LVV-T1563 LDM-639		<b>Not Executed</b>

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0389 LSE-61 (p. 1b)	LVV-18234 LVV-18234 (p. 1b)	LVV-T1564 LDM-639		Not Executed
DMS-REQ-0394 LSE-61 (p. 1a)	LVV-18295 LVV-18295 (p. 1a)	LVV-T1831 LDM-639		Not Executed
DMS-REQ-0391 LSE-61 (p. 2)	LVV-18297 LVV-18297 (p. 2)	LVV-T1867 LDM-639		Not Executed
	LVV-18911 LVV-18911 (p. 2)	LVV-T1868 LDM-639		Not Executed
DMS-REQ-0392 LSE-61 (p. 2)	LVV-18298 LVV-18298 (p. 2)	LVV-T2091 LDM-639		Not Executed
	LVV-19214 LVV-19214 (p. 2)	LVV-T2092 LDM-639		Not Executed
	LVV-19215 LVV-19215 (p. 2)	LVV-T2093 LDM-639		Not Executed
	LVV-19216 LVV-19216 (p. 2)	LVV-T2094 LDM-639		Not Executed
	LVV-19218 LVV-19218 (p. 2)	LVV-T2095 LDM-639		Not Executed
DMS-REQ-0393 LSE-61 (p. 2)	LVV-18299 LVV-18299 (p. 2)	LVV-T2097 LDM-639		Not Executed
	LVV-19217 LVV-19217 (p. 2)	LVV-T2094 LDM-639		Not Executed
		LVV-T2096 LDM-639		Not Executed
DMS-REQ-0395 LSE-61 (p. 1a)	LVV-18465 LVV-18465 (p. 1a)	LVV-T1830 LDM-639	2022-08-30 LVV-P99	Passed
DMS-REQ-0396 LSE-61 (p. 2)	LVV-18841 LVV-18841 (p. 2)	LVV-T2330 LDM-639		Not Executed
DMS-REQ-0397 LSE-61 (p. 2)	LVV-18847 LVV-18847 (p. 2)	LVV-T1863 LDM-639		Not Executed

Table 3: DM LSE-61 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-REQ-0003 LSE-61 (p. 1a)	LVV-19488 LVV-19488 (p. 1a)	LVV-T2297 LDM-639		Not Executed
DMS-REQ-0398 LSE-61 (p. 1a)	LVV-19489 LVV-19489 (p. 1a)	LVV-T2329 LDM-639		Not Executed
DMS-REQ-0066 LSE-61 (p. 1a)	LVV-19490 LVV-19490 (p. 1a)	LVV-T2303 LDM-639		Not Executed
DMS-REQ-0399 LSE-61 (p. 1b)	LVV-19491 LVV-19491 (p. 1b)	LVV-T2328 LDM-639		Not Executed

### 3.2 LSE-68 Requirements Coverage

Table 4: DM LSE-68 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
CA-DM-DAQ-ICD-0094 LSE-68	LVV-4669 LVV-4669			
	LVV-4670 LVV-4670			
CA-DM-DAQ-ICD-0082 LSE-68	LVV-4675 LVV-4675			
	LVV-4676 LVV-4676			
CA-DM-DAQ-ICD-0093 LSE-68	LVV-4729 LVV-4729			
	LVV-4730 LVV-4730			
CA-DM-DAQ-ICD-0097 LSE-68	LVV-4735 LVV-4735			



Table 4: DM LSE-68 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
	LVV-4736			
	LWV-4736			
CA-DM-DAQ-ICD-0058 LSE-68	LVV-4741			
	LWV-4741			
	LVV-4742			
	LWV-4742			
CA-DM-DAQ-ICD-0059 LSE-68	LVV-4747			
	LWV-4747			
	LVV-4748			
	LWV-4748			
CA-DM-DAQ-ICD-0060 LSE-68	LVV-4753			
	LWV-4753			
	LVV-4754			
	LWV-4754			
CA-DM-DAQ-ICD-0081 LSE-68	LVV-4759			
	LWV-4759			
	LVV-4760			
	LWV-4760			
CA-DM-DAQ-ICD-0047 LSE-68	LVV-4765			
	LWV-4765			
	LVV-4766			
	LWV-4766			
CA-DM-DAQ-ICD-0098 LSE-68	LVV-4771			
	LWV-4771			
	LVV-4772			
	LWV-4772			
CA-DM-DAQ-ICD-0100 LSE-68	LVV-4777			
	LWV-4777			
	LVV-4778			
	LWV-4778			

Table 4: DM LSE-68 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
CA-DM-DAQ-ICD-0092 LSE-68	LVV-4783			
	LWV-4783			
	LVV-4784			
CA-DM-DAQ-ICD-0084 LSE-68	LWV-4789			
	LWV-4789			
	LWV-4790			
CA-DM-DAQ-ICD-0099 LSE-68	LWV-4795			
	LWV-4795			
	LWV-4796			
CA-DM-DAQ-ICD-0085 LSE-68	LWV-4801			
	LWV-4801			
	LWV-4802			
CA-DM-DAQ-ICD-0086 LSE-68	LWV-4807			
	LWV-4807			
	LWV-4808			
CA-DM-DAQ-ICD-0091 LSE-68	LWV-4819			
	LWV-4819			
	LWV-4820			
CA-DM-DAQ-ICD-0075 LSE-68	LWV-4825			
	LWV-4825			
	LWV-4826			
CA-DM-DAQ-ICD-0080 LSE-68	LWV-4831			
	LWV-4831			

Table 4: DM LSE-68 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
	LVV-4832			
	LWV-4832			

### 3.3 LSE-69 Requirements Coverage

Table 5: DM LSE-69 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
CA-DM-CON-ICD-0003 LSE-69	LVV-4843			
	LWV-4843			
	LVV-4844			
	LWV-4844			
CA-DM-CON-ICD-0004 LSE-69	LVV-4849			
	LWV-4849			
	LVV-4850			
	LWV-4850			
CA-DM-CON-ICD-0019 LSE-69	LVV-4855			
	LWV-4855			
	LVV-4856			
	LWV-4856			
CA-DM-CON-ICD-0008 LSE-69	LVV-4861			
	LWV-4861			
	LVV-4862			
	LWV-4862			
CA-DM-CON-ICD-0002 LSE-69	LVV-4873			
	LWV-4873			
	LVV-4874			
	LWV-4874			
CA-DM-CON-ICD-0005 LSE-69	LVV-4879			
	LWV-4879			

Table 5: DM LSE-69 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
	LVV-4880			
	LVV-4880			
CA-DM-CON-ICD-0001	LVV-4885			
LSE-69	LVV-4885			
	LVV-4886			
	LVV-4886			
CA-DM-CON-ICD-0018	LVV-4897			
LSE-69	LVV-4897			
	LVV-4898			
	LVV-4898			
CA-DM-CON-ICD-0007	LVV-4903			
LSE-69	LVV-4903			
	LVV-4904			
	LVV-4904			
CA-DM-CON-ICD-0016	LVV-4909			
LSE-69	LVV-4909			
	LVV-4910			
	LVV-4910			
CA-DM-CON-ICD-0014	LVV-4915			
LSE-69	LVV-4915			
	LVV-4916			
	LVV-4916			
CA-DM-CON-ICD-0015	LVV-4921			
LSE-69	LVV-4921			
	LVV-4922			
	LVV-4922			
CA-DM-CON-ICD-0020	LVV-18849			
LSE-69	LVV-18849			
CA-DM-CON-ICD-0022	LVV-18852			
LSE-69	LVV-18852			

Table 5: DM LSE-69 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
CA-DM-CON-ICD-0023	LWV-18855			
LSE-69	LWV-18855			
CA-DM-CON-ICD-0021	LWV-18858			
LSE-69	LWV-18858			

### 3.4 LSE-72 Requirements Coverage

Table 6: DM LSE-72 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
OCS-DM-COM-ICD-0040	LWV-5237			
LSE-72	LWV-5237			
	LWV-5238			
	LWV-5238			
OCS-DM-COM-ICD-0009	LWV-5243			
LSE-72	LWV-5243			
	LWV-5244			
	LWV-5244			
OCS-DM-COM-ICD-0013	LWV-5249			
LSE-72	LWV-5249			
	LWV-5250			
	LWV-5250			
OCS-DM-COM-ICD-0015	LWV-5255			
LSE-72	LWV-5255			
	LWV-5256			
	LWV-5256			
OCS-DM-COM-ICD-0014	LWV-5261			
LSE-72	LWV-5261			
	LWV-5262			
	LWV-5262			

Table 6: DM LSE-72 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
OCS-DM-COM-ICD-0038 LSE-72	LVV-5267			
	LWV-5267			
	LVV-5268			
OCS-DM-COM-ICD-0039 LSE-72	LWV-5273			
	LWV-5273			
	LWV-5274			
OCS-DM-COM-ICD-0037 LSE-72	LWV-5279			
	LWV-5279			
	LWV-5280			
OCS-DM-COM-ICD-0036 LSE-72	LWV-5285			
	LWV-5285			
	LWV-5286			
OCS-DM-COM-ICD-0012 LSE-72	LWV-5291			
	LWV-5291			
	LWV-5292			
OCS-DM-COM-ICD-0003 LSE-72	LWV-5297			
	LWV-5297			
	LWV-5298			
OCS-DM-COM-ICD-0034 LSE-72	LWV-5303			
	LWV-5303			
	LWV-5304			
OCS-DM-COM-ICD-0032 LSE-72	LWV-5309			
	LWV-5309			

Table 6: DM LSE-72 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
	LVV-5310			
	LW-5310			
OCS-DM-COM-ICD-0006 LSE-72	LVV-5315			
	LW-5315			
	LVV-5316			
	LW-5316			
OCS-DM-COM-ICD-0004 LSE-72	LVV-5321			
	LW-5321			
	LVV-5322			
	LW-5322			
OCS-DM-COM-ICD-0008 LSE-72	LVV-5327			
	LW-5327			
	LVV-5328			
	LW-5328			
OCS-DM-COM-ICD-0033 LSE-72	LVV-5333			
	LW-5333			
	LVV-5334			
	LW-5334			
OCS-DM-COM-ICD-0005 LSE-72	LVV-5339			
	LW-5339			
	LVV-5340			
	LW-5340			
OCS-DM-COM-ICD-0035 LSE-72	LVV-5345			
	LW-5345			
	LVV-5346			
	LW-5346			
OCS-DM-COM-ICD-0007 LSE-72	LVV-5351			
	LW-5351			
	LVV-5352			
	LW-5352			

Table 6: DM LSE-72 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
OCS-DM-COM-ICD-0048 LSE-72	LVV-5357			
	LWV-5357			
	LVV-5358			
OCS-DM-COM-ICD-0055 LSE-72	LWV-5363			
	LWV-5363			
	LWV-5364			
OCS-DM-COM-ICD-0054 LSE-72	LWV-5369			
	LWV-5369			
	LWV-5370			
OCS-DM-COM-ICD-0019 LSE-72	LWV-5375			
	LWV-5375			
	LWV-5376			
OCS-DM-COM-ICD-0017 LSE-72	LWV-5381			
	LWV-5381			
	LWV-5382			
OCS-DM-COM-ICD-0018 LSE-72	LWV-5387			
	LWV-5387			
	LWV-5388			
OCS-DM-COM-ICD-0021 LSE-72	LWV-5393			
	LWV-5393			
	LWV-5394			
OCS-DM-COM-ICD-0020 LSE-72	LWV-5399			
	LWV-5399			



Table 6: DM LSE-72 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
	LVV-5400			
	LWV-5400			
OCS-DM-COM-ICD-0047 LSE-72	LVV-5405			
	LWV-5405			
	LVV-5406			
	LWV-5406			
OCS-DM-COM-ICD-0046 LSE-72	LVV-5411			
	LWV-5411			
	LVV-5412			
	LWV-5412			
OCS-DM-COM-ICD-0045 LSE-72	LVV-5417			
	LWV-5417			
	LVV-5418			
	LWV-5418			
OCS-DM-COM-ICD-0043 LSE-72	LVV-5423			
	LWV-5423			
	LVV-5424			
	LWV-5424			
OCS-DM-COM-ICD-0044 LSE-72	LVV-5429			
	LWV-5429			
	LVV-5430			
	LWV-5430			
OCS-DM-COM-ICD-0052 LSE-72	LVV-5435			
	LWV-5435			
	LVV-5436			
	LWV-5436			
OCS-DM-COM-ICD-0051 LSE-72	LVV-5441			
	LWV-5441			
	LVV-5442			
	LWV-5442			

Table 6: DM LSE-72 Requirements.

<b>Requirement</b>	<b>Verification Element</b>	<b>Test Case</b>	<b>Last Run</b>	<b>Test Status</b>
OCS-DM-COM-ICD-0056 LSE-72	LVV-5447			
	LWV-5447			
	LVV-5448			
OCS-DM-COM-ICD-0050 LSE-72	LWV-5453			
	LWV-5453			
	LWV-5454			
OCS-DM-COM-ICD-0053 LSE-72	LWV-5459			
	LWV-5459			
	LWV-5460			
OCS-DM-COM-ICD-0022 LSE-72	LWV-5465			
	LWV-5465			
	LWV-5466			
OCS-DM-COM-ICD-0049 LSE-72	LWV-5471			
	LWV-5471			
	LWV-5472			
OCS-DM-COM-ICD-0023 LSE-72	LWV-5477			
	LWV-5477			
	LWV-5478			
OCS-DM-COM-ICD-0025 LSE-72	LWV-5483			
	LWV-5483			
	LWV-5484			
OCS-DM-COM-ICD-0029 LSE-72	LWV-5489			
	LWV-5489			

Table 6: DM LSE-72 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
	LVV-5490			
	LWV-5490			
OCS-DM-COM-ICD-0042 LSE-72	LVV-5495			
	LWV-5495			
	LVV-5496			
	LWV-5496			
OCS-DM-COM-ICD-0030 LSE-72	LVV-5501			
	LWV-5501			
	LVV-5502			
	LWV-5502			
OCS-DM-COM-ICD-0026 LSE-72	LVV-5507			
	LWV-5507			
	LVV-5508			
	LWV-5508			
OCS-DM-COM-ICD-0028 LSE-72	LVV-5513			
	LWV-5513			
	LVV-5514			
	LWV-5514			
OCS-DM-COM-ICD-0041 LSE-72	LVV-5519			
	LWV-5519			
	LVV-5520			
	LWV-5520			
OCS-DM-COM-ICD-0027 LSE-72	LVV-5525			
	LWV-5525			
	LVV-5526			
	LWV-5526			
OCS-DM-COM-ICD-0031 LSE-72	LVV-5531			
	LWV-5531			
	LVV-5532			
	LWV-5532			

Table 6: DM LSE-72 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
OCS-DM-COM-ICD-0002 LSE-72	LVV-5537			
	LWV-5537			
	LWV-5538			
OCS-DM-COM-ICD-0001 LSE-72	LWV-5543			
	LWV-5543			
	LWV-5544			

### 3.5 LSE-75 Requirements Coverage

Table 7: DM LSE-75 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DM-TS-CON-ICD-0003 LSE-75	LWV-5628			
	LWV-5628			
	LWV-5629			
DM-TS-CON-ICD-0010 LSE-75	LWV-5634			
	LWV-5634			
	LWV-5635			
DM-TS-CON-ICD-0011 LSE-75	LWV-5640			
	LWV-5640			
	LWV-5641			
DM-TS-CON-ICD-0002 LSE-75	LWV-5646			
	LWV-5646			
	LWV-5647			

Table 7: DM LSE-75 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DM-TS-CON-ICD-0006 LSE-75	LVV-5652			
	LVV-5652			
	LVV-5653			
DM-TS-CON-ICD-0007 LSE-75	LVV-5658			
	LVV-5658			
	LVV-5659			
DM-TS-CON-ICD-0009 LSE-75	LVV-5664			
	LVV-5664			
	LVV-5665			
DM-TS-CON-ICD-0008 LSE-75	LVV-5670			
	LVV-5670			
	LVV-5671			
DM-TS-CON-ICD-0004 LSE-75	LVV-5676			
	LVV-5676			
	LVV-5677			
	LVV-5677			

### 3.6 LSE-130 Requirements Coverage

Table 8: DM LSE-130 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
CA-DM-SUP-ICD-0026 LSE-130	LVV-6140			
	LVV-6140			
	LVV-6141			
	LVV-6141			

Table 8: DM LSE-130 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
CA-DM-SUP-ICD-0027 LSE-130	LVV-6146			
	LWV-6146			
	LVV-6147			
CA-DM-SUP-ICD-0024 LSE-130	LWV-6152			
	LWV-6152			
	LWV-6153			
CA-DM-SUP-ICD-0023 LSE-130	LWV-6158			
	LWV-6158			
	LWV-6159			
CA-DM-SUP-ICD-0025 LSE-130	LWV-6164			
	LWV-6164			
	LWV-6165			
CA-DM-SUP-ICD-0022 LSE-130	LWV-6170			
	LWV-6170			
	LWV-6171			
CA-DM-SUP-ICD-0021 LSE-130	LWV-6176			
	LWV-6176			
	LWV-6177			
CA-DM-SUP-ICD-0028 LSE-130	LWV-6182			
	LWV-6182			
	LWV-6183			
CA-DM-SUP-ICD-0029 LSE-130	LWV-6188			
	LWV-6188			

Table 8: DM LSE-130 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
	LVV-6189			
	LVV-6189			
CA-DM-SUP-ICD-0031 LSE-130	LVV-6194			
	LVV-6194			
	LVV-6195			
	LVV-6195			
CA-DM-SUP-ICD-0030 LSE-130	LVV-6200			
	LVV-6200			
	LVV-6201			
	LVV-6201			
CA-DM-SUP-ICD-0008 LSE-130	LVV-6206			
	LVV-6206			
	LVV-6207			
	LVV-6207			
CA-DM-SUP-ICD-0007 LSE-130	LVV-6212			
	LVV-6212			
	LVV-6213			
	LVV-6213			
CA-DM-SUP-ICD-0009 LSE-130	LVV-6218			
	LVV-6218			
	LVV-6219			
	LVV-6219			
CA-DM-SUP-ICD-0010 LSE-130	LVV-6224			
	LVV-6224			
	LVV-6225			
	LVV-6225			
CA-DM-SUP-ICD-0020 LSE-130	LVV-6230			
	LVV-6230			
	LVV-6231			
	LVV-6231			

Table 8: DM LSE-130 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
CA-DM-SUP-ICD-0019 LSE-130	LVV-6236			
	LVV-6237			
	LVV-6237			
CA-DM-SUP-ICD-0005 LSE-130	LVV-6242			
	LVV-6243			
	LVV-6243			
CA-DM-SUP-ICD-0006 LSE-130	LVV-6248			
	LVV-6249			
	LVV-6249			
CA-DM-SUP-ICD-0002 LSE-130	LVV-6254			
	LVV-6255			
	LVV-6255			
CA-DM-SUP-ICD-0003 LSE-130	LVV-6260			
	LVV-6261			
	LVV-6261			
CA-DM-SUP-ICD-0004 LSE-130	LVV-6266			
	LVV-6267			
	LVV-6267			
CA-DM-SUP-ICD-0016 LSE-130	LVV-6272			
	LVV-6273			
	LVV-6273			
CA-DM-SUP-ICD-0015 LSE-130	LVV-6278			
	LVV-6278			



Table 8: DM LSE-130 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
	LVV-6279			
	LVV-6279			
CA-DM-SUP-ICD-0017 LSE-130	LVV-6284			
	LVV-6284			
	LVV-6285			
	LVV-6285			
CA-DM-SUP-ICD-0014 LSE-130	LVV-6290			
	LVV-6290			
	LVV-6291			
	LVV-6291			
CA-DM-SUP-ICD-0013 LSE-130	LVV-6296			
	LVV-6296			
	LVV-6297			
	LVV-6297			
CA-DM-SUP-ICD-0011 LSE-130	LVV-6302			
	LVV-6302			
	LVV-6303			
	LVV-6303			
CA-DM-SUP-ICD-0012 LSE-130	LVV-6308			
	LVV-6308			
	LVV-6309			
	LVV-6309			
CA-DM-SUP-ICD-0018 LSE-130	LVV-6314			
	LVV-6314			
	LVV-6315			
	LVV-6315			
CA-DM-SUP-ICD-0001 LSE-130	LVV-6320			
	LVV-6320			
	LVV-6321			
	LVV-6321			

Table 8: DM LSE-130 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
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### 3.7 LSE-131 Requirements Coverage

Table 9: DM LSE-131 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
EP-DM-CON-ICD-0004 LSE-131	LWV-6324			
	LWV-6324			
	LWV-6325			
	LWV-6325			
EP-DM-CON-ICD-0021 LSE-131	LWV-6330			
	LWV-6330			
	LWV-6331			
	LWV-6331			
EP-DM-CON-ICD-0009 LSE-131	LWV-6342			
	LWV-6342			
	LWV-6343			
	LWV-6343			
EP-DM-CON-ICD-0034 LSE-131	LWV-6348			
	LWV-6348			
	LWV-6349			
	LWV-6349			
EP-DM-CON-ICD-0031 LSE-131	LWV-6360			
	LWV-6360			
	LWV-6361			
	LWV-6361			
EP-DM-CON-ICD-0019 LSE-131	LWV-6372			
	LWV-6372			
	LWV-6373			
	LWV-6373			

Table 9: DM LSE-131 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
EP-DM-CON-ICD-0002 LSE-131	LWV-6378			
	LWV-6378			
	LWV-6379			
EP-DM-CON-ICD-0033 LSE-131	LWV-6384			
	LWV-6384			
	LWV-6385			
EP-DM-CON-ICD-0032 LSE-131	LWV-6390			
	LWV-6390			
	LWV-6391			
EP-DM-CON-ICD-0020 LSE-131	LWV-6402			
	LWV-6402			
	LWV-6403			
EP-DM-CON-ICD-0036 LSE-131	LWV-6751			
	LWV-6751			
	LWV-6752			
EP-DM-CON-ICD-0035 LSE-131	LWV-6757			
	LWV-6757			
	LWV-6758			

### 3.8 LSE-140 Requirements Coverage

Table 10: DM LSE-140 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DM-TS-AUX-ICD-0020 LSE-140	LVV-6420			
	LWV-6420			
	LVV-6421			
	LWV-6421			
DM-TS-AUX-ICD-0029 LSE-140	LVV-6426			
	LWV-6426			
	LVV-6427			
	LWV-6427			
DM-TS-AUX-ICD-0027 LSE-140	LVV-6432			
	LWV-6432			
	LVV-6433			
	LWV-6433			
DM-TS-AUX-ICD-0022 LSE-140	LVV-6438			
	LWV-6438			
DM-TS-AUX-ICD-0023 LSE-140	LVV-6444			
	LWV-6444			
DM-TS-AUX-ICD-0021 LSE-140	LVV-6450			
	LWV-6450			
DM-TS-AUX-ICD-0025 LSE-140	LVV-6456			
	LWV-6456			
	LVV-6457			
	LWV-6457			
	LVV-6462			
	LWV-6462			
	LVV-6463			
	LWV-6463			
DM-TS-AUX-ICD-0024 LSE-140	LVV-6468			
	LWV-6468			
	LVV-6469			
	LWV-6469			

Table 10: DM LSE-140 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DM-TS-AUX-ICD-0037 LSE-140	LWV-6474			
	LWV-6474			
	LWV-6475			
DM-TS-AUX-ICD-0002 LSE-140	LWV-6480			
	LWV-6480			
	LWV-6481			
DM-TS-AUX-ICD-0001 LSE-140	LWV-6486			
	LWV-6486			
	LWV-6487			
DM-TS-AUX-ICD-0007 LSE-140	LWV-6492			
	LWV-6492			
	LWV-6493			
DM-TS-AUX-ICD-0008 LSE-140	LWV-6498			
	LWV-6498			
	LWV-6499			
DM-TS-AUX-ICD-0010 LSE-140	LWV-6504			
	LWV-6504			
DM-TS-AUX-ICD-0009 LSE-140	LWV-6510			
	LWV-6510			
DM-TS-AUX-ICD-0011 LSE-140	LWV-6516			
	LWV-6516			
DM-TS-AUX-ICD-0006 LSE-140	LWV-6522			
	LWV-6522			
DM-TS-AUX-ICD-0004 LSE-140	LWV-6528			
	LWV-6528			

Table 10: DM LSE-140 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
	LWV-6529			
	LW-6529			
DM-TS-AUX-ICD-0003	LWV-6534			
LSE-140	LW-6534			
	LWV-6535			
	LW-6535			
DM-TS-AUX-ICD-0034	LWV-6540			
LSE-140	LW-6540			
	LWV-6541			
	LW-6541			
DM-TS-AUX-ICD-0036	LWV-6546			
LSE-140	LW-6546			
	LWV-6547			
	LW-6547			
DM-TS-AUX-ICD-0019	LWV-6552			
LSE-140	LW-6552			
	LWV-6553			
	LW-6553			
DM-TS-AUX-ICD-0018	LWV-6558			
LSE-140	LW-6558			
	LWV-6559			
	LW-6559			
DM-TS-AUX-ICD-0014	LWV-6564			
LSE-140	LW-6564			
DM-TS-AUX-ICD-0012	LWV-6570			
LSE-140	LW-6570			
DM-TS-AUX-ICD-0028	LWV-6576			
LSE-140	LW-6576			
DM-TS-AUX-ICD-0035	LWV-6594			
LSE-140	LW-6594			

Table 10: DM LSE-140 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
	LVV-6595			
	LWV-6595			
DM-TS-AUX-ICD-0033 LSE-140	LVV-6600			
	LWV-6600			
	LVV-6601			
	LWV-6601			
DM-TS-AUX-ICD-0032 LSE-140	LVV-6606			
	LWV-6606			
	LVV-6607			
	LWV-6607			

### 3.9 LDM-554 Requirements Coverage

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-LSP-REQ-0007 LDM-554	LVV-9806	LVV-T605		Not Executed
	LWV-9806	LDM-540		
DMS-LSP-REQ-0001 LDM-554	LVV-9807	LVV-T2	2019-05-20	Passed
	LWV-9807	LDM-540	LWV-P42	
		LVV-T598		Not Executed
		LDM-540		
DMS-LSP-REQ-0004 LDM-554	LVV-9808	LVV-T3	2019-05-20	Passed
	LWV-9808	LDM-540	LWV-P42	
		LVV-T602		Not Executed
		LDM-540		
		LVV-T1437	2019-12-09	Pass w/Deviation
		LDM-540	LWV-P48	
DMS-LSP-REQ-0005 LDM-554	LVV-9809	LVV-T2	2019-05-20	Passed
	LWV-9809	LDM-540	LWV-P42	

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T603 LDM-540		Not Executed
		LVV-T1334 LDM-540	2021-10-07 LVV-P48	Passed
		LVV-T1436 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
		LVV-T1437 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
DMS-LSP-REQ-0003 LDM-554	LVV-9810 LVV-9810	LVV-T601 LDM-540		Not Executed
		LVV-T1436 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
DMS-LSP-REQ-0002 LDM-554	LVV-9811 LVV-9811	LVV-T5 LDM-540	2019-05-20 LVV-P42	Passed
		LVV-T600 LDM-540		Not Executed
		LVV-T1334 LDM-540	2021-10-07 LVV-P48	Passed
DMS-LSP-REQ-0006 LDM-554	LVV-9812 LVV-9812	LVV-T604 LDM-540		Not Executed
		LVV-T1334 LDM-540	2021-10-07 LVV-P48	Passed
		LVV-T1436 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
		LVV-T1437 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
DMS-LSP-REQ-0009 LDM-554	LVV-9813 LVV-9813	LVV-T607 LDM-540		Not Executed
DMS-LSP-REQ-0008 LDM-554	LVV-9814 LVV-9814	LVV-T8 LDM-540		Not Executed



Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T9 LDM-540	2019-05-20 LVV-P42	Passed
		LVV-T606 LDM-540		Not Executed
DMS-LSP-REQ-0010 LDM-554	LVV-9815 LVV-9815	LVV-T1436 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
		LVV-T2172 LDM-540	2021-10-01 LVV-P79	Passed
DMS-LSP-REQ-0012 LDM-554	LVV-9816 LVV-9816	LVV-T610 LDM-540		Not Executed
DMS-LSP-REQ-0011 LDM-554	LVV-9817 LVV-9817	LVV-T609 LDM-540		Not Executed
DMS-LSP-REQ-0013 LDM-554	LVV-9818 LVV-9818	LVV-T611 LDM-540		Not Executed
DMS-LSP-REQ-0014 LDM-554	LVV-9819 LVV-9819	LVV-T5 LDM-540	2019-05-20 LVV-P42	Passed
		LVV-T6 LDM-540	2019-05-20 LVV-P42	Pass w/Deviation
		LVV-T7 LDM-540		Not Executed
		LVV-T612 LDM-540		Not Executed
		LVV-T2172 LDM-540	2021-10-01 LVV-P79	Passed
DMS-LSP-REQ-0018 LDM-554	LVV-9820 LVV-9820	LVV-T7 LDM-540		Not Executed
		LVV-T616 LDM-540		Not Executed
		LVV-T2677 LDM-540		Not Executed

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T2718		Not Executed
DMS-LSP-REQ-0017 LDM-554	LVV-9821	LVV-T6 LDM-540	2019-05-20 LVV-P42	Pass w/Deviation
		LVV-T615 LDM-540		Not Executed
		LVV-T2172 LDM-540	2021-10-01 LVV-P79	Passed
DMS-LSP-REQ-0016 LDM-554	LVV-9822	LVV-T614 LDM-540		Not Executed
DMS-LSP-REQ-0015 LDM-554	LVV-9823	LVV-T613 LDM-540		Not Executed
DMS-LSP-REQ-0028 LDM-554	LVV-9824	LVV-T4 LDM-540	2019-05-20 LVV-P42	Passed
		LVV-T617 LDM-540		Not Executed
DMS-LSP-REQ-0029 LDM-554	LVV-9825	LVV-T4 LDM-540	2019-05-20 LVV-P42	Passed
		LVV-T618 LDM-540		Not Executed
DMS-LSP-REQ-0030 LDM-554	LVV-9826	LVV-T619 LDM-540		Not Executed
DMS-LSP-REQ-0031 LDM-554	LVV-9827	LVV-T620 LDM-540		Not Executed
DMS-LSP-REQ-0019 LDM-554	LVV-9828	LVV-T621 LDM-540		Not Executed
DMS-LSP-REQ-0025 LDM-554	LVV-9829	LVV-T627 LDM-540		Not Executed
DMS-LSP-REQ-0020 LDM-554	LVV-9830	LVV-T622 LDM-540	2019-11-25 LVV-P48	Pass w/Deviation

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T1334 LDM-540	2021-10-07 LVV-P48	Passed
		LVV-T1436 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
		LVV-T1437 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
DMS-LSP-REQ-0022 LDM-554	LVV-9831 LVV-9831	LVV-T624 LDM-540		Not Executed
		LVV-T1334 LDM-540	2021-10-07 LVV-P48	Passed
		LVV-T1436 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
		LVV-T1437 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
DMS-LSP-REQ-0021 LDM-554	LVV-9832 LVV-9832	LVV-T623 LDM-540		Not Executed
DMS-LSP-REQ-0027 LDM-554	LVV-9833 LVV-9833	LVV-T629 LDM-540		Not Executed
DMS-LSP-REQ-0023 LDM-554	LVV-9834 LVV-9834	LVV-T625 LDM-540		Not Executed
		LVV-T1334 LDM-540	2021-10-07 LVV-P48	Passed
		LVV-T1436 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
		LVV-T1437 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
DMS-LSP-REQ-0024 LDM-554	LVV-9835 LVV-9835	LVV-T626 LDM-540		Not Executed
		LVV-T1334 LDM-540	2021-10-07 LVV-P48	Passed

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T1436 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
		LVV-T1437 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
DMS-LSP-REQ-0026 LDM-554	LVV-9836 LVV-9836	LVV-T628 LDM-540		Not Executed
		LVV-T1436 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
DMS-LSP-REQ-0033 LDM-554	LVV-9837 LVV-9837	LVV-T631 LDM-540		Not Executed
DMS-LSP-REQ-0034 LDM-554	LVV-9838 LVV-9838	LVV-T632 LDM-540		Not Executed
DMS-LSP-REQ-0032 LDM-554	LVV-9839 LVV-9839	LVV-T630 LDM-540		Not Executed
DMS-LSP-REQ-0035 LDM-554	LVV-9840 LVV-9840	LVV-T633 LDM-540		Not Executed
DMS-PRTL-REQ-0001 LDM-554	LVV-9841 LVV-9841	LVV-T1334 LDM-540	2021-10-07 LVV-P48	Passed
		LVV-T849		Not Executed
DMS-PRTL-REQ-0005 LDM-554	LVV-9842 LVV-9842	LVV-T638 LDM-540		Not Executed
DMS-PRTL-REQ-0007 LDM-554	LVV-9843 LVV-9843	LVV-T640 LDM-540		Not Executed
DMS-PRTL-REQ-0008 LDM-554	LVV-9844 LVV-9844	LVV-T641 LDM-540		Not Executed
DMS-PRTL-REQ-0006 LDM-554	LVV-9845 LVV-9845	LVV-T639 LDM-540		Not Executed
DMS-PRTL-REQ-0003 LDM-554	LVV-9846 LVV-9846	LVV-T636 LDM-540		Not Executed

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T1818 LDM-540	2020-05-11 LVV-P69	Pass w/Deviation
DMS-PRTL-REQ-0002 LDM-554	LVV-9847 LVV-9847	LVV-T635 LDM-540		Not Executed
DMS-PRTL-REQ-0004 LDM-554	LVV-9848 LVV-9848	LVV-T8 LDM-540		Not Executed
		LVV-T637 LDM-540		Not Executed
DMS-PRTL-REQ-0010 LDM-554	LVV-9849 LVV-9849	LVV-T643 LDM-540		Not Executed
DMS-PRTL-REQ-0013 LDM-554	LVV-9850 LVV-9850	LVV-T646 LDM-540		Not Executed
DMS-PRTL-REQ-0012 LDM-554	LVV-9851 LVV-9851	LVV-T645 LDM-540		Not Executed
DMS-PRTL-REQ-0014 LDM-554	LVV-9852 LVV-9852	LVV-T647 LDM-540		Not Executed
DMS-PRTL-REQ-0011 LDM-554	LVV-9853 LVV-9853	LVV-T644 LDM-540		Not Executed
DMS-PRTL-REQ-0009 LDM-554	LVV-9854 LVV-9854	LVV-T642 LDM-540		Not Executed
DMS-PRTL-REQ-0017 LDM-554	LVV-9855 LVV-9855	LVV-T650 LDM-540		Not Executed
		LVV-T1334 LDM-540	2021-10-07 LVV-P48	Passed
DMS-PRTL-REQ-0016 LDM-554	LVV-9856 LVV-9856	LVV-T5 LDM-540	2019-05-20 LVV-P42	Passed
		LVV-T649 LDM-540		Not Executed
		LVV-T1334 LDM-540	2021-10-07 LVV-P48	Passed

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T2172 LDM-540	2021-10-01 LVV-P79	Passed
DMS-PRTL-REQ-0015 LDM-554	LVV-9857 LVV-9857	LVV-T648 LDM-540		Not Executed
		LVV-T1334 LDM-540	2021-10-07 LVV-P48	Passed
		LVV-T2172 LDM-540	2021-10-01 LVV-P79	Passed
DMS-PRTL-REQ-0018 LDM-554	LVV-9858 LVV-9858	LVV-T651 LDM-540		Not Executed
DMS-PRTL-REQ-0028 LDM-554	LVV-9859 LVV-9859	LVV-T5 LDM-540	2019-05-20 LVV-P42	Passed
		LVV-T652 LDM-540		Not Executed
DMS-PRTL-REQ-0029 LDM-554	LVV-9860 LVV-9860	LVV-T653 LDM-540		Not Executed
DMS-PRTL-REQ-0030 LDM-554	LVV-9861 LVV-9861	LVV-T654 LDM-540		Not Executed
DMS-PRTL-REQ-0022 LDM-554	LVV-9862 LVV-9862	LVV-T5 LDM-540	2019-05-20 LVV-P42	Passed
		LVV-T2716 LDM-540		Not Executed
		LVV-T2717		Not Executed
DMS-PRTL-REQ-0023 LDM-554	LVV-9863 LVV-9863	LVV-T658 LDM-540		Not Executed
DMS-PRTL-REQ-0024 LDM-554	LVV-9864 LVV-9864	LVV-T659 LDM-540		Not Executed
DMS-PRTL-REQ-0021 LDM-554	LVV-9865 LVV-9865	LVV-T5 LDM-540	2019-05-20 LVV-P42	Passed

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T656 LDM-540		Not Executed
DMS-PRTL-REQ-0020 LDM-554	LVV-9866 LVV-9866	LVV-T655 LDM-540		Not Executed
		LVV-T1334 LDM-540	2021-10-07 LVV-P48	Passed
		LVV-T2172 LDM-540	2021-10-01 LVV-P79	Passed
		LVV-T2716 LDM-540		Not Executed
		LVV-T2717		Not Executed
DMS-PRTL-REQ-0025 LDM-554	LVV-9867 LVV-9867	LVV-T660 LDM-540		Not Executed
DMS-PRTL-REQ-0027 LDM-554	LVV-9868 LVV-9868	LVV-T5 LDM-540	2019-05-20 LVV-P42	Passed
		LVV-T662 LDM-540		Not Executed
DMS-PRTL-REQ-0026 LDM-554	LVV-9869 LVV-9869	LVV-T5 LDM-540	2019-05-20 LVV-P42	Passed
		LVV-T661 LDM-540		Not Executed
		LVV-T1334 LDM-540	2021-10-07 LVV-P48	Passed
		LVV-T2172 LDM-540	2021-10-01 LVV-P79	Passed
		LVV-T2716 LDM-540		Not Executed
		LVV-T2717		Not Executed

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-PRTL-REQ-0019 LDM-554	LVV-9870	LVV-T663		Not Executed
	LVV-9870	LDM-540		Not Executed
		LVV-T2677	LDM-540	Not Executed
		LVV-T2711		Not Executed
DMS-PRTL-REQ-0034 LDM-554	LVV-9871 LVV-9871	LVV-T668 LDM-540		Not Executed
DMS-PRTL-REQ-0033 LDM-554	LVV-9872 LVV-9872	LVV-T667 LDM-540		Not Executed
DMS-PRTL-REQ-0032 LDM-554	LVV-9873 LVV-9873	LVV-T666 LDM-540		Not Executed
DMS-PRTL-REQ-0031 LDM-554	LVV-9874 LVV-9874	LVV-T2677 LDM-540		Not Executed
DMS-PRTL-REQ-0039 LDM-554	LVV-9875 LVV-9875	LVV-T673	LDM-540	Not Executed
		LVV-T707	LDM-540	Not Executed
		LVV-T2709		Not Executed
		LVV-T2710		Not Executed
		LVV-T2721	LDM-540	Not Executed
DMS-PRTL-REQ-0037 LDM-554	LVV-9876 LVV-9876	LVV-T2677 LDM-540		Not Executed
DMS-PRTL-REQ-0036 LDM-554	LVV-9877 LVV-9877	LVV-T670 LDM-540		Not Executed
DMS-PRTL-REQ-0035 LDM-554	LVV-9878 LVV-9878	LVV-T669 LDM-540		Not Executed



Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T2677 LDM-540		Not Executed
		LVV-T2712		Not Executed
DMS-PRTL-REQ-0038 LDM-554	LVV-9879 LVV-9879	LVV-T672 LDM-540		Not Executed
		LVV-T707 LDM-540		Not Executed
		LVV-T2677 LDM-540		Not Executed
		LVV-T2709		Not Executed
		LVV-T2710		Not Executed
DMS-PRTL-REQ-0041 LDM-554	LVV-9880 LVV-9880	LVV-T7 LDM-540		Not Executed
		LVV-T674 LDM-540		Not Executed
DMS-PRTL-REQ-0040 LDM-554	LVV-9881 LVV-9881	LVV-T7 LDM-540		Not Executed
		LVV-T675 LDM-540		Not Executed
DMS-PRTL-REQ-0044 LDM-554	LVV-9882 LVV-9882	LVV-T679 LDM-540		Not Executed
		LVV-T2677 LDM-540		Not Executed
		LVV-T2721 LDM-540		Not Executed
DMS-PRTL-REQ-0043 LDM-554	LVV-9883 LVV-9883	LVV-T678 LDM-540		Not Executed

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T2677 LDM-540		Not Executed
		LVV-T2721 LDM-540		Not Executed
DMS-PRTL-REQ-0042 LDM-554	LVV-9884 LVV-9884	LVV-T677 LDM-540		Not Executed
		LVV-T2677 LDM-540		Not Executed
		LVV-T2721 LDM-540		Not Executed
DMS-PRTL-REQ-0045 LDM-554	LVV-9885 LVV-9885	LVV-T680 LDM-540		Not Executed
DMS-PRTL-REQ-0046 LDM-554	LVV-9886 LVV-9886	LVV-T681 LDM-540		Not Executed
		LVV-T1818 LDM-540	2020-05-11 LVV-P69	Pass w/Deviation
DMS-PRTL-REQ-0048 LDM-554	LVV-9887 LVV-9887	LVV-T683 LDM-540		Not Executed
DMS-PRTL-REQ-0047 LDM-554	LVV-9888 LVV-9888	LVV-T682 LDM-540		Not Executed
DMS-PRTL-REQ-0050 LDM-554	LVV-9889 LVV-9889	LVV-T6 LDM-540	2019-05-20 LVV-P42	Pass w/Deviation
		LVV-T685 LDM-540		Not Executed
		LVV-T2172 LDM-540	2021-10-01 LVV-P79	Passed
DMS-PRTL-REQ-0052 LDM-554	LVV-9890 LVV-9890	LVV-T687 LDM-540		Not Executed
DMS-PRTL-REQ-0049 LDM-554	LVV-9891 LVV-9891	LVV-T6 LDM-540	2019-05-20 LVV-P42	Pass w/Deviation

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T684 LDM-540		Not Executed
		LVV-T1334 LDM-540	2021-10-07 LVV-P48	Passed
		LVV-T2172 LDM-540	2021-10-01 LVV-P79	Passed
DMS-PRTL-REQ-0051 LDM-554	LVV-9892 LVV-9892	LVV-T686 LDM-540		Not Executed
DMS-PRTL-REQ-0054 LDM-554	LVV-9893 LVV-9893	LVV-T6 LDM-540	2019-05-20 LVV-P42	Pass w/Deviation
		LVV-T689 LDM-540		Not Executed
		LVV-T2172 LDM-540	2021-10-01 LVV-P79	Passed
DMS-PRTL-REQ-0053 LDM-554	LVV-9894 LVV-9894	LVV-T6 LDM-540	2019-05-20 LVV-P42	Pass w/Deviation
		LVV-T688 LDM-540		Not Executed
DMS-PRTL-REQ-0056 LDM-554	LVV-9895 LVV-9895	LVV-T6 LDM-540	2019-05-20 LVV-P42	Pass w/Deviation
		LVV-T691 LDM-540		Not Executed
DMS-PRTL-REQ-0061 LDM-554	LVV-9896 LVV-9896	LVV-T696 LDM-540		Not Executed
DMS-PRTL-REQ-0059 LDM-554	LVV-9897 LVV-9897	LVV-T694 LDM-540		Not Executed
DMS-PRTL-REQ-0058 LDM-554	LVV-9898 LVV-9898	LVV-T693 LDM-540		Not Executed
DMS-PRTL-REQ-0060 LDM-554	LVV-9899 LVV-9899	LVV-T695 LDM-540		Not Executed

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-PRTL-REQ-0057 LDM-554	LVV-9900 LVV-9900	LVV-T692 LDM-540		Not Executed
DMS-PRTL-REQ-0055 LDM-554	LVV-9901 LVV-9901	LVV-T6 LDM-540	2019-05-20 LVV-P42	Pass w/Deviation
		LVV-T690 LDM-540		Not Executed
		LVV-T2172 LDM-540	2021-10-01 LVV-P79	Passed
DMS-PRTL-REQ-0067 LDM-554	LVV-9902 LVV-9902	LVV-T701 LDM-540		Not Executed
DMS-PRTL-REQ-0066 LDM-554	LVV-9903 LVV-9903	LVV-T700 LDM-540		Not Executed
DMS-PRTL-REQ-0065 LDM-554	LVV-9904 LVV-9904	LVV-T2721 LDM-540		Not Executed
DMS-PRTL-REQ-0062 LDM-554	LVV-9905 LVV-9905	LVV-T2677 LDM-540		Not Executed
		LVV-T2712		Not Executed
DMS-PRTL-REQ-0063 LDM-554	LVV-9906 LVV-9906	LVV-T697 LDM-540		Not Executed
DMS-PRTL-REQ-0064 LDM-554	LVV-9907 LVV-9907	LVV-T698 LDM-540		Not Executed
DMS-PRTL-REQ-0068 LDM-554	LVV-9908 LVV-9908	LVV-T702 LDM-540		Not Executed
DMS-PRTL-REQ-0069 LDM-554	LVV-9909 LVV-9909	LVV-T2677 LDM-540		Not Executed
		LVV-T2718		Not Executed
DMS-PRTL-REQ-0074 LDM-554	LVV-9910 LVV-9910	LVV-T2677 LDM-540		Not Executed

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T2718		Not Executed
DMS-PRTL-REQ-0071 LDM-554	LVV-9911 LVV-9911	LVV-T2677 LDM-540		Not Executed
		LVV-T2718		Not Executed
DMS-PRTL-REQ-0072 LDM-554	LVV-9912 LVV-9912	LVV-T706 LDM-540		Not Executed
DMS-PRTL-REQ-0073 LDM-554	LVV-9913 LVV-9913	LVV-T707 LDM-540		Not Executed
DMS-PRTL-REQ-0070 LDM-554	LVV-9914 LVV-9914	LVV-T704 LDM-540		Not Executed
		LVV-T2677 LDM-540		Not Executed
		LVV-T2716 LDM-540		Not Executed
		LVV-T2718		Not Executed
DMS-PRTL-REQ-0075 LDM-554	LVV-9915 LVV-9915	LVV-T2677 LDM-540		Not Executed
		LVV-T2721 LDM-540		Not Executed
DMS-PRTL-REQ-0077 LDM-554	LVV-9916 LVV-9916	LVV-T711 LDM-540		Not Executed
DMS-PRTL-REQ-0076 LDM-554	LVV-9917 LVV-9917	LVV-T710 LDM-540		Not Executed
		LVV-T2172 LDM-540	2021-10-01 LVV-P79	Passed
DMS-PRTL-REQ-0078 LDM-554	LVV-9918 LVV-9918	LVV-T2716 LDM-540		Not Executed

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-PRTL-REQ-0081 LDM-554	LVV-9919	LVV-T715	LDM-540	Not Executed
DMS-PRTL-REQ-0080 LDM-554	LVV-9920	LVV-T2716	LDM-540	Not Executed
DMS-PRTL-REQ-0082 LDM-554	LVV-9921	LVV-T716	LDM-540	Not Executed
DMS-PRTL-REQ-0079 LDM-554	LVV-9922	LVV-T2716	LDM-540	Not Executed
DMS-PRTL-REQ-0087 LDM-554	LVV-9923	LVV-T2677	LDM-540	Not Executed
		LVV-T2718		Not Executed
DMS-PRTL-REQ-0083 LDM-554	LVV-9924	LVV-T717	LDM-540	Not Executed
		LVV-T2677	LDM-540	Not Executed
		LVV-T2716	LDM-540	Not Executed
		LVV-T2718		Not Executed
DMS-PRTL-REQ-0086 LDM-554	LVV-9925	LVV-T2677	LDM-540	Not Executed
		LVV-T2716	LDM-540	Not Executed
		LVV-T2718		Not Executed
DMS-PRTL-REQ-0085 LDM-554	LVV-9926	LVV-T719	LDM-540	Not Executed
		LVV-T2677	LDM-540	Not Executed

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T2718		Not Executed
DMS-PRTL-REQ-0088 LDM-554	LVV-9927 LVV-9927	LVV-T722 LDM-540		Not Executed
DMS-PRTL-REQ-0084 LDM-554	LVV-9928 LVV-9928	LVV-T2677 LDM-540		Not Executed
		LVV-T2716 LDM-540		Not Executed
DMS-PRTL-REQ-0091 LDM-554	LVV-9929 LVV-9929	LVV-T725 LDM-540		Not Executed
DMS-PRTL-REQ-0093 LDM-554	LVV-9930 LVV-9930	LVV-T727 LDM-540		Not Executed
DMS-PRTL-REQ-0092 LDM-554	LVV-9931 LVV-9931	LVV-T726 LDM-540		Not Executed
		LVV-T2172 LDM-540	2021-10-01 LVV-P79	Passed
DMS-PRTL-REQ-0095 LDM-554	LVV-9932 LVV-9932	LVV-T729 LDM-540		Not Executed
		LVV-T1334 LDM-540	2021-10-07 LVV-P48	Passed
		LVV-T1818 LDM-540	2020-05-11 LVV-P69	Pass w/Deviation
		LVV-T2172 LDM-540	2021-10-01 LVV-P79	Passed
DMS-PRTL-REQ-0090 LDM-554	LVV-9933 LVV-9933	LVV-T724 LDM-540		Not Executed
		LVV-T2172 LDM-540	2021-10-01 LVV-P79	Passed
DMS-PRTL-REQ-0089 LDM-554	LVV-9934 LVV-9934	LVV-T723 LDM-540		Not Executed

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-PRTL-REQ-0094 LDM-554	LVV-9935	LVV-T728		Not Executed
	LVV-9935	LDM-540		
DMS-PRTL-REQ-0096 LDM-554	LVV-9936	LVV-T730		Not Executed
	LVV-9936	LDM-540		
DMS-PRTL-REQ-0097 LDM-554	LVV-9937	LVV-T2677		Not Executed
	LVV-9937	LDM-540		
		LVV-T2718		Not Executed
DMS-PRTL-REQ-0105 LDM-554	LVV-9938	LVV-T739		Not Executed
	LVV-9938	LDM-540		
DMS-PRTL-REQ-0107 LDM-554	LVV-9939	LVV-T741		Not Executed
	LVV-9939	LDM-540		
		LVV-T2172	2021-10-01	Passed
		LDM-540	LVV-P79	
DMS-PRTL-REQ-0102 LDM-554	LVV-9940	LVV-T736		Not Executed
	LVV-9940	LDM-540		
DMS-PRTL-REQ-0106 LDM-554	LVV-9941	LVV-T740		Not Executed
	LVV-9941	LDM-540		
		LVV-T2172	2021-10-01	Passed
		LDM-540	LVV-P79	
		LVV-T2677		Not Executed
		LDM-540		
		LVV-T2712		Not Executed
		LDM-540		
		LVV-T2721		Not Executed
		LDM-540		
DMS-PRTL-REQ-0098 LDM-554	LVV-9942	LVV-T732		Not Executed
	LVV-9942	LDM-540		
		LVV-T2171	2021-10-01	Passed
		LDM-540	LVV-P79	



Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T2172 LDM-540	2021-10-01 LVV-P79	Passed
	LVV-20546 LVV-20546			
DMS-PRTL-REQ-0099 LDM-554	LVV-9943 LVV-9943	LVV-T733 LDM-540		Not Executed
DMS-PRTL-REQ-0100 LDM-554	LVV-9944 LVV-9944	LVV-T734 LDM-540		Not Executed
DMS-PRTL-REQ-0101 LDM-554	LVV-9945 LVV-9945	LVV-T735 LDM-540		Not Executed
DMS-PRTL-REQ-0104 LDM-554	LVV-9946 LVV-9946	LVV-T738 LDM-540		Not Executed
DMS-PRTL-REQ-0108 LDM-554	LVV-9947 LVV-9947	LVV-T742 LDM-540		Not Executed
DMS-PRTL-REQ-0103 LDM-554	LVV-9948 LVV-9948	LVV-T737 LDM-540		Not Executed
DMS-PRTL-REQ-0109 LDM-554	LVV-9949 LVV-9949	LVV-T743 LDM-540		Not Executed
DMS-PRTL-REQ-0113 LDM-554	LVV-9950 LVV-9950	LVV-T747 LDM-540		Not Executed
DMS-PRTL-REQ-0111 LDM-554	LVV-9951 LVV-9951	LVV-T745 LDM-540		Not Executed
		LVV-T1818 LDM-540	2020-05-11 LVV-P69	Pass w/Deviation
		LVV-T2677 LDM-540		Not Executed
		LVV-T2718		Not Executed
DMS-PRTL-REQ-0114 LDM-554	LVV-9952 LVV-9952	LVV-T748 LDM-540		Not Executed

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-PRTL-REQ-0112 LDM-554	LVV-9953	LVV-T746		Not Executed
DMS-PRTL-REQ-0110 LDM-554	LVV-9954	LVV-T744		Not Executed
		LVV-T1818	2020-05-11	Pass w/Deviation
		LDM-540	LVV-P69	
DMS-PRTL-REQ-0115 LDM-554	LVV-9955	LVV-T749		Not Executed
DMS-PRTL-REQ-0117 LDM-554	LVV-9956	LVV-T751		Not Executed
DMS-PRTL-REQ-0118 LDM-554	LVV-9957	LVV-T752		Not Executed
DMS-PRTL-REQ-0116 LDM-554	LVV-9958	LVV-T750		Not Executed
DMS-PRTL-REQ-0127 LDM-554	LVV-9959	LVV-T756		Not Executed
DMS-PRTL-REQ-0119 LDM-554	LVV-9960	LVV-T753		Not Executed
DMS-PRTL-REQ-0120 LDM-554	LVV-9961	LVV-T754		Not Executed
DMS-PRTL-REQ-0121 LDM-554	LVV-9962	LVV-T755		Not Executed
DMS-PRTL-REQ-0122 LDM-554	LVV-9963	LVV-T757		Not Executed
DMS-PRTL-REQ-0124 LDM-554	LVV-9964	LVV-T759		Not Executed
DMS-PRTL-REQ-0123 LDM-554	LVV-9965	LVV-T758		Not Executed
DMS-PRTL-REQ-0126 LDM-554	LVV-9966	LVV-T761		Not Executed

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-PRTL-REQ-0125 LDM-554	LVV-9967 LVV-9967	LVV-T760 LDM-540		Not Executed
DMS-NB-REQ-0010 LDM-554	LVV-9968 LVV-9968	LVV-T767 LDM-540		Not Executed
DMS-NB-REQ-0009 LDM-554	LVV-9969 LVV-9969	LVV-T766 LDM-540		Not Executed
		LVV-T2171 LDM-540	2021-10-01 LVV-P79	Passed
DMS-NB-REQ-0014 LDM-554	LVV-9970 LVV-9970	LVV-T771 LDM-540		Not Executed
DMS-NB-REQ-0005 LDM-554	LVV-9971 LVV-9971	LVV-T762 LDM-540		Not Executed
		LVV-T1436 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
		LVV-T2171 LDM-540	2021-10-01 LVV-P79	Passed
DMS-NB-REQ-0015 LDM-554	LVV-9972 LVV-9972	LVV-T772 LDM-540		Not Executed
DMS-NB-REQ-0013 LDM-554	LVV-9973 LVV-9973	LVV-T1436 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
DMS-NB-REQ-0007 LDM-554	LVV-9974 LVV-9974	LVV-T2171 LDM-540	2021-10-01 LVV-P79	Passed
DMS-NB-REQ-0008 LDM-554	LVV-9975 LVV-9975	LVV-T765 LDM-540		Not Executed
DMS-NB-REQ-0006 LDM-554	LVV-9976 LVV-9976	LVV-T1436 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
DMS-NB-REQ-0012 LDM-554	LVV-9977 LVV-9977	LVV-T769 LDM-540		Not Executed
DMS-NB-REQ-0011 LDM-554	LVV-9978 LVV-9978	LVV-T768 LDM-540		Not Executed

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-NB-REQ-0023 LDM-554	LVV-9979	LVV-T780		Not Executed
	LVV-9979	LDM-540		
		LVV-T2339	LVV-P90	Not Executed
DMS-NB-REQ-0017 LDM-554	LVV-9980	LVV-T774		Not Executed
	LVV-9980	LDM-540		
		LVV-T1436	2019-12-09	
		LDM-540	LVV-P48	
		LVV-T2171	2021-10-01	Passed
		LDM-540	LVV-P79	
DMS-NB-REQ-0021 LDM-554	LVV-9981 LVV-9981	LVV-T778 LDM-540		Not Executed
DMS-NB-REQ-0022 LDM-554	LVV-9982 LVV-9982	LVV-T779 LDM-540		Not Executed
DMS-NB-REQ-0016 LDM-554	LVV-9983 LVV-9983	LVV-T773 LDM-540		Not Executed
DMS-NB-REQ-0020 LDM-554	LVV-9984 LVV-9984	LVV-T777 LDM-540		Not Executed
DMS-NB-REQ-0018 LDM-554	LVV-9985 LVV-9985	LVV-T775 LDM-540		Not Executed
DMS-NB-REQ-0019 LDM-554	LVV-9986 LVV-9986	LVV-T776 LDM-540		Not Executed
DMS-NB-REQ-0025 LDM-554	LVV-9987 LVV-9987	LVV-T782 LDM-540		Not Executed
DMS-NB-REQ-0024 LDM-554	LVV-9988 LVV-9988	LVV-T781 LDM-540		Not Executed
DMS-NB-REQ-0026 LDM-554	LVV-9989 LVV-9989	LVV-T783 LDM-540		Not Executed
DMS-NB-REQ-0032 LDM-554	LVV-9990 LVV-9990	LVV-T2171 LDM-540	2021-10-01 LVV-P79	Passed

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-NB-REQ-0033 LDM-554	LVV-9991 LVV-9991	LVV-T785 LDM-540		Not Executed
DMS-NB-REQ-0035 LDM-554	LVV-9992 LVV-9992	LVV-T787 LDM-540		Not Executed
DMS-NB-REQ-0034 LDM-554	LVV-9993 LVV-9993	LVV-T786 LDM-540		Not Executed
DMS-NB-REQ-0036 LDM-554	LVV-9994 LVV-9994	LVV-T788 LDM-540		Not Executed
DMS-NB-REQ-0030 LDM-554	LVV-9995 LVV-9995	LVV-T2171 LDM-540	2021-10-01 LVV-P79	Passed
DMS-NB-REQ-0029 LDM-554	LVV-9996 LVV-9996	LVV-T1436 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
DMS-NB-REQ-0031 LDM-554	LVV-9997 LVV-9997	LVV-T791 LDM-540		Not Executed
DMS-NB-REQ-0002 LDM-554	LVV-9998 LVV-9998	LVV-T793 LDM-540		Not Executed
		LVV-T1436 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
DMS-NB-REQ-0003 LDM-554	LVV-9999 LVV-9999	LVV-T794 LDM-540		Not Executed
DMS-NB-REQ-0001 LDM-554	LVV-10000 LVV-10000	LVV-T792 LDM-540		Not Executed
		LVV-T1436 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
		LVV-T2171 LDM-540	2021-10-01 LVV-P79	Passed
DMS-NB-REQ-0004 LDM-554	LVV-10001 LVV-10001	LVV-T795 LDM-540		Not Executed
DMS-API-REQ-0023 LDM-554	LVV-10002 LVV-10002	LVV-T798 LDM-540		Not Executed

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LVV-T1437 LDM-540	2019-12-09 LVV-P48	Pass w/Deviation
DMS-API-REQ-0022 LDM-554	LVV-10003 LVV-10003	LVV-T797 LDM-540		Not Executed
		LVV-T2677 LDM-540		Not Executed
		LVV-T2678 LDM-540		Not Executed
		LVV-T2712		Not Executed
DMS-API-REQ-0028 LDM-554	LVV-10004 LVV-10004	LVV-T803 LDM-540		Not Executed
		LVV-T2678 LDM-540		Not Executed
DMS-API-REQ-0024 LDM-554	LVV-10005 LVV-10005	LVV-T799 LDM-540		Not Executed
DMS-API-REQ-0026 LDM-554	LVV-10006 LVV-10006	LVV-T801 LDM-540		Not Executed
DMS-API-REQ-0027 LDM-554	LVV-10007 LVV-10007	LVV-T802 LDM-540		Not Executed
DMS-API-REQ-0030 LDM-554	LVV-10008 LVV-10008	LVV-T805 LDM-540		Not Executed
		LVV-T2678 LDM-540		Not Executed
DMS-API-REQ-0025 LDM-554	LVV-10009 LVV-10009	LVV-T800 LDM-540		Not Executed
DMS-API-REQ-0029 LDM-554	LVV-10010 LVV-10010	LVV-T804 LDM-540		Not Executed
DMS-API-REQ-0021 LDM-554	LVV-10011 LVV-10011	LVV-T796 LDM-540		Not Executed

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-API-REQ-0009 LDM-554	LVV-10012	LVV-T1437	2019-12-09	Pass w/Deviation
	LVV-10012	LDM-540	LVV-P48	
DMS-API-REQ-0008 LDM-554	LVV-10013	LVV-T808		Not Executed
	LVV-10013	LVV-T1334	2021-10-07	Passed
		LDM-540	LVV-P48	
		LVV-T1437	2019-12-09	Pass w/Deviation
		LDM-540	LVV-P48	
DMS-API-REQ-0007 LDM-554	LVV-10014	LVV-T807	2019-11-25	Passed
	LVV-10014	LDM-540	LVV-P48	
		LVV-T1334	2021-10-07	Passed
		LDM-540	LVV-P48	
		LVV-T1437	2019-12-09	Pass w/Deviation
		LDM-540	LVV-P48	
DMS-API-REQ-0006 LDM-554	LVV-10015	LVV-T1437	2019-12-09	Pass w/Deviation
	LVV-10015	LDM-540	LVV-P48	
DMS-API-REQ-0016 LDM-554	LVV-10016	LVV-T810		Not Executed
	LVV-10016	LDM-540		
		LVV-T2678		Not Executed
		LDM-540		
DMS-API-REQ-0018 LDM-554	LVV-10017	LVV-T812		Not Executed
	LVV-10017	LDM-540		
		LVV-T2678		Not Executed
		LDM-540		
DMS-API-REQ-0017 LDM-554	LVV-10018	LVV-T811		Not Executed
	LVV-10018	LDM-540		
		LVV-T2678		Not Executed
		LDM-540		
DMS-API-REQ-0039 LDM-554	LVV-10019	LVV-T1437	2019-12-09	Pass w/Deviation
	LVV-10019	LDM-540	LVV-P48	

Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-API-REQ-0038 LDM-554	LVV-10020 LVV-10020	LVV-T813 LDM-540		Not Executed
DMS-API-REQ-0040 LDM-554	LVV-10021 LVV-10021	LVV-T815 LDM-540		Not Executed
DMS-API-REQ-0034 LDM-554	LVV-10022 LVV-10022	LVV-T816 LDM-540		Not Executed
		LVV-T2678 LDM-540		Not Executed
DMS-API-REQ-0019 LDM-554	LVV-10023 LVV-10023	LVV-T817 LDM-540		Not Executed
DMS-API-REQ-0020 LDM-554	LVV-10024 LVV-10024	LVV-T818 LDM-540		Not Executed
DMS-API-REQ-0014 LDM-554	LVV-10025 LVV-10025	LVV-T823 LDM-540		Not Executed
DMS-API-REQ-0013 LDM-554	LVV-10026 LVV-10026	LVV-T822 LDM-540		Not Executed
DMS-API-REQ-0015 LDM-554	LVV-10027 LVV-10027	LVV-T824 LDM-540		Not Executed
DMS-API-REQ-0012 LDM-554	LVV-10028 LVV-10028	LVV-T821 LDM-540		Not Executed
DMS-API-REQ-0010 LDM-554	LVV-10029 LVV-10029	LVV-T819 LDM-540		Not Executed
DMS-API-REQ-0011 LDM-554	LVV-10030 LVV-10030	LVV-T820 LDM-540		Not Executed
DMS-API-REQ-0033 LDM-554	LVV-10031 LVV-10031	LVV-T827 LDM-540		Not Executed
DMS-API-REQ-0031 LDM-554	LVV-10032 LVV-10032	LVV-T825 LDM-540		Not Executed
DMS-API-REQ-0032 LDM-554	LVV-10033 LVV-10033	LVV-T826 LDM-540		Not Executed



Table 11: DM LDM-554 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-API-REQ-0003 LDM-554	LVV-10034	LVV-T829		Not Executed
	LVV-10034	LDM-540		
DMS-API-REQ-0004 LDM-554	LVV-10035	LVV-T1437	2019-12-09	Pass w/Deviation
	LVV-10035	LDM-540	LVV-P48	
DMS-API-REQ-0005 LDM-554	LVV-10036	LVV-T831	2019-12-09	Not Executed
DMS-API-REQ-0001 LDM-554	LVV-10037	LVV-T828		Not Executed
	LVV-10037	LDM-540		
DMS-API-REQ-0035 LDM-554	LVV-10038	LVV-T1437	2019-12-09	Pass w/Deviation
	LVV-10038	LDM-540	LVV-P48	
DMS-API-REQ-0037 LDM-554	LVV-10039	LVV-T832		Not Executed
DMS-API-REQ-0002 LDM-554	LVV-10040	LVV-T835		Not Executed
DMS-API-REQ-0036 LDM-554	LVV-10041	LVV-T833		Not Executed
DMS-API-REQ-0003 LDM-554	LVV-10041	LVV-T834		Not Executed

### 3.10 LSE-400 Requirements Coverage

Table 12: DM LSE-400 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
OCS-EFD-HS-0001 LSE-400	LVV-18271			

Table 12: DM LSE-400 Requirements.

<b>Requirement</b>	<b>Verification Element</b>	<b>Test Case</b>	<b>Last Run</b>	<b>Test Status</b>
OCS-EFD-HS-0002 LSE-400	LWV-18272 LWV-18272			
OCS-EFD-HS-0003 LSE-400	LWV-18273 LWV-18273			
OCS-EFD-HS-0004 LSE-400	LWV-18274 LWV-18274			
OCS-EFD-HS-0005 LSE-400	LWV-18275 LWV-18275			
OCS-EFD-HS-0006 LSE-400	LWV-18276 LWV-18276			
OCS-EFD-HS-0007 LSE-400	LWV-18277 LWV-18277			
OCS-EFD-HS-0008 LSE-400	LWV-18278 LWV-18278			
OCS-EFD-HS-0009 LSE-400	LWV-18279 LWV-18279			
OCS-EFD-HS-0010 LSE-400	LWV-18280 LWV-18280			
OCS-EFD-HS-0011 LSE-400	LWV-18281 LWV-18281			
OCS-EFD-HS-0012 LSE-400	LWV-18282 LWV-18282			
OCS-EFD-HS-0013 LSE-400	LWV-18283 LWV-18283			
OCS-EFD-HS-0014 LSE-400	LWV-18284 LWV-18284			
OCS-EFD-HS-0015 LSE-400	LWV-18285 LWV-18285			

### 3.11 LDM-556 Requirements Coverage

Table 13: DM LDM-556 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-MWBT-REQ-0014 LDM-556 (p. 1a)	LVV-19739 LVV-19739	LVV-T2459		Not Executed
DMS-MWBT-REQ-0053 LDM-556 (p. 1a)	LVV-19740 LVV-19740	LVV-T2482	2022-05-19 LVV-P77	Passed
DMS-MWBT-REQ-0067 LDM-556 (p. 1a)	LVV-19741 LVV-19741	LVV-T2499	2022-05-05 LVV-P77	Passed
DMS-MWST-REQ-0013 LDM-556	LVV-19742 LVV-19742	LVV-T1982	2022-02-15 LVV-P77	Passed
		LVV-T1983	2022-04-11 LVV-P77	Passed
DMS-MWBT-REQ-0005 LDM-556 (p. 1a)	LVV-19743 LVV-19743	LVV-T2443	2022-05-18 LVV-P77	Passed
DMS-MWBT-REQ-0023 LDM-556 (p. 1b)	LVV-19744 LVV-19744			
DMS-MWBT-REQ-0051 LDM-556 (p. 1a)	LVV-19745 LVV-19745			
DMS-MWBT-REQ-0073 LDM-556 (p. 1a)	LVV-19746 LVV-19746	LVV-T2492	2022-05-03 LVV-P77	Passed
DMS-MWBT-REQ-0074 LDM-556 (p. 1a)	LVV-19747 LVV-19747	LVV-T2493	2022-05-02 LVV-P77	Passed
DMS-MWBT-REQ-0020 LDM-556 (p. 1a)	LVV-19748 LVV-19748	LVV-T1982	2022-02-15 LVV-P77	Passed
DMS-MWBT-REQ-0025 LDM-556 (p. 1a)	LVV-19749 LVV-19749	LVV-T2476	2022-05-18 LVV-P77	Passed
DMS-MWST-REQ-0014 LDM-556	LVV-19750 LVV-19750	LVV-T1982	2022-02-15 LVV-P77	Passed
		LVV-T1983	2022-04-11 LVV-P77	Passed

Table 13: DM LDM-556 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-MWBT-REQ-0011 LDM-556 (p. 1a)	LVV-19751 LVV-19751			
DMS-MWBT-REQ-0057 LDM-556 (p. 1a)	LVV-19752 LVV-19752			
DMS-MWBT-REQ-0054 LDM-556 (p. 1a)	LVV-19753 LVV-19753	LVV-T2483	2022-05-09 LVV-P77	Passed
DMS-MWBT-REQ-0078 LDM-556 (p. 1a)	LVV-19754 LVV-19754	LVV-T2496		Not Executed
DMS-MWBT-REQ-0079 LDM-556 (p. 1b)	LVV-19755 LVV-19755			
DMS-MWBT-REQ-0034 LDM-556 (p. 1a)	LVV-19756 LVV-19756	LVV-T2480	2022-04-26 LVV-P77	Passed
DMS-MWBT-REQ-0060 LDM-556 (p. 1a)	LVV-19757 LVV-19757	LVV-T2449	2022-04-19 LVV-P77	Passed
DMS-MWBT-REQ-0050 LDM-556 (p. 1a)	LVV-19758 LVV-19758	LVV-T2490		Not Executed
DMS-MWBT-REQ-0075 LDM-556 (p. 1a)	LVV-19759 LVV-19759	LVV-T2453	2022-04-19 LVV-P77	Passed
DMS-MWBT-REQ-0082 LDM-556 (p. 1a)	LVV-19760 LVV-19760	LVV-T2469	2022-04-21 LVV-P77	Passed
DMS-MWBT-REQ-0035 LDM-556 (p. 1a)	LVV-19761 LVV-19761			
DMS-MWST-REQ-0001 LDM-556	LVV-19762 LVV-19762	LVV-T2466	2022-05-14 LVV-P77	Passed
DMS-MWST-REQ-0023 LDM-556	LVV-19763 LVV-19763	LVV-T2457	2022-05-14 LVV-P77	Passed
DMS-MWBT-REQ-0066 LDM-556 (p. 1a)	LVV-19764 LVV-19764	LVV-T2452	2022-04-18 LVV-P77	Passed
DMS-MWBT-REQ-0089 LDM-556 (p. 1a)	LVV-19765 LVV-19765			

Table 13: DM LDM-556 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-MWBT-REQ-0055 LDM-556 (p. 1a)	LVV-19766 LVV-19766	LVV-T2484		Not Executed
DMS-MWBT-REQ-0047 LDM-556 (p. 1a)	LVV-19767 LVV-19767	LVV-T1982	2022-02-15 LVV-P77	Passed
		LVV-T1983	2022-04-11 LVV-P77	Passed
DMS-MWBT-REQ-0028 LDM-556 (p. 1a)	LVV-19768 LVV-19768			
DMS-MWBT-REQ-0017 LDM-556 (p. 1a)	LVV-19769 LVV-19769	LVV-T2461	2022-05-17 LVV-P77	Passed
DMS-MWST-REQ-0003 LDM-556	LVV-19770 LVV-19770	LVV-T2455	2022-05-05 LVV-P77	Passed
DMS-MWBT-REQ-0096 LDM-556 (p. 2)	LVV-19771 LVV-19771			
DMS-MWBT-REQ-0058 LDM-556 (p. 1a)	LVV-19772 LVV-19772	LVV-T2485	2022-05-09 LVV-P77	Passed
DMS-MWBT-REQ-0031 LDM-556 (p. 1a)	LVV-19773 LVV-19773	LVV-T2478	2022-05-06 LVV-P77	Passed
DMS-MWST-REQ-0005 LDM-556	LVV-19774 LVV-19774	LVV-T1982	2022-02-15 LVV-P77	Passed
		LVV-T1983	2022-04-11 LVV-P77	Passed
DMS-MWBT-REQ-0041 LDM-556 (p. 1a)	LVV-19775 LVV-19775			
DMS-MWBT-REQ-0044 LDM-556 (p. 1a)	LVV-19776 LVV-19776			
DMS-MWST-REQ-0018 LDM-556	LVV-19777 LVV-19777	LVV-T2463	2022-05-14 LVV-P77	Passed
DMS-MWBT-REQ-0007 LDM-556 (p. 1a)	LVV-19778 LVV-19778	LVV-T2445		Not Executed

Table 13: DM LDM-556 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-MWBT-REQ-0059 LDM-556 (p. 1a)	LVV-19779 LVV-19779	LVV-T2491	2022-05-03 LVV-P77	Passed
DMS-MWBT-REQ-0009 LDM-556 (p. 1a)	LVV-19780 LVV-19780	LVV-T1985	2022-05-13 LVV-P77	Passed
DMS-MWBT-REQ-0062 LDM-556 (p. 1a)	LVV-19781 LVV-19781	LVV-T2498	2022-04-20 LVV-P77	Passed
DMS-MWBT-REQ-0048 LDM-556 (p. 1a)	LVV-19782 LVV-19782	LVV-T2481	2022-05-03 LVV-P77	Passed
DMS-MWST-REQ-0030 LDM-556	LVV-19783 LVV-19783			
DMS-MWBT-REQ-0081 LDM-556 (p. 1a)	LVV-19784 LVV-19784	LVV-T2468	2022-04-21 LVV-P77	Passed
DMS-MWBT-REQ-0046 LDM-556 (p. 1a)	LVV-19785 LVV-19785	LVV-T1982	2022-02-15 LVV-P77	Passed
		LVV-T1983	2022-04-11 LVV-P77	Passed
		LVV-T1985	2022-05-13 LVV-P77	Passed
DMS-MWST-REQ-0009 LDM-556	LVV-19786 LVV-19786			
DMS-MWBT-REQ-0064 LDM-556 (p. 1a)	LVV-19787 LVV-19787	LVV-T2451	2022-05-04 LVV-P77	Passed
DMS-MWBT-REQ-0013 LDM-556 (p. 1a)	LVV-19788 LVV-19788	LVV-T2447	2022-05-17 LVV-P77	Passed
DMS-MWST-REQ-0029 LDM-556	LVV-19789 LVV-19789			
DMS-MWBT-REQ-0090 LDM-556 (p. 1a)	LVV-19790 LVV-19790			
DMS-MWST-REQ-0022 LDM-556	LVV-19791 LVV-19791	LVV-T2458	2022-05-04 LVV-P77	Passed

Table 13: DM LDM-556 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-MWST-REQ-0002 LDM-556	LVV-19792	LVV-T2465	2022-05-14 LVV-P77	Passed
DMS-MWST-REQ-0031 LDM-556	LVV-19793			
DMS-MWST-REQ-0021 LDM-556	LVV-19794	LVV-T2460	2022-04-18 LVV-P77	Passed
DMS-MWST-REQ-0004 LDM-556	LVV-19795	LVV-T1982	2022-02-15 LVV-P77	Passed
		LVV-T1983	2022-04-11 LVV-P77	Passed
DMS-MWST-REQ-0011 LDM-556	LVV-19796	LVV-T1982	2022-02-15 LVV-P77	Passed
		LVV-T1983	2022-04-11 LVV-P77	Passed
DMS-MWBT-REQ-0004 LDM-556 (p. 1a)	LVV-19797	LVV-T2442	2022-05-18 LVV-P77	Passed
DMS-MWBT-REQ-0012 LDM-556 (p. 1a)	LVV-19798	LVV-T1983	2022-04-11 LVV-P77	Passed
DMS-MWBT-REQ-0068 LDM-556 (p. 1a)	LVV-19799	LVV-T2501	2022-05-16 LVV-P77	Passed
DMS-MWST-REQ-0027 LDM-556	LVV-19800			
DMS-MWBT-REQ-0026 LDM-556 (p. 1a)	LVV-19801			
DMS-MWBT-REQ-0030 LDM-556 (p. 1a)	LVV-19802	LVV-T2477	2022-05-06 LVV-P77	Passed
DMS-MWBT-REQ-0038 LDM-556 (p. 1a)	LVV-19803			
DMS-MWBT-REQ-0019 LDM-556 (p. 1a)	LVV-19804			

Table 13: DM LDM-556 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-MWST-REQ-0025 LDM-556	LVV-19805 LVV-19805	LVV-T2456	2022-05-04 LVV-P77	Passed
DMS-MWBT-REQ-0080 LDM-556 (p. 1a)	LVV-19806 LVV-19806	LVV-T2467	2022-04-21 LVV-P77	Passed
DMS-MWBT-REQ-0063 LDM-556 (p. 1a)	LVV-19807 LVV-19807	LVV-T2450	2022-05-05 LVV-P77	Passed
DMS-MWST-REQ-0026 LDM-556	LVV-19808 LVV-19808			
DMS-MWST-REQ-0012 LDM-556	LVV-19809 LVV-19809	LVV-T1982	2022-02-15 LVV-P77	Passed
		LVV-T1983	2022-04-11 LVV-P77	Passed
DMS-MWBT-REQ-0088 LDM-556 (p. 1a)	LVV-19810 LVV-19810	LVV-T2471	2022-04-21 LVV-P77	Passed
DMS-MWBT-REQ-0072 LDM-556 (p. 1a)	LVV-19811 LVV-19811			
DMS-MWBT-REQ-0052 LDM-556 (p. 1b)	LVV-19812 LVV-19812			
DMS-MWBT-REQ-0008 LDM-556 (p. 1a)	LVV-19813 LVV-19813	LVV-T1985	2022-05-13 LVV-P77	Passed
DMS-MWBT-REQ-0024 LDM-556 (p. 1a)	LVV-19814 LVV-19814	LVV-T2446	2022-04-26 LVV-P77	Passed
DMS-MWBT-REQ-0061 LDM-556 (p. 1a)	LVV-19815 LVV-19815	LVV-T2497	2022-04-20 LVV-P77	Passed
DMS-MWBT-REQ-0029 LDM-556 (p. 1a)	LVV-19816 LVV-19816			
DMS-MWBT-REQ-0002 LDM-556 (p. 1a)	LVV-19817 LVV-19817	LVV-T2440	2022-05-03 LVV-P77	Passed
DMS-MWBT-REQ-0094 LDM-556 (p. 1a)	LVV-19818 LVV-19818			



Table 13: DM LDM-556 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-MWBT-REQ-0006 LDM-556 (p. 1a)	LVV-19819 LVV-19819	LVV-T2444	2022-05-04 LVV-P77	Passed
DMS-MWBT-REQ-0040 LDM-556 (p. 1a)	LVV-19820 LVV-19820			
DMS-MWBT-REQ-0071 LDM-556 (p. 1a)	LVV-19821 LVV-19821	LVV-T2503	2022-05-02 LVV-P77	Passed
DMS-MWBT-REQ-0036 LDM-556 (p. 1a)	LVV-19822 LVV-19822	LVV-T2486	2022-05-05 LVV-P77	Passed
DMS-MWBT-REQ-0037 LDM-556 (p. 1a)	LVV-19823 LVV-19823	LVV-T2487	2022-05-05 LVV-P77	Passed
DMS-MWBT-REQ-0039 LDM-556 (p. 1a)	LVV-19824 LVV-19824	LVV-T2488	2022-05-03 LVV-P77	Passed
DMS-MWBT-REQ-0095 LDM-556 (p. 1a)	LVV-19825 LVV-19825			
DMS-MWBT-REQ-0033 LDM-556 (p. 1a)	LVV-19826 LVV-19826	LVV-T2479	2022-04-26 LVV-P77	Passed
DMS-MWBT-REQ-0022 LDM-556 (p. 1b)	LVV-19827 LVV-19827	LVV-T1985	2022-05-13 LVV-P77	Passed
DMS-MWBT-REQ-0076 LDM-556 (p. 1a)	LVV-19828 LVV-19828	LVV-T2494	2022-05-02 LVV-P77	Passed
DMS-MWBT-REQ-0045 LDM-556 (p. 1a)	LVV-19829 LVV-19829			
DMS-MWBT-REQ-0077 LDM-556 (p. 1a)	LVV-19830 LVV-19830	LVV-T2495	2022-04-26 LVV-P77	Passed
DMS-MWBT-REQ-0065 LDM-556 (p. 1a)	LVV-19831 LVV-19831			
DMS-MWBT-REQ-0043 LDM-556 (p. 1a)	LVV-19832 LVV-19832			
DMS-MWBT-REQ-0092 LDM-556 (p. 3)	LVV-19833 LVV-19833	LVV-T2472	2022-05-10 LVV-P77	Passed

Table 13: DM LDM-556 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-MWBT-REQ-0042 LDM-556 (p. 1a)	LVV-19834 LVV-19834			
DMS-MWBT-REQ-0021 LDM-556 (p. 2)	LVV-19835 LVV-19835	LVV-T2464	2022-04-20 LVV-P77	Passed
DMS-MWBT-REQ-0032 LDM-556 (p. 1a)	LVV-19836 LVV-19836			
DMS-MWBT-REQ-0027 LDM-556 (p. 1a)	LVV-19837 LVV-19837			
DMS-MWBT-REQ-0091 LDM-556 (p. 1b)	LVV-19838 LVV-19838			
DMS-MWBT-REQ-0093 LDM-556 (p. 2)	LVV-19839 LVV-19839			
DMS-MWBT-REQ-0010 LDM-556 (p. 1a)	LVV-19840 LVV-19840			
DMS-MWBT-REQ-0069 LDM-556 (p. 1a)	LVV-19841 LVV-19841	LVV-T2500		Not Executed
DMS-MWBT-REQ-0015 LDM-556 (p. 1a)	LVV-19842 LVV-19842	LVV-T2448	LVV-P96	Not Executed
DMS-MWST-REQ-0028 LDM-556	LVV-19843 LVV-19843			
DMS-MWST-REQ-0019 LDM-556	LVV-19844 LVV-19844			
DMS-MWST-REQ-0020 LDM-556	LVV-19845 LVV-19845	LVV-T2462	2022-04-19 LVV-P77	Passed
DMS-MWST-REQ-0024 LDM-556	LVV-19846 LVV-19846			
DMS-MWBT-REQ-0084 LDM-556 (p. 1a)	LVV-19847 LVV-19847	LVV-T2474	2022-05-05 LVV-P77	Passed
DMS-MWBT-REQ-0083 LDM-556 (p. 1a)	LVV-19848 LVV-19848	LVV-T2473	2022-05-05 LVV-P77	Passed

Table 13: DM LDM-556 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
DMS-MWBT-REQ-0003 LDM-556 (p. 2)	LVV-19849	LVV-T2441	2022-05-09 LVV-P77	Passed
DMS-MWST-REQ-0006 LDM-556	LVV-19850	LVV-T1982	2022-02-15 LVV-P77	Passed
		LVV-T1983	2022-04-11 LVV-P77	Passed
DMS-MWBT-REQ-0085 LDM-556 (p. 1a)	LVV-19851	LVV-T2475	2022-05-05 LVV-P77	Passed
DMS-MWBT-REQ-0001 LDM-556 (p. 1a)	LVV-19852	LVV-T2439	2022-05-03 LVV-P77	Passed
DMS-MWST-REQ-0015 LDM-556	LVV-19853			
DMS-MWBT-REQ-0016 LDM-556 (p. 1a)	LVV-19854			
DMS-MWBT-REQ-0086 LDM-556 (p. 1a)	LVV-19855			
DMS-MWBT-REQ-0070 LDM-556 (p. 1a)	LVV-19856	LVV-T2502	2022-05-05 LVV-P77	Passed
DMS-MWST-REQ-0016 LDM-556	LVV-19857	LVV-T2454	2022-05-10 LVV-P77	Passed
DMS-MWBT-REQ-0049 LDM-556 (p. 1a)	LVV-19858	LVV-T2489	LVV-P96	Not Executed
DMS-MWST-REQ-0007 LDM-556	LVV-19859	LVV-T1982	2022-02-15 LVV-P77	Passed
		LVV-T1983	2022-04-11 LVV-P77	Passed
DMS-MWBT-REQ-0087 LDM-556 (p. 1a)	LVV-19860	LVV-T2470	2022-04-21 LVV-P77	Passed
DMS-MWST-REQ-0010 LDM-556	LVV-19861	LVV-T1982	2022-02-15 LVV-P77	Passed

Table 13: DM LDM-556 Requirements.

Requirement	Verification Element	Test Case	Last Run	Test Status
		LWV-T1983	2022-04-11	Passed
			LWV-P77	
DMS-MWBT-REQ-0056 LDM-556 (p. 1a)	LWV-19862 LWV-19862			
DMS-MWST-REQ-0008 LDM-556	LWV-19863 LWV-19863	LWV-T1982	2022-02-15 LWV-P77	Passed
		LWV-T1983	2022-04-11 LWV-P77	Passed
DMS-MWST-REQ-0017 LDM-556	LWV-19864 LWV-19864	LWV-T1982	2022-02-15 LWV-P77	Passed
		LWV-T1983	2022-04-11 LWV-P77	Passed
DMS-MWBT-REQ-0018 LDM-556 (p. 1a)	LWV-19865 LWV-19865			

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## B Acronyms

Acronym	Description
API	Application Programming Interface
CA-DM-CON-ICD	Requirements for Interface between the Camera and Data Management (LSE-69)
CA-DM-DAQ-ICD	Requirements for Camera Data Acquisition Interface (LSE-68)
CA-DM-SUP-ICD	Requirements for Support-Data Exchanges between Data Management and Camera (LSE-130)
DAQ	Data Acquisition System
DM	Data Management
DM-TS-AUX-ICD	Requirements for Auxiliary Instrumentation Interface between Data Management and Telescope (LSE-140)

DM-TS-CON-ICD	Requirements for Control System Interfaces between the Telescope & Data Management (LSE-75)
DMS	Data Management Subsystem
DMS-API-REQ	LSP API Aspect requirements (LDM-554)
DMS-LSP-REQ	Top level LSP requirements (LDM-554)
DMS-NB-REQ	LSP Notebook Aspect requirements (LDM-554)
DMS-PRTL-REQ	LSP Portal Aspect requirements (LDM-554)
DMS-REQ	Data Management top level requirements (LSE-61)
DMTR	DM Test (Plan and) Report
EFD	Engineering and Facility Database
EP-DM-CON-ICD	Requirements for Interface between Data Management and EPO (LSE-131)
ICD	Interface Control Document
LDM	LSST Data Management (Document Handle)
LSE	LSST Systems Engineering (Document Handle)
LSP	LSST Science Platform (now Rubin Science Platform)
LSST	Legacy Survey of Space and Time (formerly Large Synoptic Survey Telescope)
LVV	LSST Verification and Validation (Jira project)
OCS	Observatory Control System
OCS-DM-COM-ICD	Requirements for OCS - Data Management Software Communication Interface (LSE-72)
OCS-EFD-HS	Requirements for Header Service Interface between OCS and EFD (LSE-400)
RFC	Request For Comment
TS	Test Specification
VCD	Verification Control Document